PRINCIPLES OF EE1 LAB

LAB 4

Thevenin Theorem

full name:	•
Student's ID:	
Class:	•
)ate:	

THE WATTON OF TH

INTERNATIONAL UNIVERSITY SCHOOL OF ELECTRICAL ENGINEERING (EE)

I. OBJECTIVES

- To find experimentally the values for a Thévenin's equivalent of a circuit.
- To check the experimental values versus calculated values.
- To find the conditions for maximum power delivered to a load.
- To build a Thévenin equivalent of the original circuit and check to see if it really is equivalent.

II. EQUIPMENT

- Variable Voltage Supply
- Breadboard
- Digital Multimeter (DMM)
- Fixed Resistors: 1.0 k, 1.2 k, 1.5 k, 2-1.8 k, 2.2 k, 5.6 k
- Variable Resistance: 1 k

III. INTRODUCTION

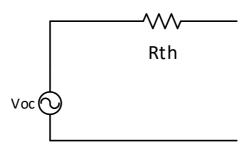
3.1. Thévenin's theorem

- 1. Given a linear circuit.
 - (a) Rearrange it into Network A and Network B.
 - (b) If dependent sources exist, the control variable must be in same Network.
- 2. Calculate the open circuit voltage, Voc, across the Network A terminals with:
 - (a) Network B disconnected.
 - (b) IL = 0 (no current drawn from Network A by Network B).
- 3. "Kill" Network A (make it a "dead" Network) with:
 - (a) A Short Circuit replaces all independent Voltage sources.
 - (b) An Open Circuit replaces all independent Current sources.
 - (c) If dependent sources exist they must be left in the circuit.
 - Then calculate R_{th} for the "dead" Network A (looking back into the terminals).

If the dead network contains dependent sources, then an external source must be connected between the two terminals to determine the impedance. Either a current source can be connected and the voltage across the source determined or a voltage source may be used and the current flowing from the source calculated. The ratio of the voltage to the current will give the Thévenin Equivalent impedance.

- 4. Replace Network A with:
 - (a) Independent Voltage source equal to Voc.
 - (b) Connected in series with RTh.
- 5. Draw Thévenin equivalent circuit.





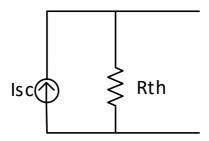
 $I_{sc} = V_{oc}/R_{Th}$ is the current that flows when a short circuit is placed across the terminals.

2. Norton's theorem

- 1. Given a linear circuit.
 - (a) Rearrange it into Network A and Network B.
 - (b) If dependent sources exist, the control variable must be in same Network.
- 2. Define (or calc.) the short circuit current, Isc, through the Network A terminals with Network B disconnected and replaced with a Short Circuit. This will give, VL = 0, since no voltage can appear across a short circuit.
- 3. "Kill" Network A (make it a "dead" Network) with:
 - (a) A Short Circuit replacing all independent Voltage sources.
 - (b) An Open Circuit replacing all independent Current sources.
 - (c) If dependent sources exist they must be left in the circuit.

Then calculate Rth for the "dead" Network A (looking back into the terminals). If the dead network contains dependent sources, then an external source must be connected between the two terminals to determine the impedance. Either a current source can be connected and the voltage across the source determined or a voltage source may be used and the current flowing from the source calculated. The ratio of the voltage to the current will give the Thévenin Equivalent impedance.

- 4. Replace Network A with:
 - (a) an Independent Current source equal to Isc.
 - (b) Connected in parallel with Rth.
- 5. Draw Norton equivalent circuit.



 V_{oc} = $R_{Th}I_{sc}$ is the voltage between the terminals with no external load connected.

IV. PROCEDURE:

Problem 1:

1. After measuring the actual resistor values, construct the source circuit shown in Figure 1 using a variable voltage supply, breadboard, resistors, and jumper wires. Set the supply voltage as close to 10 volts as you can and record the actual value measured by the DMM.

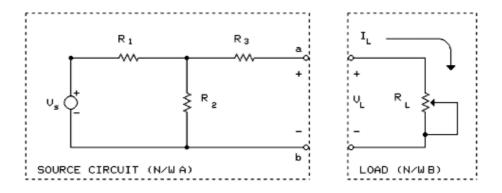


Figure 1: Original circuit and variable load.

$$R_1 = 1.5 \text{ k}\Omega, R_2 = 1.8 \text{k}\Omega, R_3 = 1.2 \text{ k}\Omega$$

- 2. Measure the open circuit voltage (V_{Th}=V_{oc}) of network A between terminals a and b.
- 3. Measure the short circuit current (I_{Sc}) of network A from terminal a to terminal b.
- 4. Calculate the Thévenin Equivalent resistance using these two measured values.

Use $R_{th} = V_{Th} / I_{Sc}$ for this calculations .

In the circuit shown below the portion to the left of terminals a and b is the Thévenin Equivalent of the original circuit and the portion to the right of the terminals is the load.

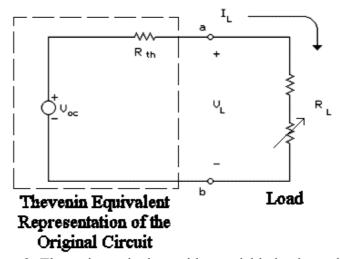


Figure 2: Thevenin equivalent with a variable load attached.

THE RNATIONAL UNIVERSITY OF THE PROPERTY OF TH

INTERNATIONAL UNIVERSITY SCHOOL OF ELECTRICAL ENGINEERING (EE)

Analyze the circuit in figure 2 to find, V_L , as a function of R_L . Use voltage division to find V_L . Then solve for the value of R_L as a function of V_L .

Use the values found above to calculate the values of load resistance, R_L , that will give each of the following load voltages: $V_L = 0.7*V_{Th}$, $V_L = 0.5*V_{Th}$, and $V_L = 0.3*V_{Th}$.

Note: For $V_L = 0.7*V_{Th} = V_L$ this gives $V_L = 0.7*V_{Th} = V_{Th} * R_L/(R_L + R_{th})$ or $0.7 = R_L/(R_L + R_{th})$. Solve for R_L and repeat for 0.5 and 0.3.

5. Then using a variable resistance (1 KOhm potentiometer) in series with a fixed resistor of appropriate value for each of these values of R_L .

Use the closest standard resistor less than R_L for the fixed resistor. This should give sufficient adjustment range to get the desired output voltage when the series combination is connected across the terminals a-b of the original circuit. Adjust the resistance to obtain the corresponding value of V_L . Then measure the actual value of V_L and R_L (the series combination of the fixed resistor and the adjustable resistor) for each of the three specified load voltages.

V _∟ Approximate		VL	RL	$I_L = V_L / R_L$
Desired value	Calculated value	Measured	Measured	Calculated
0.7 *V _{Th}				
0.5 *V _{Th}				
0.3 *V _{Th}				

Calculate I_L from the measured values of V_L and R_L and put it into a tabulation similar to the one shown above.

6. Disconnect V_s from the circuit and replace it with a jumper wire (short circuit). This is equivalent to $V_s = 0$. Then measure the Thévenin equivalent resistance (R_{Th}) looking into this source free version of the original circuit between terminals a and b. This should be very close to the value calculated by dividing the open circuit voltage by the short circuit current.

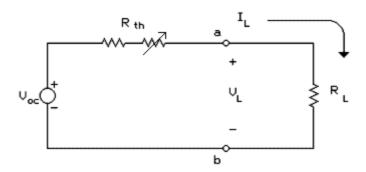


Figure 3: Thévenin equivalent with fixed load attached.



7. Construct a Thévenin equivalent circuit using the values of V_{Th} and R_{Th} obtained in steps 2 and 6, respectively. Use an appropriate fixed resistor and the potentiometer and set the series combination to the measured value of R_{th} . Use a variable voltage supply for V_{Th} , and set it to the measured value of V_{Th} .

Now use the fixed resistors listed below as R_L . For each value of R_L , measure R_L , and V_L . Then calculate I_L using the measured value of the load resistor and voltage. Record the data in a form similar to the table below.

$R_L(k\Omega)$	$R_L(k\Omega)$	V _L (V)	I _L (mA)	
Nominal	Measured	Measured	measured	Calculated
0				
1.0				
1.8				
2.2				
5.6				
open				

CALCULATIONS, COMPARISONS & GRAPHS:

- 1. From the data in step 5 under procedure use a spreadsheet to plot a graph of V_L (y axis) versus I_L (x axis) using an open circle as the symbol with no line connecting the points. Also plot on the same graph the points for $(V_{Th} = V_{OC}, I_L = 0)$ and $(V_L = 0, I_L = I_{SC})$ connected with a line. Do the original data points lie on this line?
- 2. Now on the same graph plot the data from step 7 under the PROCEDURE. Use an open square box symbol to identify these points, but do not connect the points with a line. Identify each set of data with a legend at the bottom of the graph. Is the Thévenin circuit in Figure 3 equivalent to the original circuit in Figure 1? If so, why can you make that statement?
- 3. Next, using the data from step 5 under the PROCEDURE, calculate the power (P) delivered to R_L . On a separate graph plot P (on the y axis) versus R_L (on the x axis) using an open circle for each point with no connecting lines. Add the points from step 7 using an open square box for each point. Do not connect either set of points with a line. Plot the theoretical power curve from R=0 to R=5 k Ω on the same graph using a step size of 0.1 k Ω so that the line connecting these points with no symbols will form a smooth curve. At what value of resistance does the power reach a maximum? Does this make sense? How close are the two sets of experimental points to the theoretical curve?
- 4. Calculate V_{Th} , I_{sc} and R_{th} of circuit in Figure 1 using the measured source and resistor values and compare them to measured values in steps 2, 3, and 6 of the PROCEDURE.

THE RNATIONAL CHINEBRAL CHINEBRA CHINEBRAL CHINEBRA CHINEBRA

INTERNATIONAL UNIVERSITY SCHOOL OF ELECTRICAL ENGINEERING (EE)

Problem 2:

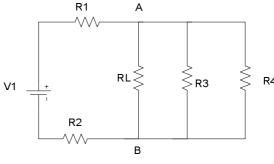


Figure 4 V1 = 8V

$$R1 = 1.5K$$
, $R2 = 1K$, $R3 = R4 = 2.7K$

- a. Measure the open circuit voltage $V_{\text{oc}} = V_{\text{th}}$ between the terminals A and B
- b. Measure the R_{th}
- c. Construct the Thevenin circuit

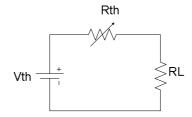


Figure 5

- d. Measure the load voltage in figure 2a and figure 2b in 2 cases: RL = 330 Ohm, RL = 1K. Calculate the percentage of error.
- e. Verify your result by calculation