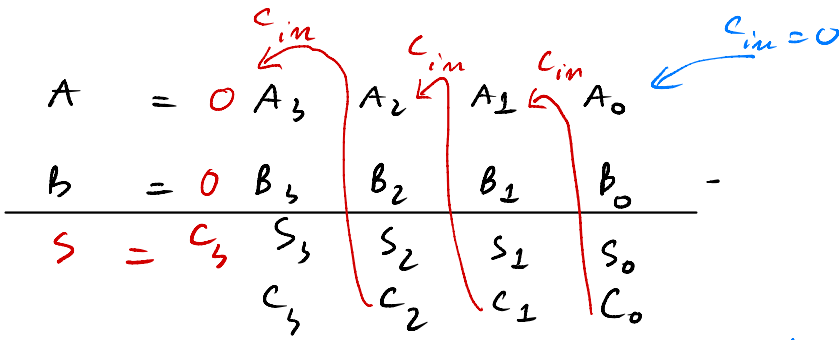
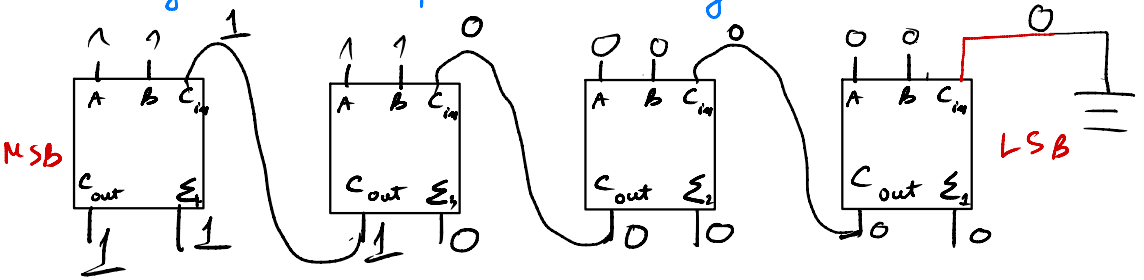


Parallel Binary Adders:

Design 4-bit parallel adder using Full Adders



C_{n-1}	A_n	B_n	Σ_n	C_n
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

output carry

$$A_4 A_3 A_2 A_1 = 1100, \quad b_4 b_3 b_2 b_1 = 1100$$

11000