

Lecture 1&2 Quiz Solution

March 2020

Lecture 1

Question 1

1. Find the period T of the sinusoidal signal $v(t) = 10\sin(100\pi t)$ volts
2. What is the rms value of the signal given in question 1.
3. How to reduce the quantization error of a Analog to Digital Converter

Answer:

1. $T = \frac{2\pi}{\omega} = \frac{2\pi}{100\pi} = 0.02 \text{ s}$
2. $V_{\text{rms}} = \frac{V_{\text{pk}}}{\sqrt{2}} = \frac{10}{\sqrt{2}} = 5\sqrt{2} \approx 7.071 \text{ volts}$
3. Increase **quantization levels**.

Question 2

1. What is the normal voltage gain if the voltage gain in dB is 20.
2. Why does an amplifier use dc bias.
3. What is the instantaneous value at the output of an amplifier at $t = 0.1[\text{s}]$ if the DC component is $4[\text{V}]$, and the ac component is $0.4\cos(200\pi t)[\text{V}]$.

Answer:

1. Voltage gain $A_v (\text{dB}) \equiv 20 \log |A_v|$
 $20 = 20 \log |A_v|$
 $\Rightarrow |A_v| = 10$
2. Use dc bias to operate the circuit near the middle of the transfer curve \rightarrow **quiescent point**.
3. $v_o(t) = V_o + v_o(t)$
 $V_o = 4 \text{ V}$
 $V_o(t) = 0.4\cos(200\pi t)$
 $\Rightarrow v_o(t) = 4 + 0.4\cos(200\pi t)$
 $\Rightarrow v_o(0.1) = 4 + 0.4\cos(200\pi \cdot 0.1)$
 $= 4.4 \text{ V}$

Question 3

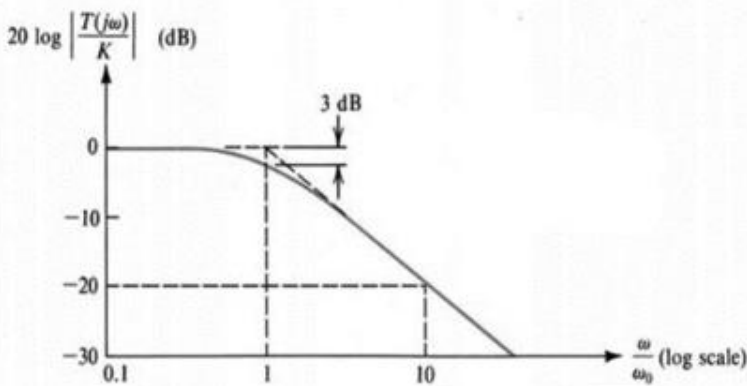
1. For Low Pass STC network, a resistor of $1\text{k}\Omega$, a capacitor of $0.1\mu\text{F}$. Find 3dB frequency (or cutoff frequency). Sketch the magnitude of the frequency response.
2. For High Pass STC network, a resistor of $2\text{k}\Omega$, a capacitor of $0.01\mu\text{F}$. Find 3dB frequency (or cutoff frequency). Sketch the magnitude of the frequency response.

Answer:

1.

$$\omega_o = 1/\tau = 1/RC = 1/10^3 \times 0.1 \times 10^{-6}$$

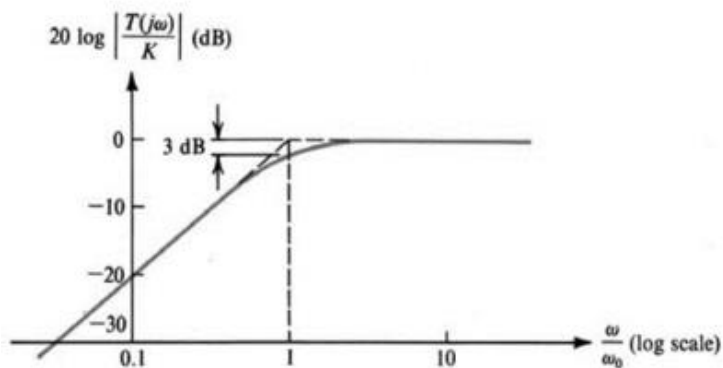
$$= 10000 \text{ rad/s}$$



2.

$$\omega_o = 1/\tau = 1/RC = 1/2 \times 10^3 \times 0.01 \times 10^{-6}$$

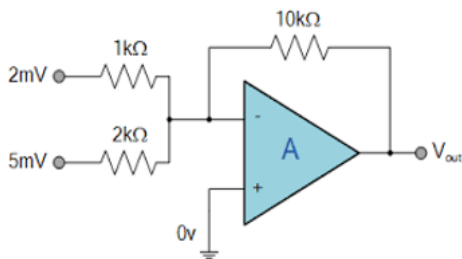
$$= 50000 \text{ rad/s}$$



Lecture 2

Question 1

a)



What is the name of this circuit? Compute the value of output voltage

- b) Design an inverting amplifier of an ideal op-amp with input impedance of $2k\Omega$ and voltage gain of -4 . Plot the circuit with component values.

Answer

- a) The name of the circuit is the weighted summer using the inverting configuration

We have:

$$V_o = -\left(\frac{R_f}{R_1} v_1 + \frac{R_f}{R_2} v_2\right)$$

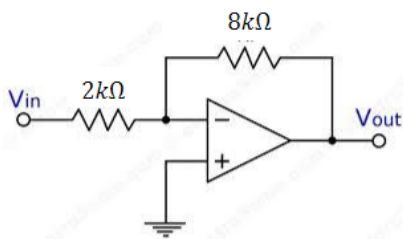
$$= -\left(\frac{10}{1} * 2 * 10^{-3} + \frac{10}{2} * 5 * 10^{-3}\right) = -45 * 10^{-3} = -45 \text{ mV}$$

b)

$$-\frac{R_f}{R_i} = -4$$

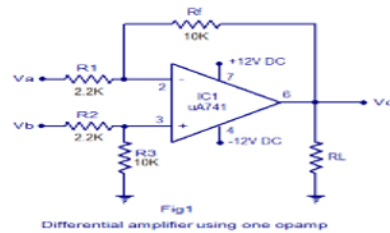
$$\Rightarrow R_f = 4 * R_i = 8k\Omega$$

The circuit with component values:

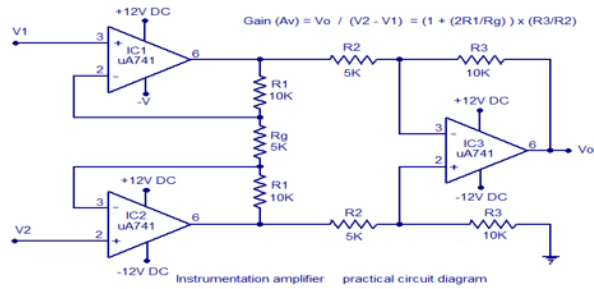


Question2

a) If $v_a = -0.8\text{V}$ and $v_b = 1.8\text{V}$ what is the value v_o . What is the name of this circuit? What is the disadvantage of this circuit?



b) If $v_2 = 0.2\text{V}$ and $v_1 = -0.4\text{V}$ what is the value v_o . What is the name of this circuit? What is the input impedance of this circuit?



Answer

a)

Since $R_1/R_2 = R_3/R_4$

$$\begin{aligned} \Rightarrow V_o &= (v_b - v_a) \cdot R_1/R_2 \\ &= (1.8 - (-0.8)) \cdot 10/2.2 \\ &= 11.82\text{ V} \end{aligned}$$

This is a differential amplifier using one op-amp with loaded output.

Disadvantage: Large R_1 can be used to increase R_i but R_f will become impractically large to maintain required gain.

b)

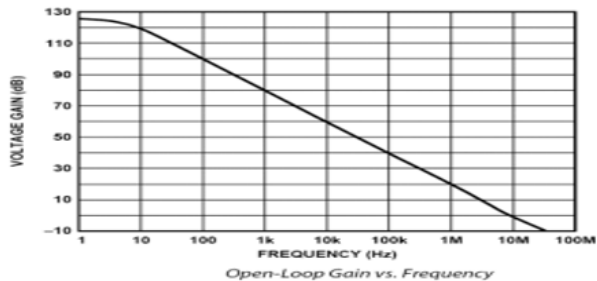
$$\begin{aligned} V_o &= (V_2 - V_1) \left(2 \cdot \frac{R_1}{R_g} + 1 \right) \frac{R_3}{R_2} \\ &= (0.2 - (-0.4)) \left(2 \cdot \frac{10}{5} + 1 \right) \frac{10}{5} = 6\text{ V} \end{aligned}$$

This circuit is an instrumentation amplifier.

The input impedance of this circuit is infinite.

Question 3

a) Referring to the Figure, the voltage gain of 0dB corresponds approximately to 10 MHz. What is the name of that frequency (10 MHz)? If $\omega_b = 10\text{ Hz}$ what is the DC voltage gain ($\omega = 0$) in dB and in normal values? Find ω_{3dB} if the voltage gain is 50 dB



b) If the SR (Slew Rate) of an op-amp is $13\text{ V}/\mu\text{sec}$ and maximum output voltage of the sinusoidal signal (no distortion) is 4V. Find the maximum frequency that can accept.

Answer

- a. The name of the 10 MHz frequency is unity-gain bandwidth (ω_T)

$$\omega_T = A_{Vo} \omega_b \rightarrow A_{Vo} = \frac{\omega_T}{\omega_b} = \frac{10 \times 10^6}{10} = 10^6 = 120 \text{ dB}.$$

Note: $A_{Vo} \sim 120 \text{ dB}$ is accepted according to the given figure.

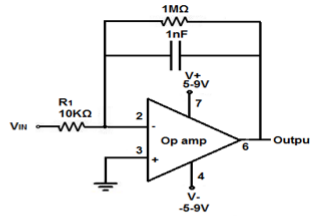
$$\omega_{3dB} = |A_{Vo}| \cdot \omega_b / |G| = \frac{10^6}{316} \cdot 10 = 31.6k \text{ (rad/s)}$$

b)

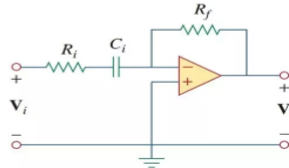
$$f_M = \frac{SR}{2\pi \cdot V_{0 \max}} = \frac{13}{10^{-6} \cdot 2\pi \cdot 4} = 517253.565 \text{ Hz}$$

Question 4

a) Referring to the Figure, what is the name of this circuit? Compute 3dB frequency (cutoff frequency). Find the frequency at which the gain is unit. Find the gain at DC ($\omega = 0$).



b) Referring to the Figure, what is the name of this circuit? If $R_i = 10k\Omega$, $C_i = 0.01\mu F$, and $R_f = 20k\Omega$, Compute 3dB frequency (cutoff frequency). Find the frequency at which the gain is unit. Find the gain as $\omega \rightarrow \infty$.



Answer:

a)

- The name of the circuit is the Miller Integrator with parallel feedback resistance.
- Cutoff Frequency : $\omega_{3dB} = \frac{1}{R_f \cdot C} = \frac{1}{1 \times 10^6 \cdot 1 \times 10^{-9}} = 1000 \text{ rad/s}$
- The frequency at which the gain is unit:

$$\omega_{\text{unity}} = \frac{1}{R_1 \cdot C} = \frac{1}{10 \times 10^3 \cdot 1 \times 10^{-9}} = 100 \times 10^3 \text{ rad/s}$$

- Gain at DC ($\omega=0$): $\frac{-R_f}{R_1} = \frac{1 \times 10^6}{10 \times 10^3} = 100$

b)

- The name of the circuit is the Differentiator with series resistance.
- Cutoff Frequency:

$$\omega_{3dB} = \frac{1}{R_i \cdot C_i} = \frac{1}{10 \times 10^3 \cdot 0.01 \times 10^{-6}} = 10000 \text{ rad/s}$$

- The frequency at which the gain is unit:

$$\omega_{\text{unity}} = \frac{1}{R_f \cdot C_i} = \frac{1}{20 \times 10^3 \cdot 0.01 \times 10^{-6}} = 5000 \text{ rad/s}$$

- The gain as $\omega \rightarrow \infty$: $\frac{-R_f}{R_i} = \frac{20 \times 10^3}{10 \times 10^3} = 2$