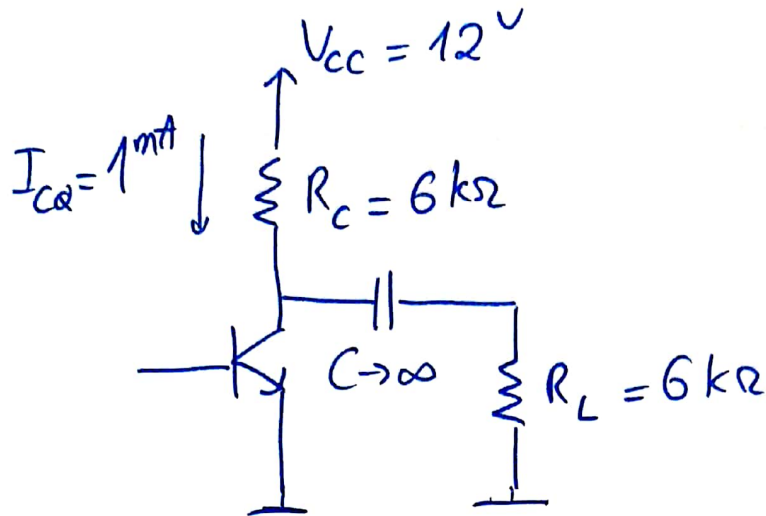


Analog Electronics

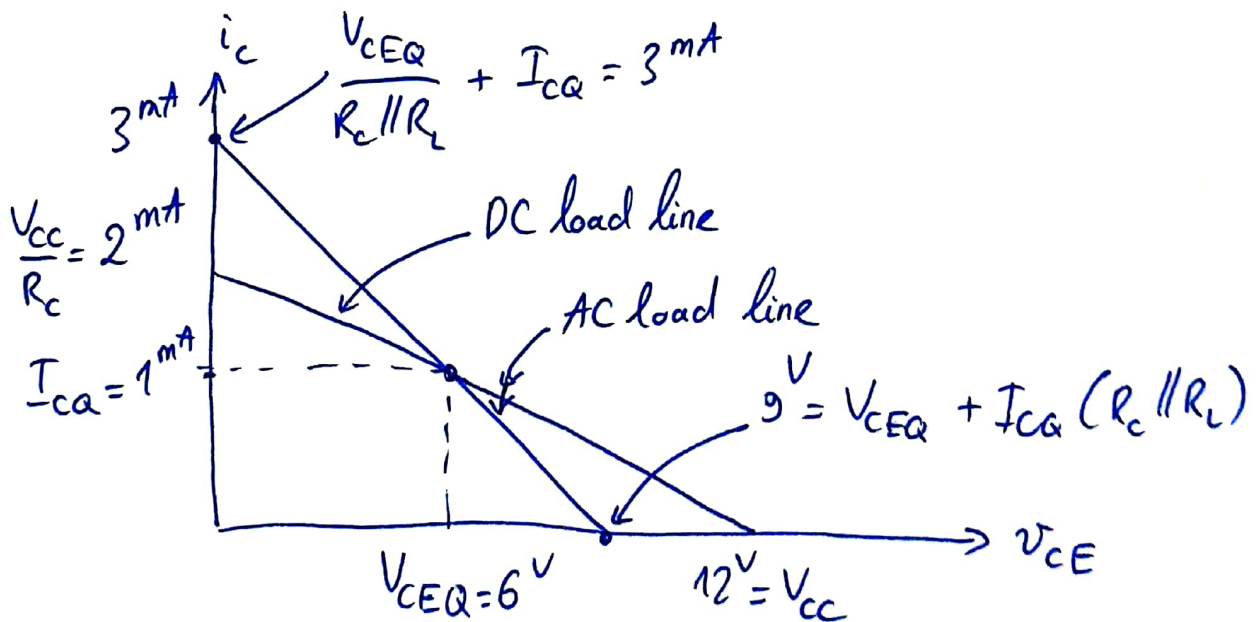
QUIZ #6

①

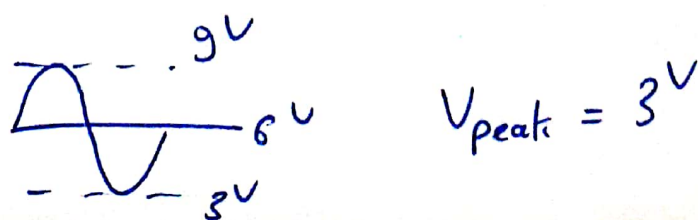


a) • DC load line: $i_c = \frac{V_{CC} - v_{CE}}{R_C}$

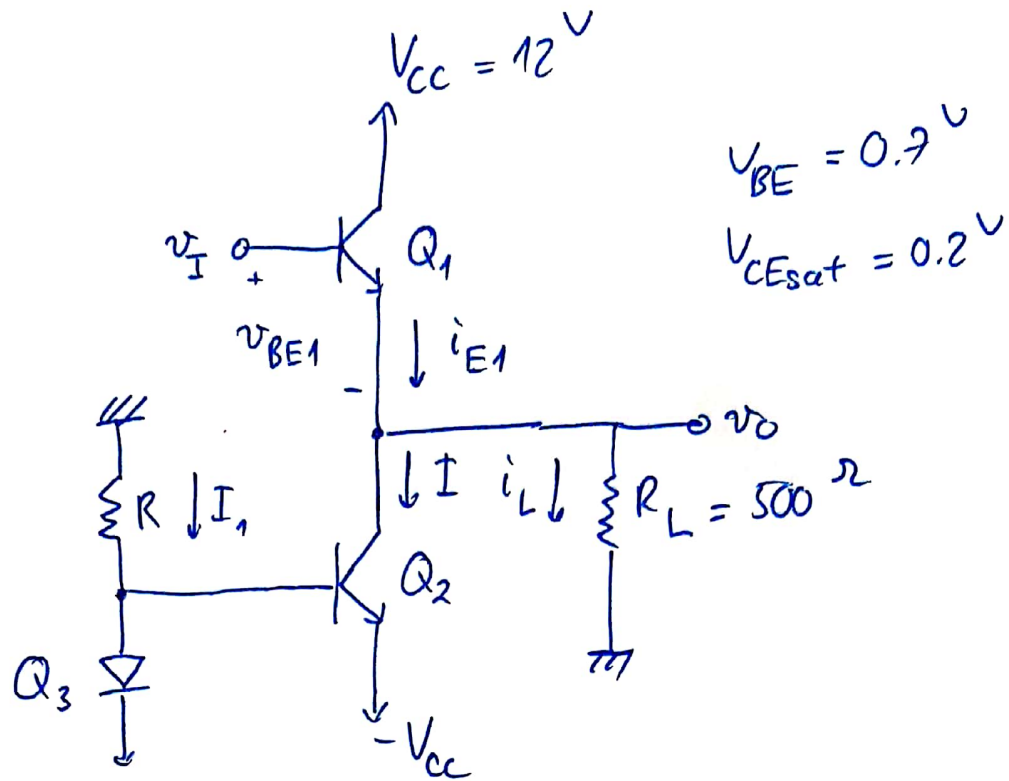
• AC load line: $i_c = \frac{V_{CEQ}}{R_C \parallel R_L} + I_{CQ} - \frac{v_{CE}}{R_C \parallel R_L}$



b) Swing of v_o



2



$$a) \quad I = \frac{|-V_{CC} + V_{CE2sat}|}{R_L} = \frac{|-12 + 0.2|}{500} = 23.6 \text{ mA}$$

$$R = \frac{0 - (-V_{CC} + V_{BE2})}{I_1} = \frac{V_{CC} - V_{BE2}}{I} = \frac{12 - 0.7}{23.6 \text{ mA}} = 478.8 \Omega$$

$$b) \quad v_{op} = V_{CC} - V_{CE1sat} = 12 - 0.2 = 11.8 \text{ V}$$

$$P_L = \frac{v_{op}^2}{2R_L} = \frac{11.8^2}{2 \times 500} = 139 \text{ mW}$$

$$c) \quad v_o \in [-V_{CC} + V_{CE2sat}, V_{CC} - V_{CE1sat}]$$

$$\Rightarrow v_o \in [-11.8 \text{ V}, 11.8 \text{ V}]$$

$$v_i = v_o + V_{BE1} \Rightarrow v_i \in [-11.1 \text{ V}, 12.5 \text{ V}]$$

$$\bullet \text{ At } v_{i \min} = -11.1 \text{ V}, i_{E1} = 0 \text{ A}$$

$$\bullet \text{ At } v_{i \max} = 12.5 \text{ V}, i_{E1} = 2I = 47.2 \text{ mA}$$

d) No. Because there are no capacitors in the circuit.