

Reasoning About a Machine with Local Capabilities

Provably Safe Stack and Return Pointer Management

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ESOP, April 17, 2018

What Does This Program Do?

```
let x = ref 0 in  
   $\lambda f.$  (x := 0;  
        f();  
        x := 1;  
        f();  
        assert (x == 1))
```

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Reasoning About a Machine with Local Capabilities

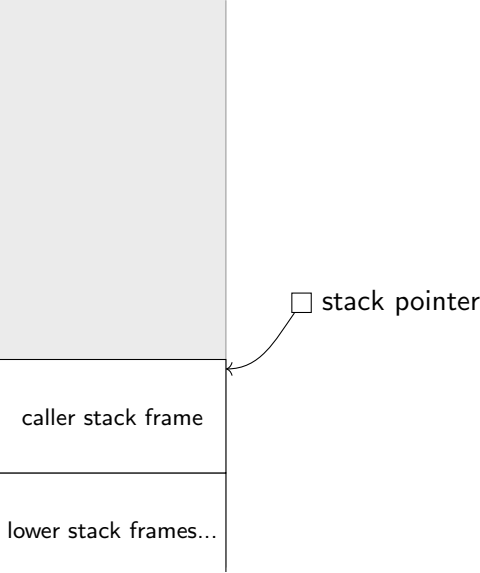
└ What Does This Program Do?

Informally, what does this program do? It allocates a reference to 0 and saves a closure to x. The closure takes a callback, assigns 0 to x, calls the callback, assigns 1 to x, calls the callback another time and finally asserts x to be 1. As a programmer that is how I would think. But what happens if I compile this closure to assembly and let some untrusted piece of code interact with it? My reasoning depends on the assumption that when I call f, then if f returns it returns to a certain point. The low-level machine needs to enforce this if I want to be able to do have this closure interact with arbitrary machine code.

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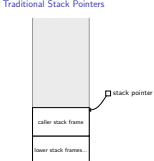
Traditional Stack Pointers



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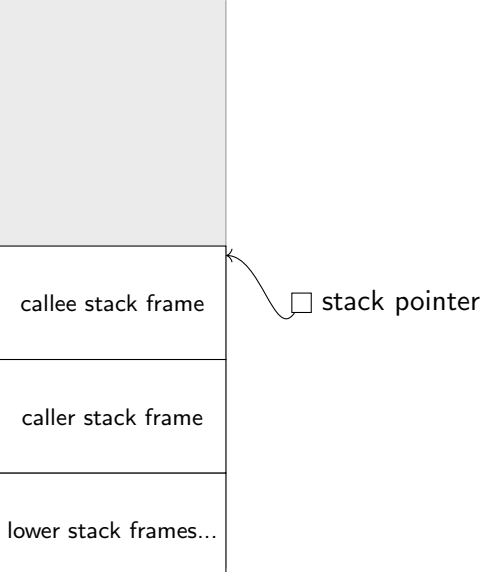
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Traditional Stack Pointers



Let's first consider what happens with the stack during a call on a traditional low-level machine and what can go wrong. We need something to enforce security properties on a low-level machine.

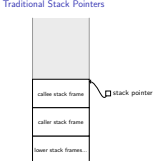
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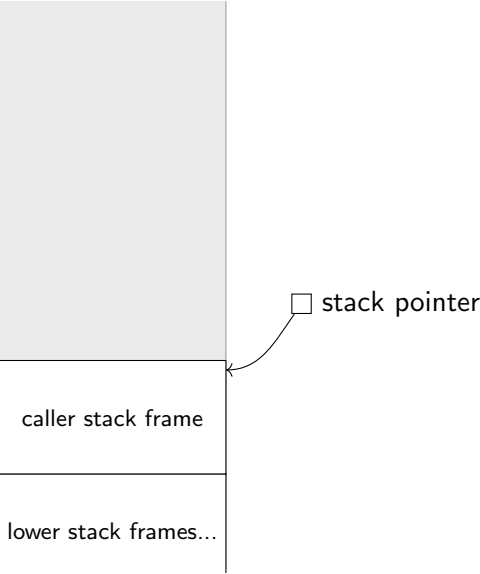
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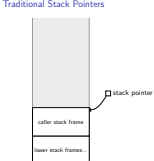
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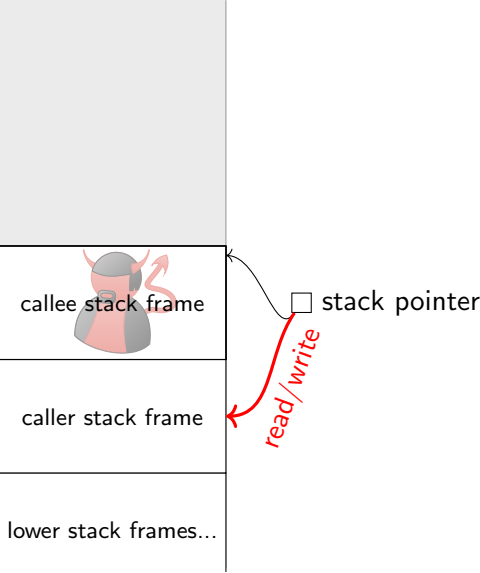
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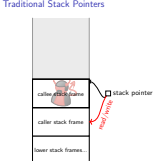
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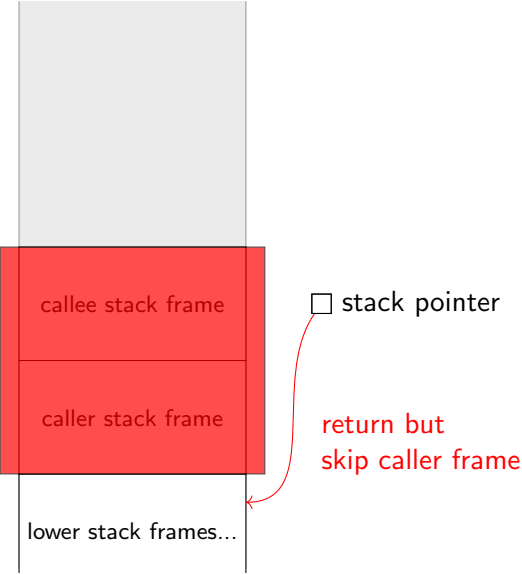
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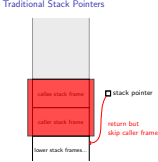
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Traditional Stack Pointers



Let's first consider what happens with the stack during a call on a traditional low-level machine and what can go wrong. We need something to enforce security properties on a low-level machine.

Capability Machine

- ▶ Low-level machine
- ▶ Capabilities replace pointers
 - ▶ Pointer
 - ▶ Range of authority
 - ▶ Kind of authority
 - ▶ read/write/execute
 - ▶ enter
- ▶ Authority checked dynamically

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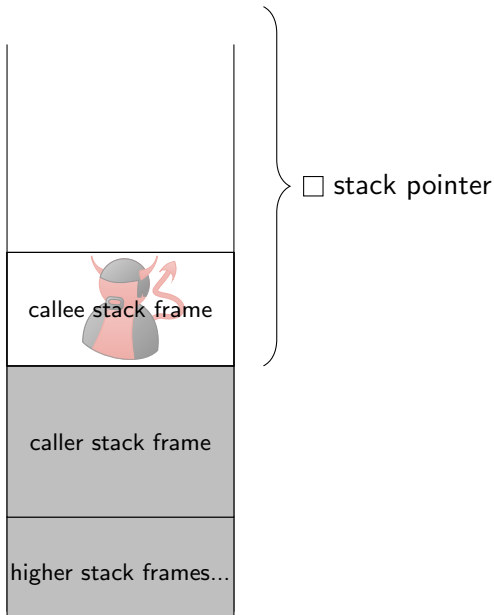
Reasoning About a Machine with Local Capabilities

- └ Capability Machine

Has instructions as you would expect, load, store, jmp, etc. Has instructions for manipulating capabilities. This particular setup gives a very fine-grained memory control.

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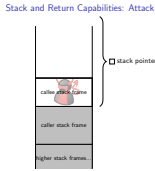
Stack and Return Capabilities: Attack 1



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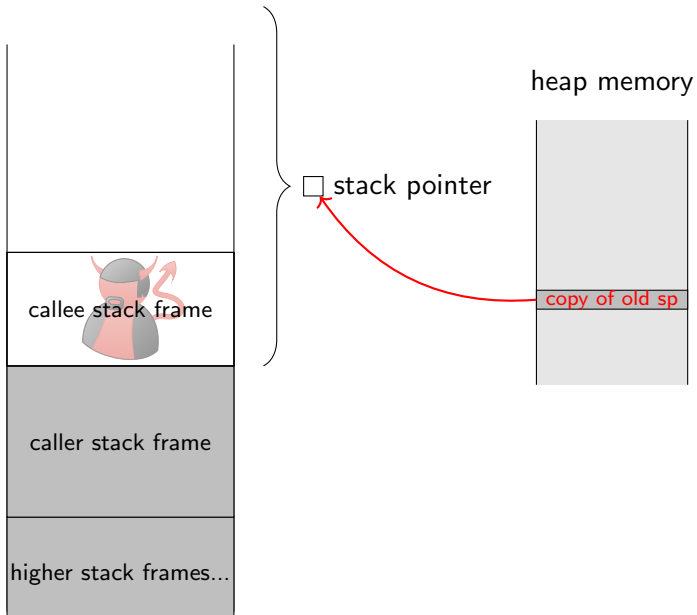
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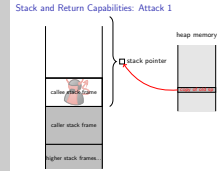
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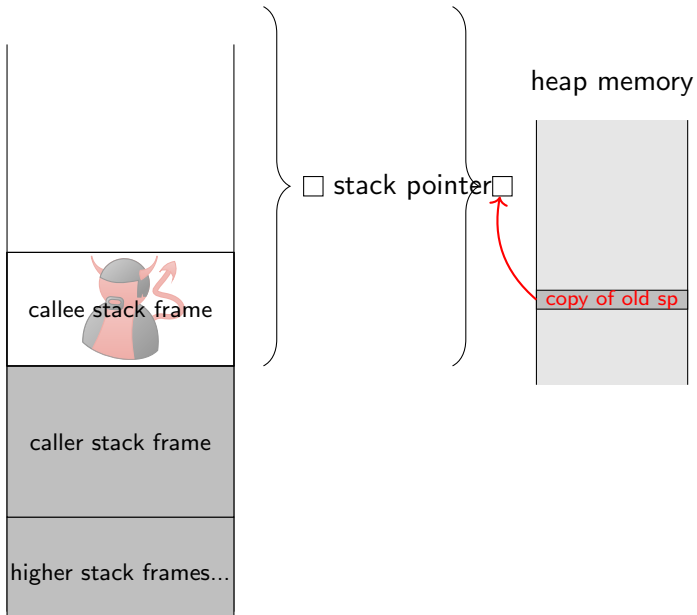
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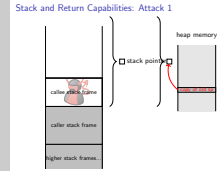
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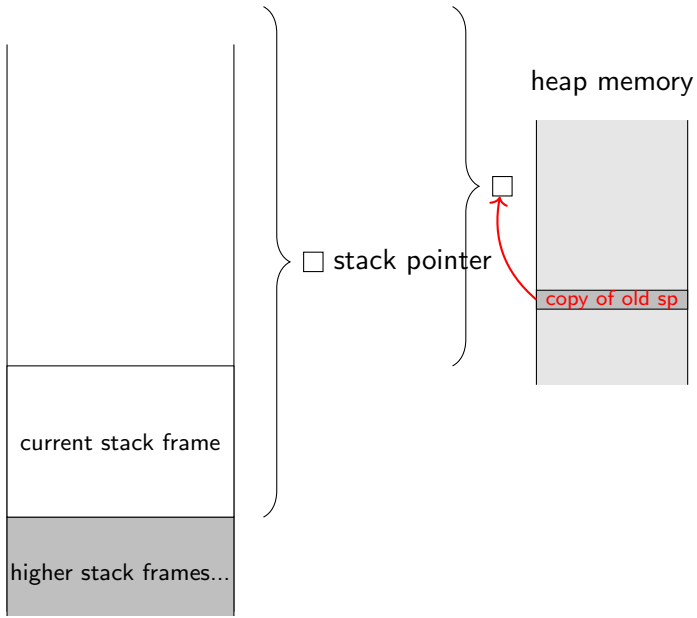
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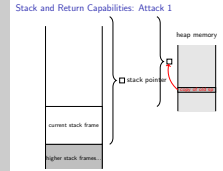
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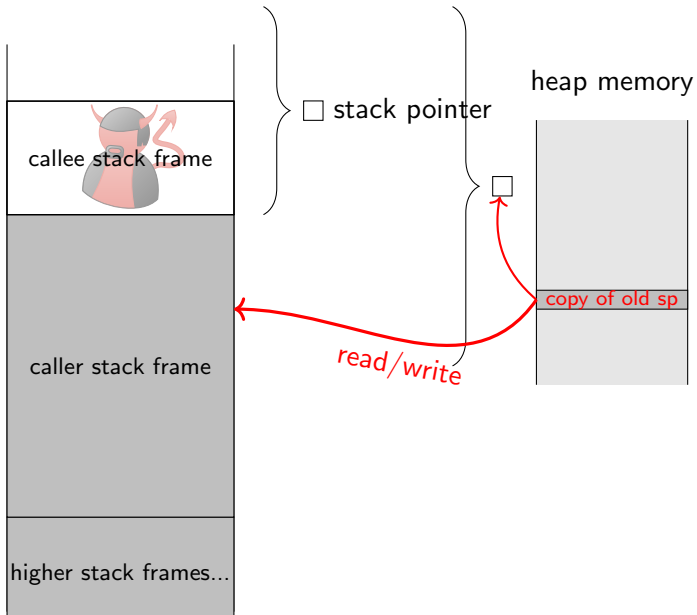
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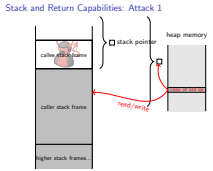
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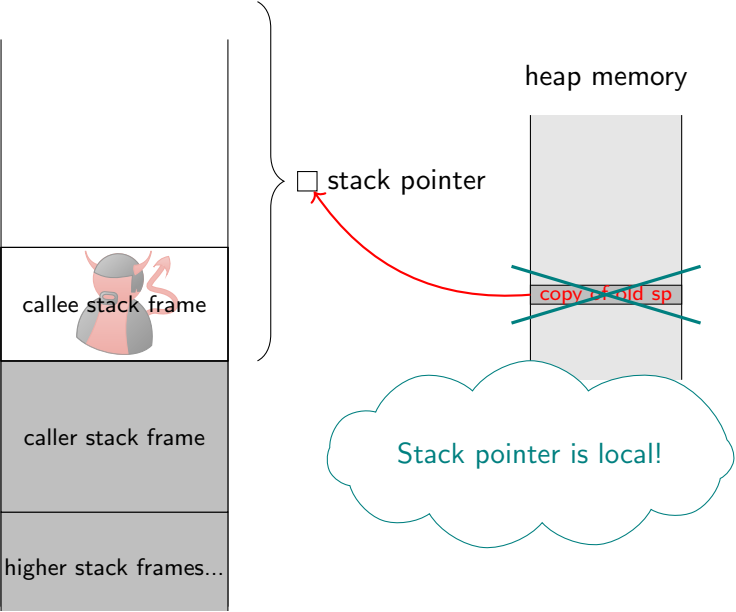
- Local Capabilities

- ▶ Capabilities tagged with locality (local or global)
 - ▶ New write-local permission.
 - ▶ Local capabilities can only be stored by capabilities with write-local permission
- Calling convention highlights
- ▶ Stack capability is local with permission read, write-local, and execute.
 - ▶ Clear stack before passing stack capability to untrusted code.

We call non-local capabilities global

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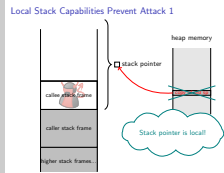
Local Stack Capabilities Prevent Attack 1



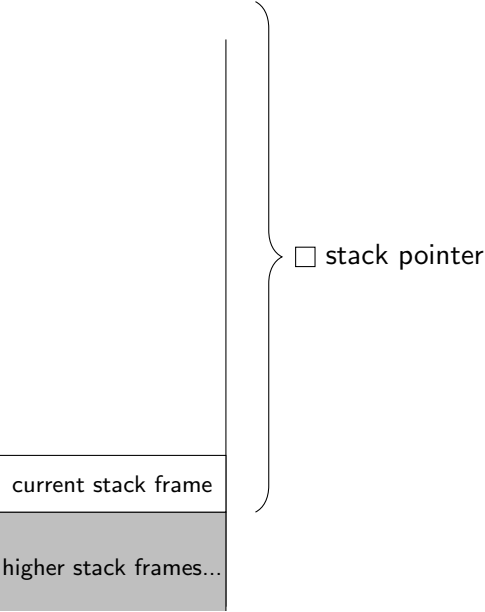
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Reasoning About a Machine with Local Capabilities

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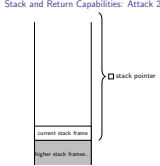
Stack and Return Capabilities: Attack 2



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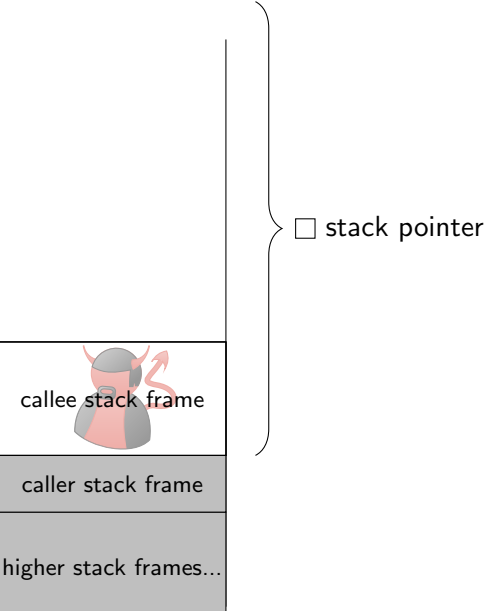
Reasoning About a Machine with Local Capabilities

└ Stack and Return Capabilities: Attack 2



The stack was the only place to store the local capability, so the adversary hid it there.

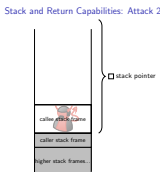
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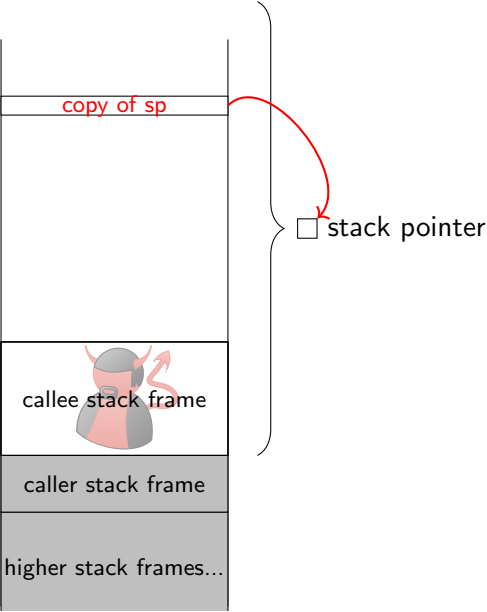
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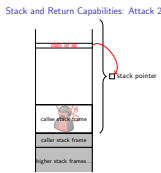
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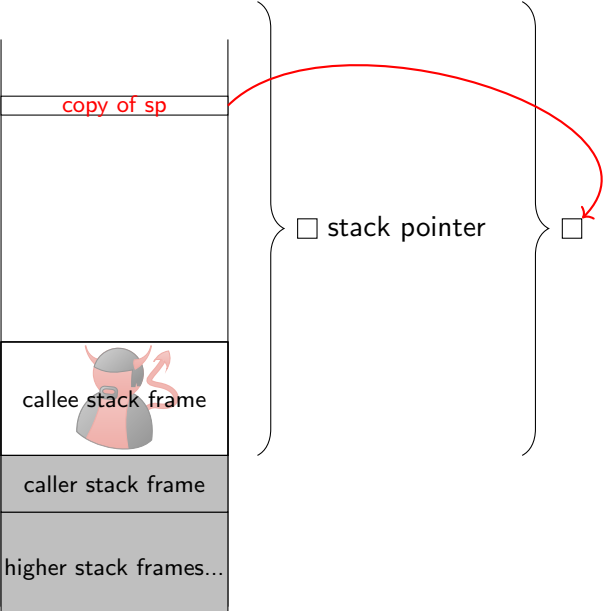
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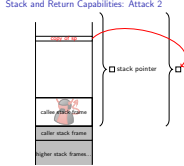
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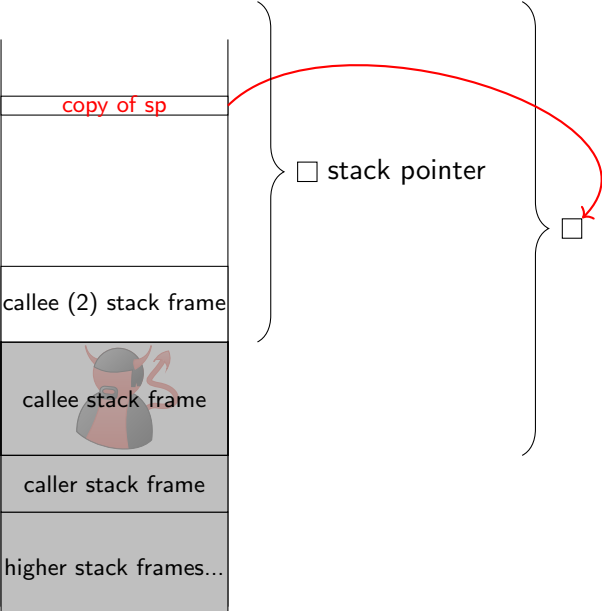
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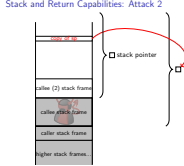
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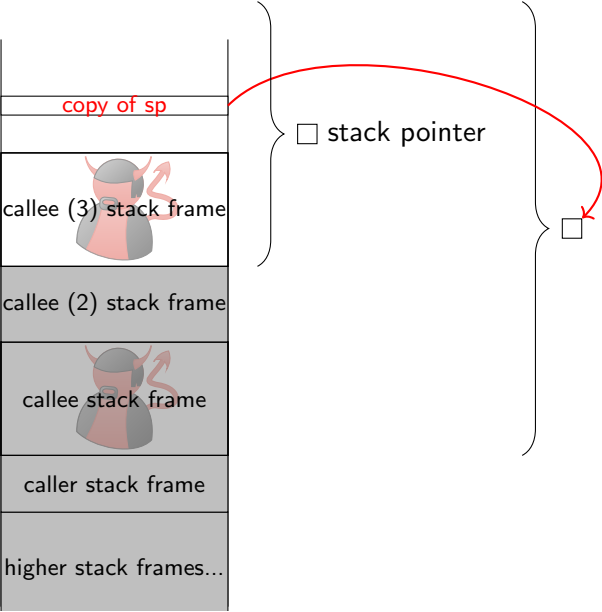
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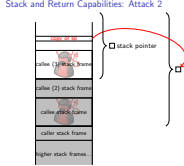
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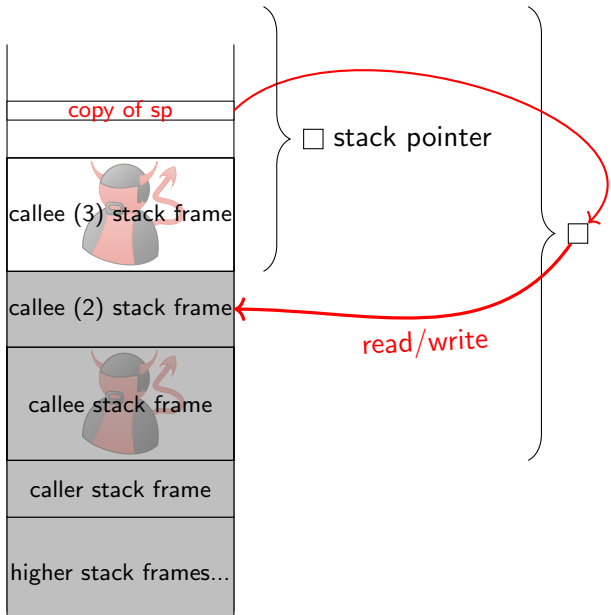
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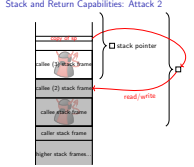
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Calling Convention (Continued)

- ▶ ...
- ▶ Clear stack and non-argument registers before invoking untrusted code.

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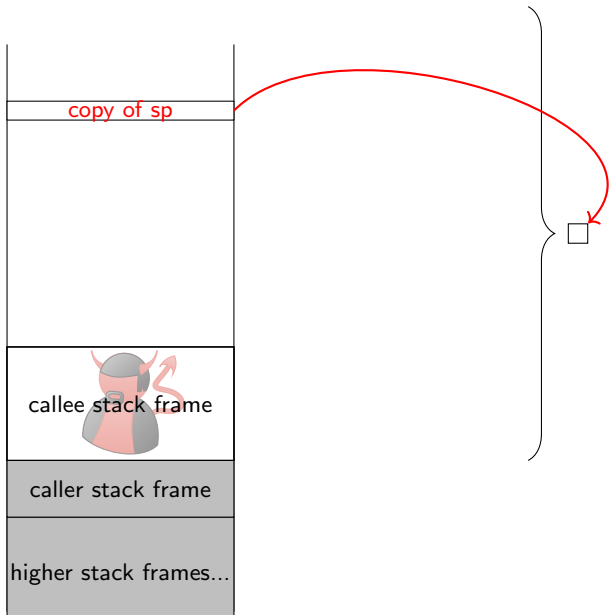
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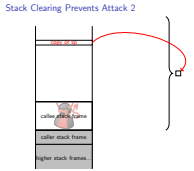
Stack Clearing Prevents Attack 2



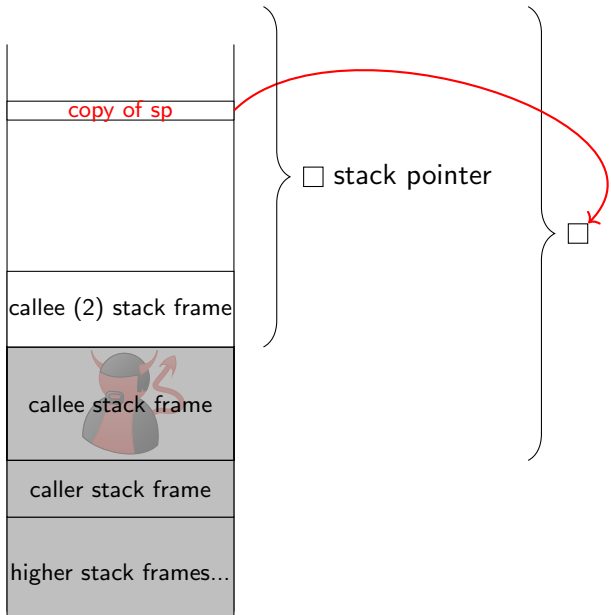
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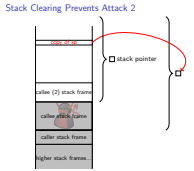
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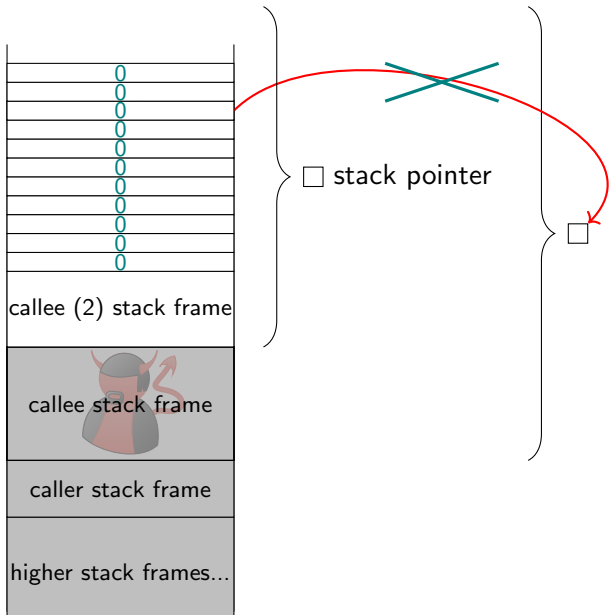
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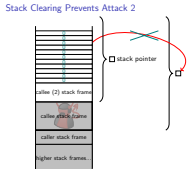
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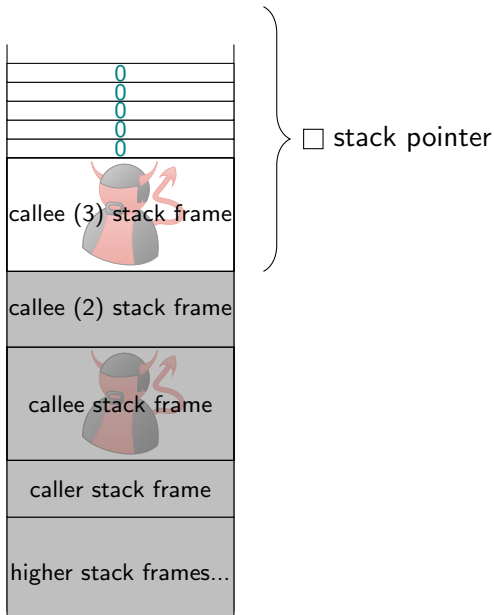
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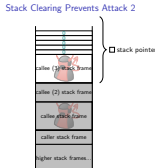
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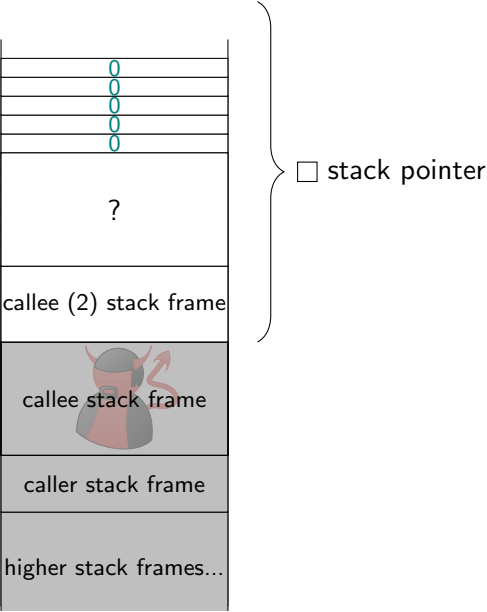
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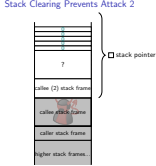
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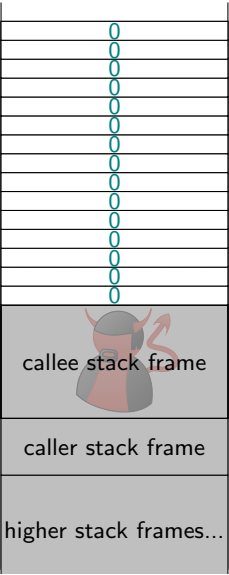
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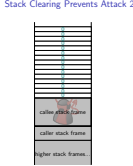
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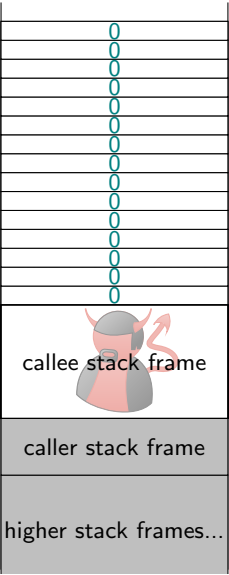
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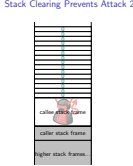
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- ## Reasoning About a Machine with Local Capabilities

(Full) Calling Convention

- ▶ Initially:
 - ▶ Stack capability local capability with read, write-local, and execute authority.
 - ▶ No global write-local capabilities on the machine.
- ▶ Prior to returning to untrusted code:
 - ▶ Clear the stack.
 - ▶ Clear non-return registers.
- ▶ Prior to calls to untrusted code:
 - ▶ Push activation record to the stack and create enter-capability
 - ▶ Restrict the stack pointer to the unused part and clear that part.
 - ▶ Clear non-argument registers.
- ▶ Only invoke global call-backs.
- ▶ When invoked by untrusted code
 - ▶ Make sure the stack pointer has read, write-local and execute authority.

- ▶ How do we know the calling convention works?
- ▶ Unary step-indexed Kripke logical relation over recursive worlds
 - ▶ Statement of guarantees provided by the capability machine

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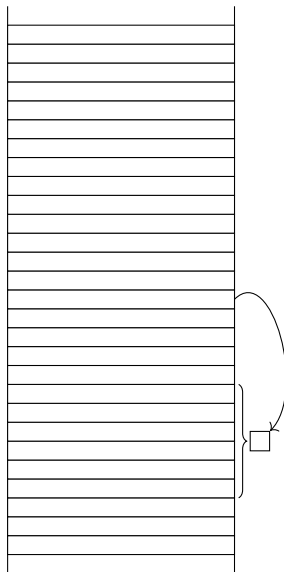
Reasoning About a Machine with Local Capabilities

└ Formalizing the Guarantees of a Capability Machine

- ▶ How do we know the calling convention works?
- ▶ Unary step-indexed Kripke logical relation over recursive worlds
 - ▶ Statement of guarantees provided by the capability machine

How can we be sure calling convention works. Specifically, if a program interacts with intrusted code using the CC, can we formally show the correctness of the program if it relies on well-bracketedness or local-state encapsulation. Need formal statement of the guarantees provided by the capability machine including the specific guarantees for local capabilities. We state this formal statement in terms of a unary step-indexed Kripke logical relation over recursive worlds. Calling convention main application, but it is very general - can be used to reason about other programs. In the following: give some intuition for different parts of LR correctness here could be assert not violated. mention better than previous. Define the guarantees

- ▶ Capabilities represent bound on executing code



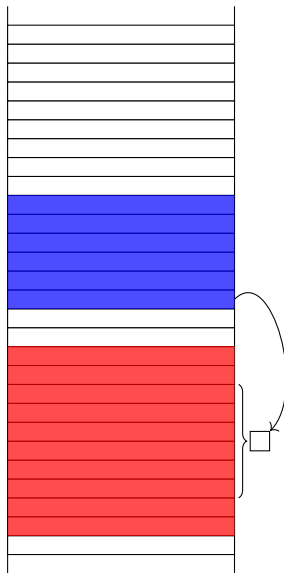
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└ Worlds, Safe Values, and Step-Indexing

compared to normal assembly, capabilities represent bounds on executing block of code. we have no observable I/O, so the authority bounds we consider are related to memory. However, more fine-grained/detailed than read/write-authority. a piece of code can be bound by arbitrary memory invariants which we define in a world. essentially, a world is a collection of invariants and safety of words defined with respect to a world. Define a set of words that are safe w.r.t. world W $V(W)$ in $P(\text{Word})$ Whether a capability is safe depends on the authority it carries Example, w.r.t. a world with the invariant that an address contains constant. Safe for read capability, not write as write capability can break this invariant. safety for a read capability is the case if the read capability only gives access to safe capabilities. What if that part of memory contains a read capability for the same part of memory? Cyclic definition. Solved by step-indexing. related to similar issue with languages with recursive types or higher-order ML-list references Solved by step-indexing - safety up to a certain number of

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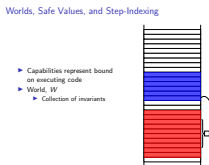
- ▶ Capabilities represent bound on executing code
- ▶ World, W
 - ▶ Collection of invariants



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Worlds, Safe Values, and Step-Indexing



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- A diagram of a memory stack represented as a vertical column of cells. The stack is divided into three main sections: a top section of 10 white cells, a middle section of 10 blue cells, and a bottom section of 10 red cells. A bracket on the right side of the red section is labeled with the number 42. A curved arrow points from the right side of the blue section down to a small square box, which is also connected to the bracket on the red section.

└ Worlds, Safe Values, and Step-Indexing

Worlds, Safe Values, and Step-Indexing

- Capabilities represent bound on executing code
- World, W**
 - Collection of invariants



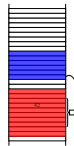
The diagram shows a vertical stack of memory cells. The top section consists of several white cells. Below these is a blue-shaded section labeled 'W' on the right, representing a world. Below the blue section is a red-shaded section labeled 'W' on the right, representing another world. A bracket on the right side of the red section points to a small box labeled 'W', indicating a collection of invariants. The stack continues with more white cells at the bottom.

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Worlds, Safe Values, and Step-Indexing

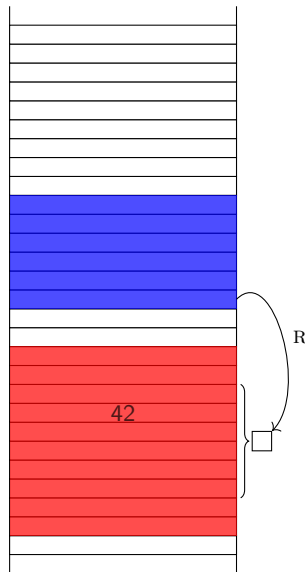
- ▶ Capabilities represent bounds on executing code
- ▶ World, W
 - ▶ Collection of invariants
- ▶ Predicate for safe values w.r.t world, $\mathcal{V}(W)$



compared to normal assembly, capabilities represent bounds on executing block of code. we have no observable I/O, so the authority bounds we consider are related to memory. However, more fine-grained/detailed than read/write-authority. a piece of code can be bound by arbitrary memory invariants which we define in a world. essentially, a world is a collection of invariants and safety of words defined with respect to a world. Define a set of words that are safe w.r.t. world W $V(W)$ in $P(\text{Word})$ Whether a capability is safe depends on the authority it carries Example, w.r.t. a world with the invariant that an address contains constant. Safe for read capability, not write as write capability can break this invariant. safety for a read capability is the case if the read capability only gives access to safe capabilities. What if that part of memory contains a read capability for the same part of memory? Cyclic definition. Solved by step-indexing. related to similar issue with languages with recursive types or higher-order ML-list references Solved by step-indexing - safety up to a certain number of

Worlds, Safe Values, and Step-Indexing

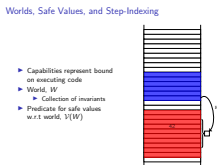
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Reasoning About a Machine with Local Capabilities

Worlds, Safe Values, and Step-Indexing

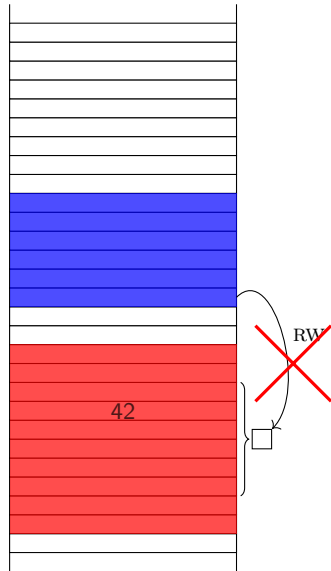


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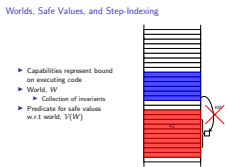
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Reasoning About a Machine with Local Capabilities

Worlds, Safe Values, and Step-Indexing



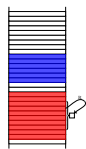
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- A vertical stack of memory cells. The top 10 cells are white. The next 5 cells are blue. Below the blue cells is one white cell, followed by a group of 8 red cells. To the right of the red cells, a bracket groups them, with an arrow pointing to a small square box labeled 'R'. Below the red cells is one white cell, and the stack ends with one more white cell at the bottom.

Worlds, Safe Values, and Step-Indexing

- ▶ Capabilities represent bounds on executing code
- ▶ World, W
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- ▶ Predicate for safe values w.r.t world, $V(W)$



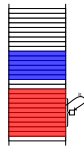
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- A vertical stack of memory cells. The top 10 cells are white. The next 8 cells are blue. The next 2 cells are white. The next 12 cells are red. The bottom 2 cells are white. A bracket on the right side of the red region is labeled 'R'.

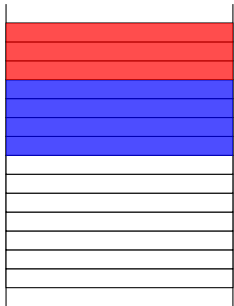
Worlds, Safe Values, and Step-Indexing

- ▶ Capabilities represent bound on executing code
- ▶ World, W
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- ▶ Predicate for safe values w.r.t world, $\mathcal{V}(W)$
 - ▶ Recursively defined



list references Solved by step-indexing - safety up to a certain number of

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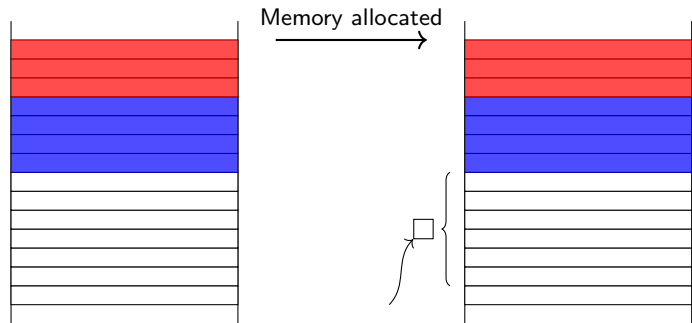
Reasoning About a Machine with Local Capabilities

Future Worlds and Invariants, and Recursive Worlds



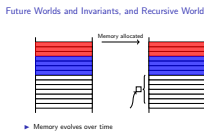
Memory changes over time, for instance new memory may be allocated. Allow worlds to evolve. New invariants can be added to handle freshly allocated memory. Safety of words monotone w.r.t. worlds (which makes it into a Kripke Logical relation).

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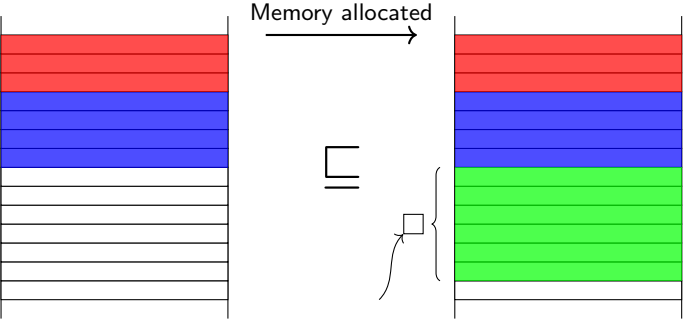
- ## Reasoning About a Machine with Local Capabilities

- Future Worlds and Invariants, and Recursive Worlds



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Future Worlds and Invariants, and Recursive Worlds

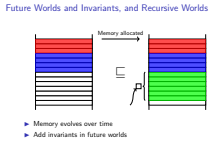


- ▶ Memory evolves over time
- ▶ Add invariants in future worlds

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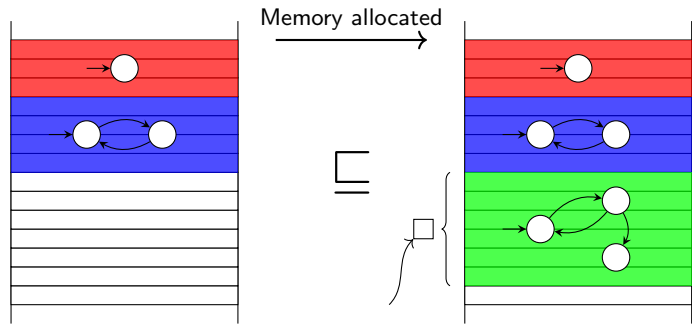
Reasoning About a Machine with Local Capabilities

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Future Worlds and Invariants, and Recursive Worlds

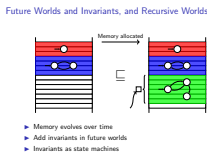


- ▶ Memory evolves over time
- ▶ Add invariants in future worlds
- ▶ Invariants as state machines

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Reasoning About a Machine with Local Capabilities

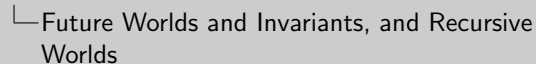
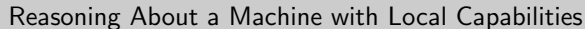
Future Worlds and Invariants, and Recursive Worlds



- ▶ Memory evolves over time
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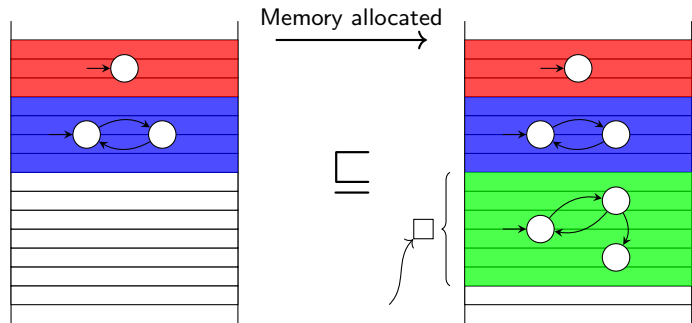
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- ▶ Each state contains a predicate of accepted memory segments

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Future Worlds and Invariants, and Recursive Worlds



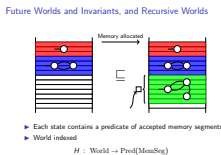
- ▶ Each state contains a predicate of accepted memory segments
- ▶ World indexed

$$H : \text{World} \rightarrow \text{Pred}(\text{MemSeg})$$

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Reasoning About a Machine with Local Capabilities

Future Worlds and Invariants, and Recursive Worlds



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Local Capabilities

\mathfrak{f} is unknown code and \mathfrak{c} is a capability.

 $f(c);$ $f(1)$

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Reasoning About a Machine with Local Capabilities

- Local Capabilities

Local Capabilities

```
f(c);
f();
```

when c global, second invocation in a world where c is safewhen c local, second invocation in a world where c is not (necessarily) safec essentially revoked, so the invariants it relies on need not hold.Two future world relations, one for all capabilities and one for non-local capabilitiesIf c global, then second invocation must happen in public future world, so c valid.If c local, then second invocation may happen in a private future world.How does local/global capabilities affect all this.If we hand a global capability to untrusted code, then it may be stored in memory, so we will only be able to reinvoke that code if we can guarantee that those values are still valid. Formally, the worlds contain the invariants that the global capability depend on and reinvocation is only possible in future worlds where these invariants are respected.Local capabilities on the other provides a means to revoke capabilities. If we invoke untrusted code and give them a local capability, then they have no way to store it aside from the register file (and the stack) so when they return we can be sure that the local capability

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Reasoning About a Machine with Local Capabilities

- Local Capabilities

f is unknown code and c is a capability.

```
f(c);  
f(1)
```

► c global \Rightarrow available in second invocation of f

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Reasoning About a Machine with Local Capabilities

Local Capabilities

ε is unknown code and c is a capability

$$\begin{aligned} & f(c); \\ & f(1) \end{aligned}$$

- ▶ c global \Rightarrow available in second invocation of ε
- ▶ c local \Rightarrow not available in second invocation of

Local Capabilities

\mathfrak{f} is unknown code and \mathfrak{c} is a capability.

$$\frac{f(c)}{f(1)}$$

- ▶ c global \Rightarrow available in second invocation of f
- ▶ c local \Rightarrow not available in second invocation of f

Lemma (Double monotonicity of value relation)

- ▶ If $(n, w) \in \mathcal{V}(W)$ and $W' \sqsubseteq^{pub} W$ then $(n, w) \in \mathcal{V}(W')$.
- ▶ If $(n, w) \in \mathcal{V}(W)$ and $W' \sqsubseteq^{priv} W$ and w is not a local capability, then $(n, w) \in \mathcal{V}(W')$.

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Reasoning About a Machine with Local Capabilities

└ Local Capabilities

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Local Capabilities

ε is unknown code and c is a capability.

$$\begin{aligned} & \mathbb{E}(c) \\ & \mathbb{E}(1) \end{aligned}$$

- ▶ c global \Rightarrow available in second invocation of E
- ▶ c local \Rightarrow not available in second invocation of E

Lemma (Double monotonicity of value relation)

- ▶ If $(n, w) \in V(W)$ and $W' \sqsupseteq^{\text{pub}} W$ then $(n, w) \in V(W')$
- ▶ If $(n, w) \in V(W)$ and $W' \sqsupseteq^{\text{priv}} W$ and w is not a local capability, then $(n, w) \in V(W')$.

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- ## Reasoning About a Machine with Local Capabilities

└ Fundamental Theorem of Logical Relations

- ▶ General statement about the guarantees provided by the capability machine.
- ▶ Intuitively: any program is safe as long as it only has access to safe values.

Theorem (Fundamental theorem (simplified))

If

$$(n, \{b, a\}) \in \text{readCond}[g](W)$$

then

$$(n, ((\text{rx}, g), b, a)) \in \mathcal{E}(W)$$

readCond is the assumption that every thing in the interval $[a, e]$ is safe to read. \mathcal{E} is safe to execute relation. That is, it will respect all the memory invariants. That is take an arbitrary capability. If it only has access to safe capabilities then it will preserve the invariants of the world. Remember, dynamic checks = failing is considered secure

$$(n, ((RX, g), b, e, a)) \in \mathcal{E}(W)$$

“Awkward Example”

```
let x = ref 0 in
  λf. (x := 0;
       f();
       x := 1;
       f();
       assert (x == 1))
```

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Reasoning About a Machine with Local Capabilities

- └ “Awkward Example”

"Awkward Example"

```
let x = ref 0 in
  let f () =
    x := 0;
    f();
    x := 1;
    f();
    assert(x == 1)
  in
```

example known from the literature even just in ML difficult as f can be the closure. the assert can fail if the calls are not well-bracketed! the local state is difficult to handle as the closure and the context needs to be able to update the invariant for x in different ways. (closure can switch between 0 and 1 as it pleases, but context can transition only from 0 to 1.) relies heavily on well-bracketedness we have made a faithful translation and proved correctness (i.e., the assertion never fails). more semantic statement of guarantees allows us to do this.

Reasoning About a Machine with Local Capabilities

Conclusion

- ▶ Capability machines can guarantee properties of high-level languages
- ▶ Calling convention for well-bracketedness and local-state encapsulation
- ▶ Unary step-indexed Kripke logical relation over recursive worlds
 - ▶ Formal statement about guarantees provided by capability machine
 - ▶ Reasoning about programs in general
- ▶ Applied on the "awkward example"

Thank you!