Interface description

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| Pin. No. | Name | Description | Note for our implementation |
| 1 | DAT3 | MMC: Chip select | Chip IO Pin, mmc\_cs, D7 |
| 2 | CMD | Command | Chip IO Pin, mmc\_mosi, D8,  (MOSI – master out, slave in) |
| 3 | GND | Ground | Board ground |
| 4 | VDD | Voltage | Supply Voltage (+3.3 V) |
| 5 | CLK | Clock | Chip IO Pin, mmc\_clock, D10 |
| 6 | GND | Ground | Board ground |
| 7 | DAT0 | Data | Chip IO, mmc\_miso, B4 |
| 8 | DAT1 | Data | Pull up (Voltage via resistor) |
| 9 | DAT2 | Data | Pull up (Voltage via resistor) |

