

# Systemnahe Informatik

## Übungsgruppe Xeon Phi

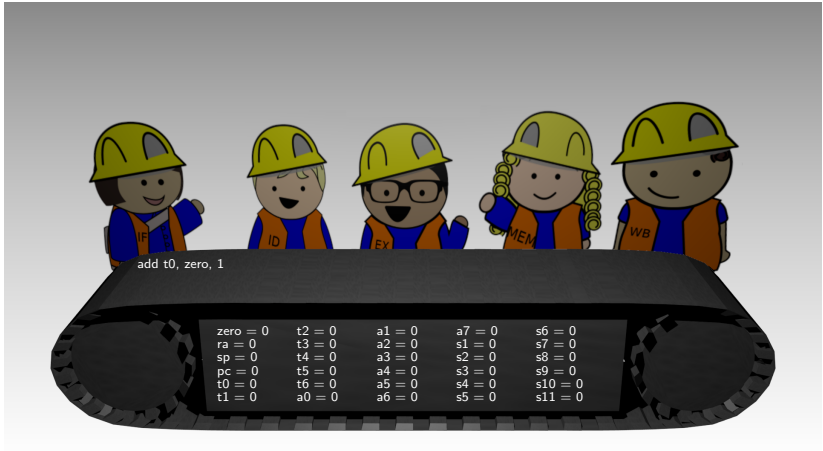
Dominik Walter

Sommersemester 2018

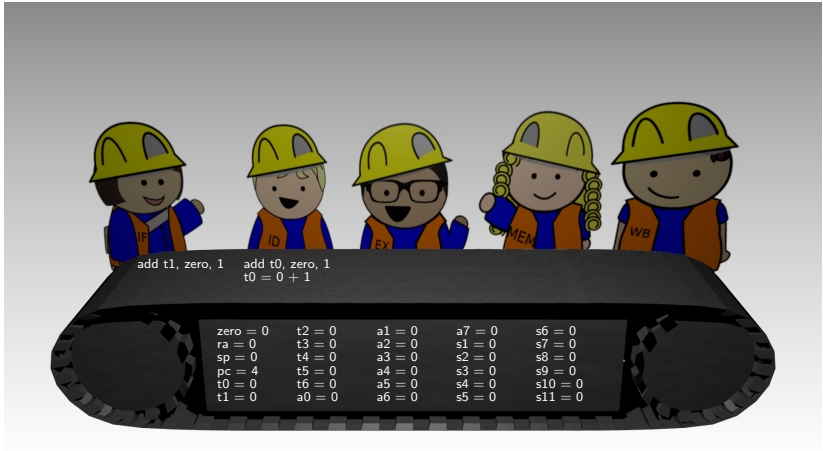
## Conditional Branch

```
0:      add t0 , zero , 1
4:      add t1 , zero , 1
8:      nop
12:     nop
16:     beq t0 , t1 , end
20:     add t2 , zero , 3
24:     add t3 , zero , 4
28:     add t4 , zero , 5
32:     add t5 , zero , 6
end:
36:     mul t0 , t1 , t2
```

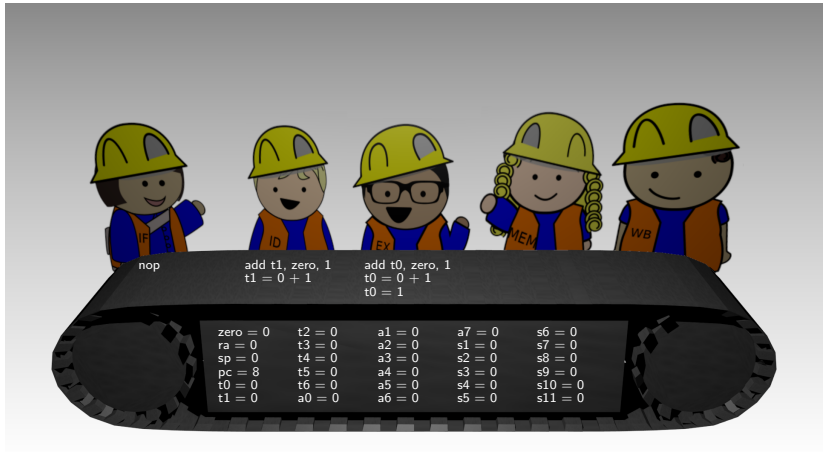
# Conditional Branch — Takt 0



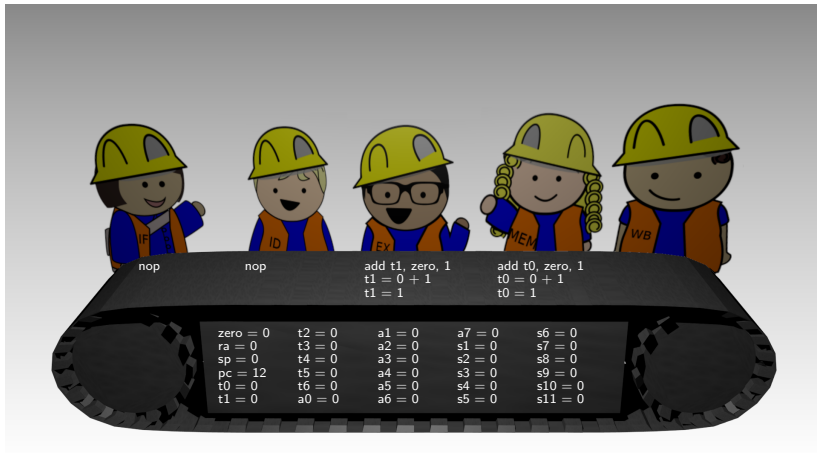
# Conditional Branch — Takt 1



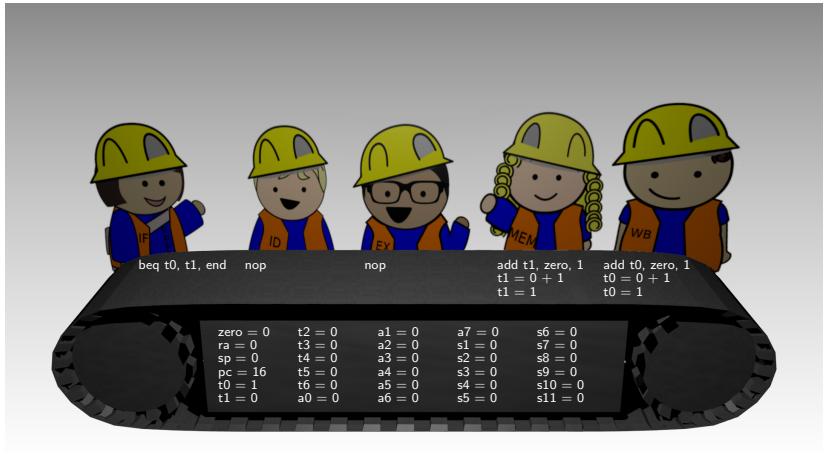
# Conditional Branch — Takt 2



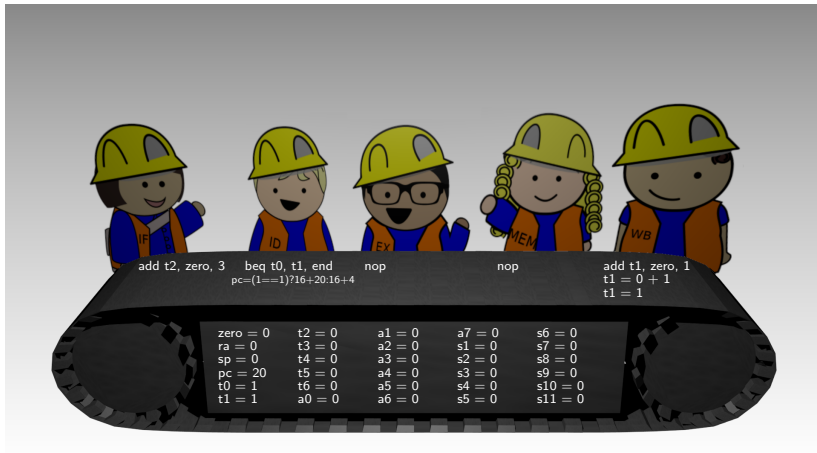
# Conditional Branch — Takt 3



# Conditional Branch — Takt 4

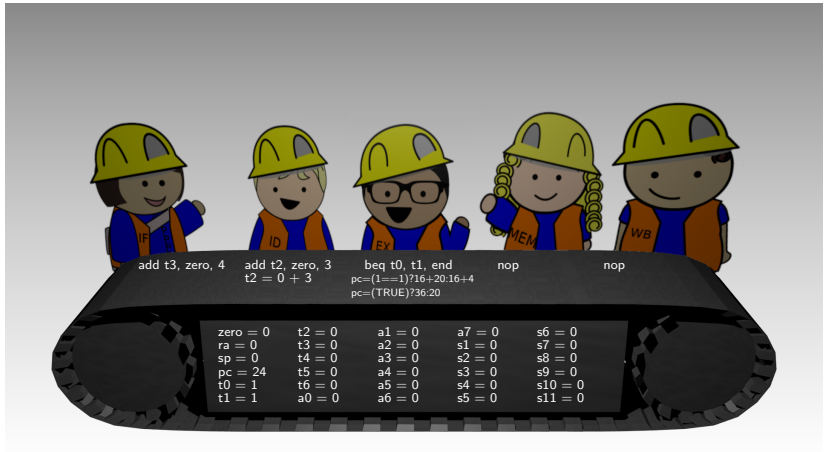


# Conditional Branch — Takt 5

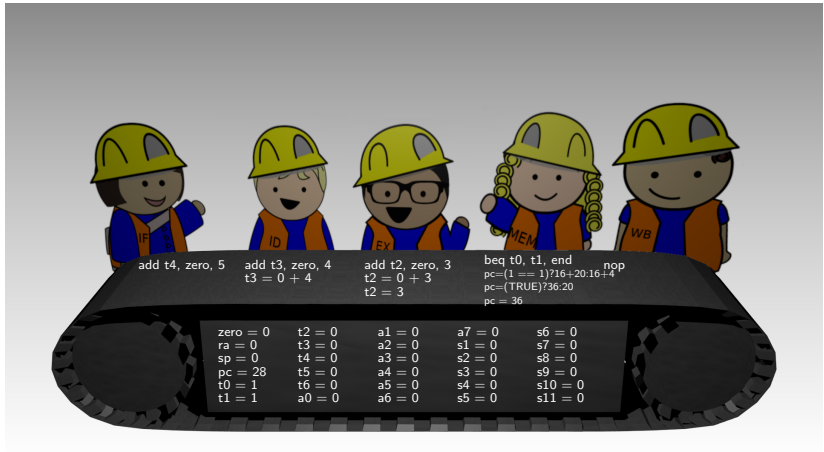




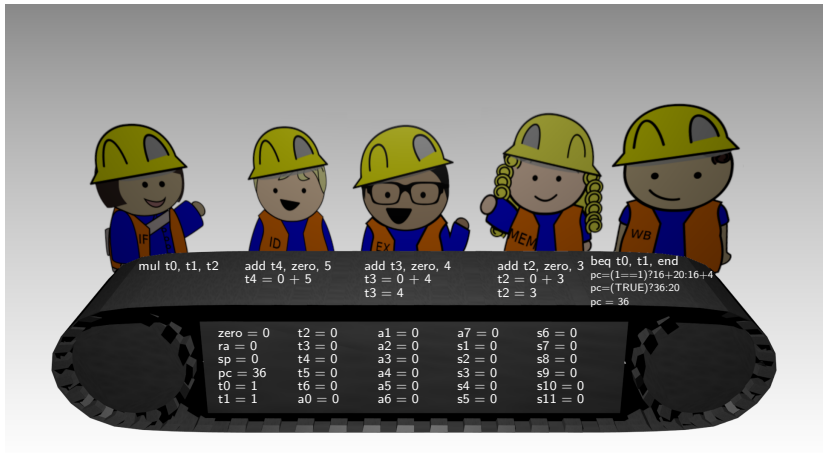
# Conditional Branch — Takt 6



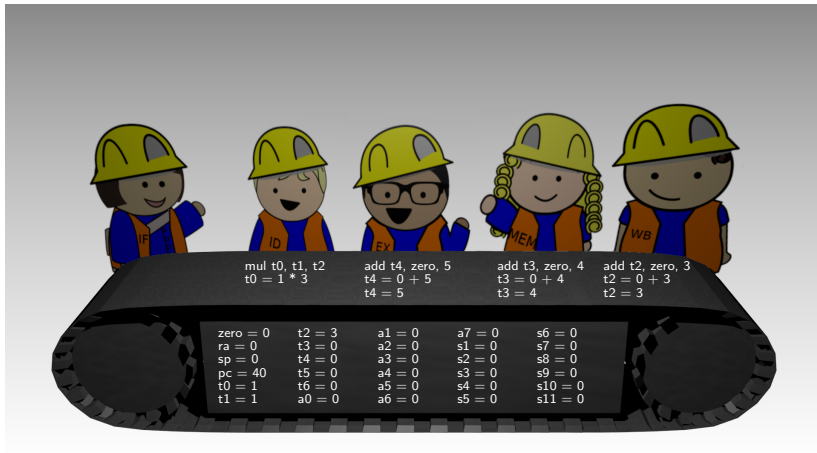
# Conditional Branch — Takt 7



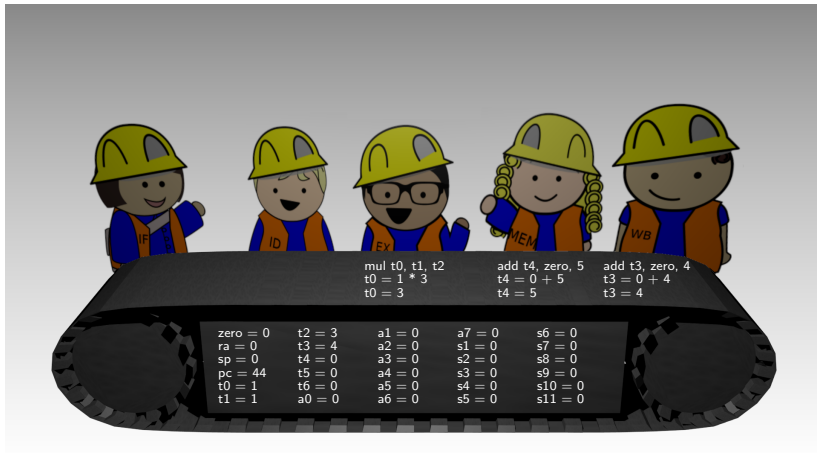
# Conditional Branch — Takt 8



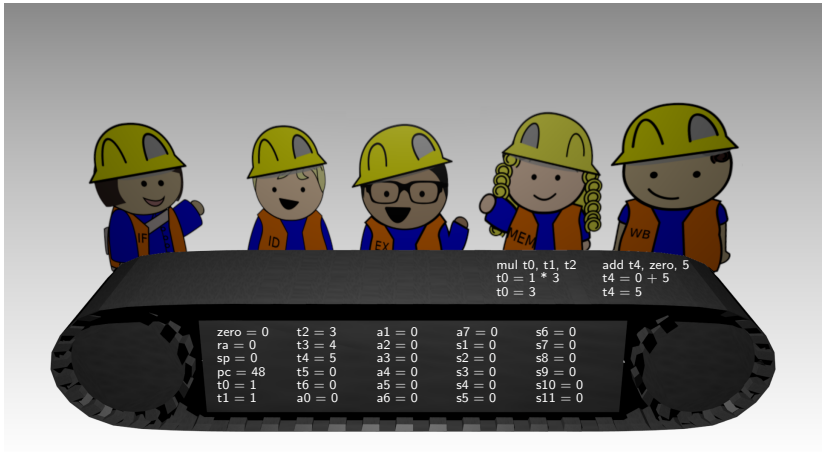
# Conditional Branch — Takt 9



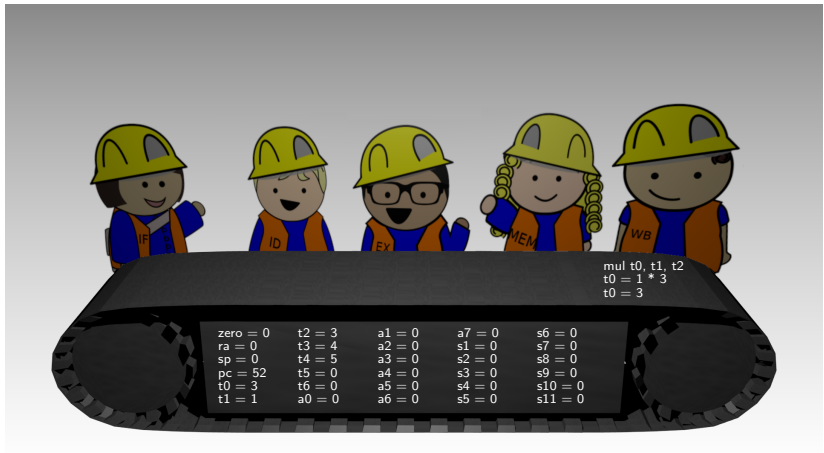
# Conditional Branch — Takt 10



# Conditional Branch — Takt 11



# Conditional Branch — Takt 12



## Conditional Branch — Takt 13

