

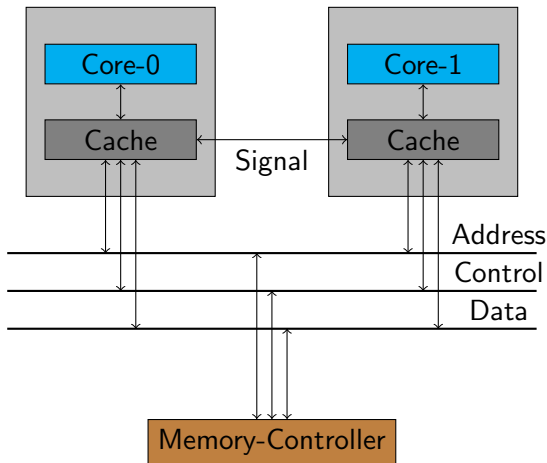
Systemnahe Informatik

Übungsgruppe Xeon Phi

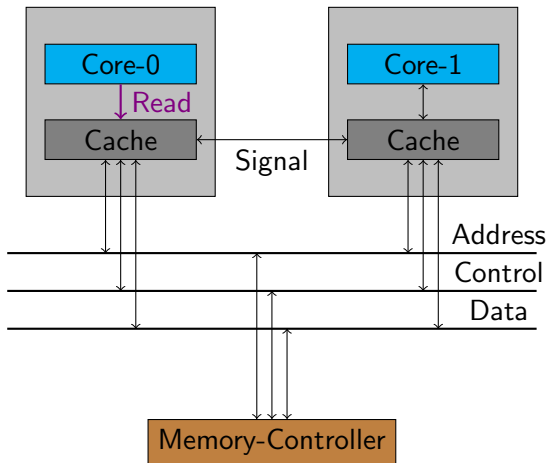
Dominik Walter

Sommersemester 2018

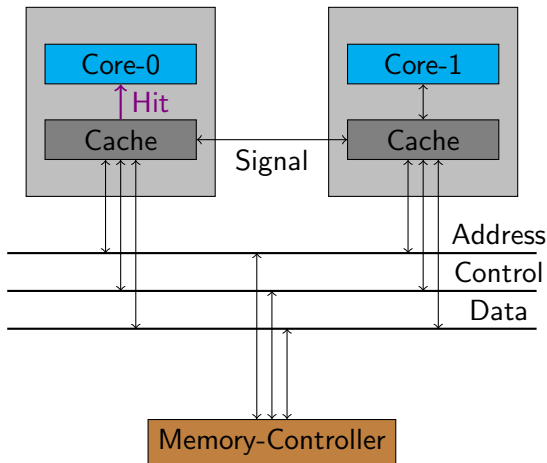
Vereinfachter Multicore



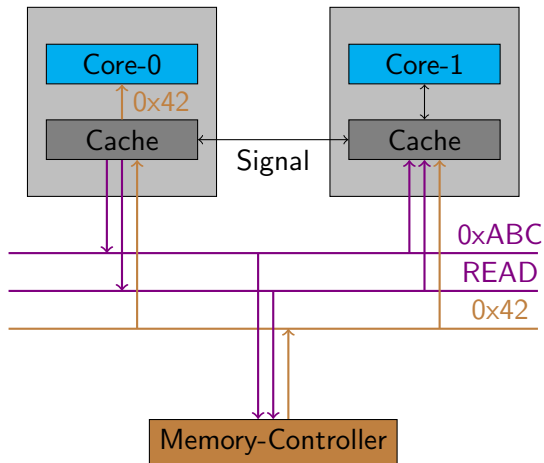
Vereinfachter Multicore — Read



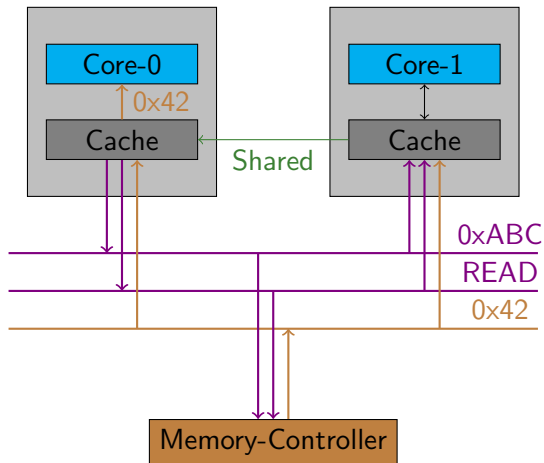
Vereinfachter Multicore — Read-Hit



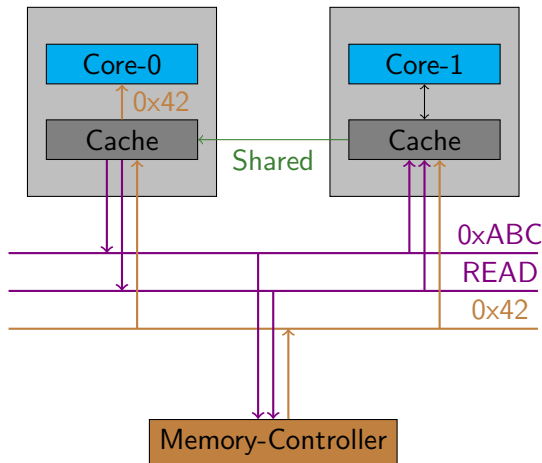
Vereinfachter Multicore — Read Miss (I/I → E/I)



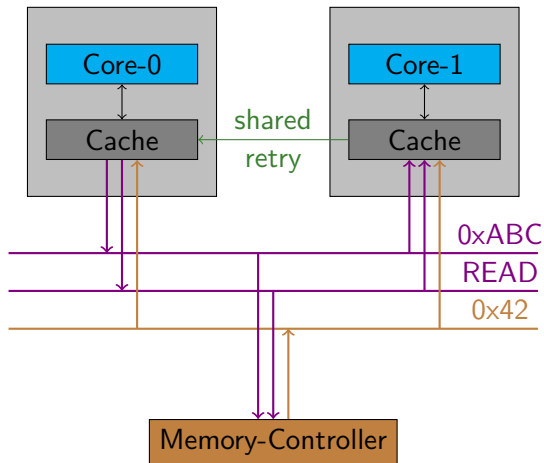
Vereinfachter Multicore — Read Miss (I/E → S/S)



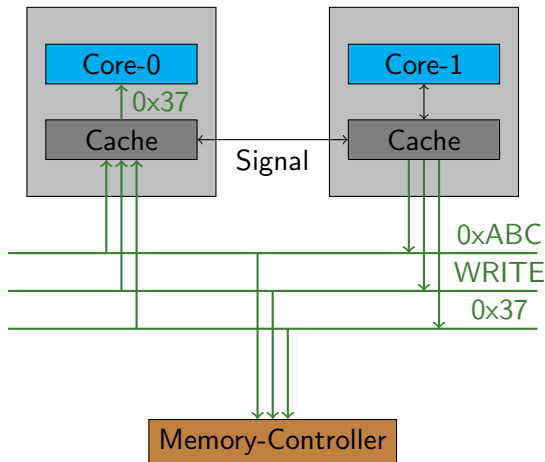
Vereinfachter Multicore — Read Miss (I/S → S/S)



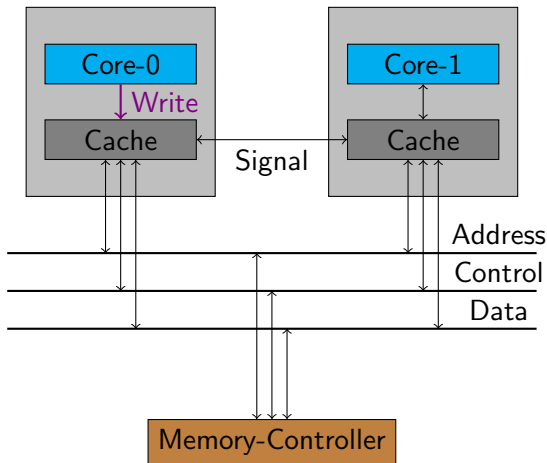
Vereinfachter Multicore — Read Miss (I/M \rightarrow S/S)



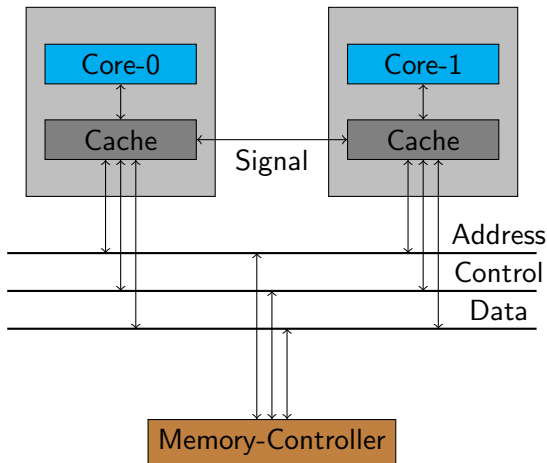
Vereinfachter Multicore — Read Miss (I/M \rightarrow S/S) (2)



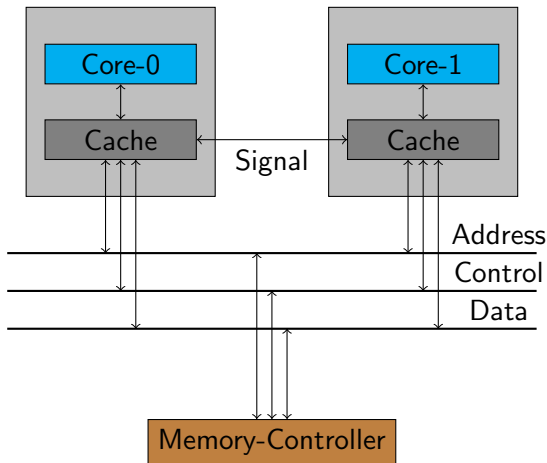
Vereinfachter Multicore — Write



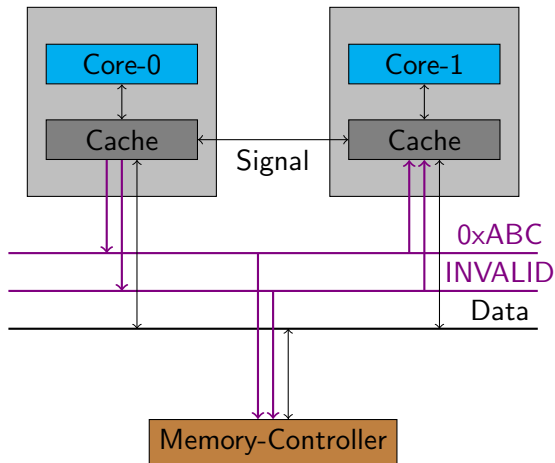
Vereinfachter Multicore — Write Hit ($M/I \rightarrow M/I$)



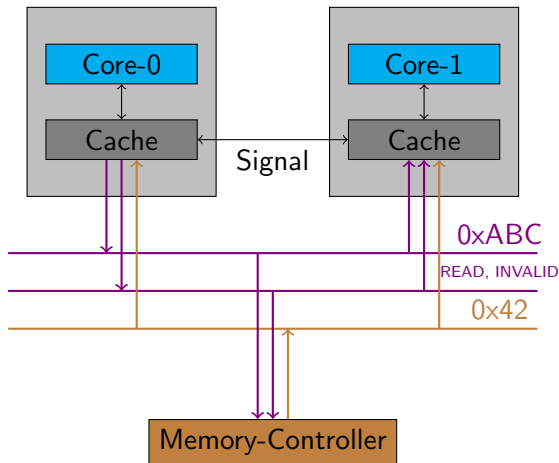
Vereinfachter Multicore — Write Hit (E/I → M/I)



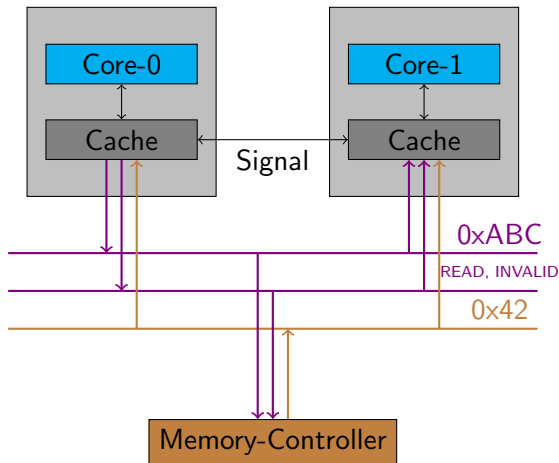
Vereinfachter Multicore — Write Hit (S/S \rightarrow M/I)



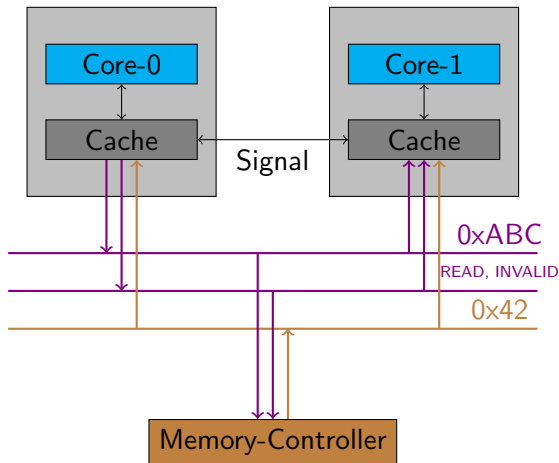
Vereinfachter Multicore — Write Miss (I/E \rightarrow M/I)



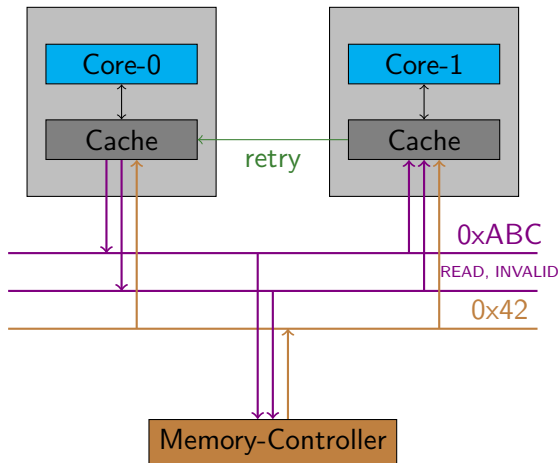
Vereinfachter Multicore — Write Miss (I/S → M/I)



Vereinfachter Multicore — Write Miss (I/I \rightarrow M/I)



Vereinfachter Multicore — Write Miss (I/M \rightarrow M/I)



Vereinfachter Multicore — Write Miss (I/M \rightarrow M/I) (2)

