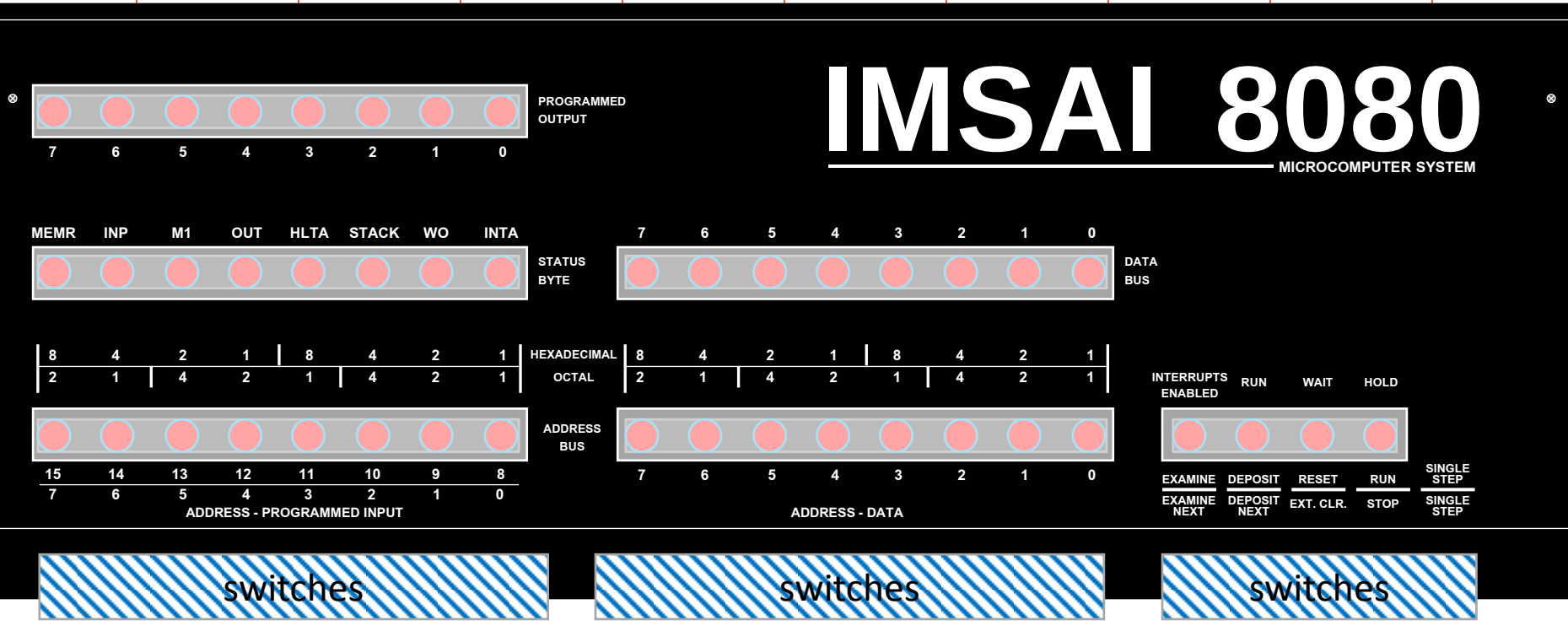
Cable

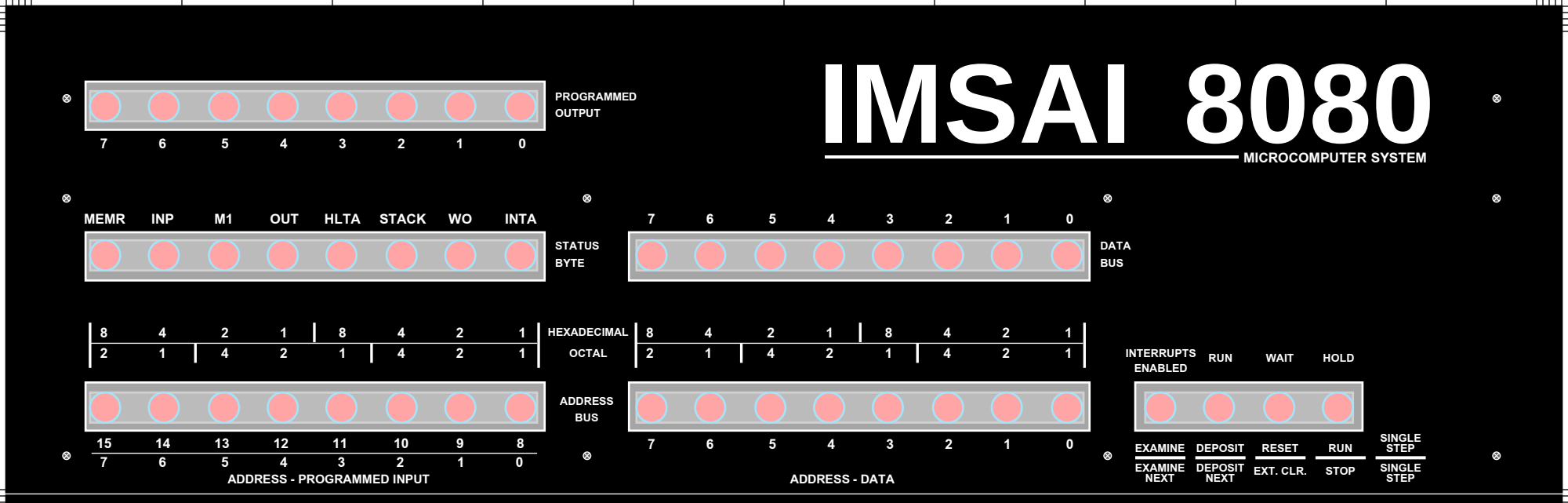


switches

switches

switches

Cable

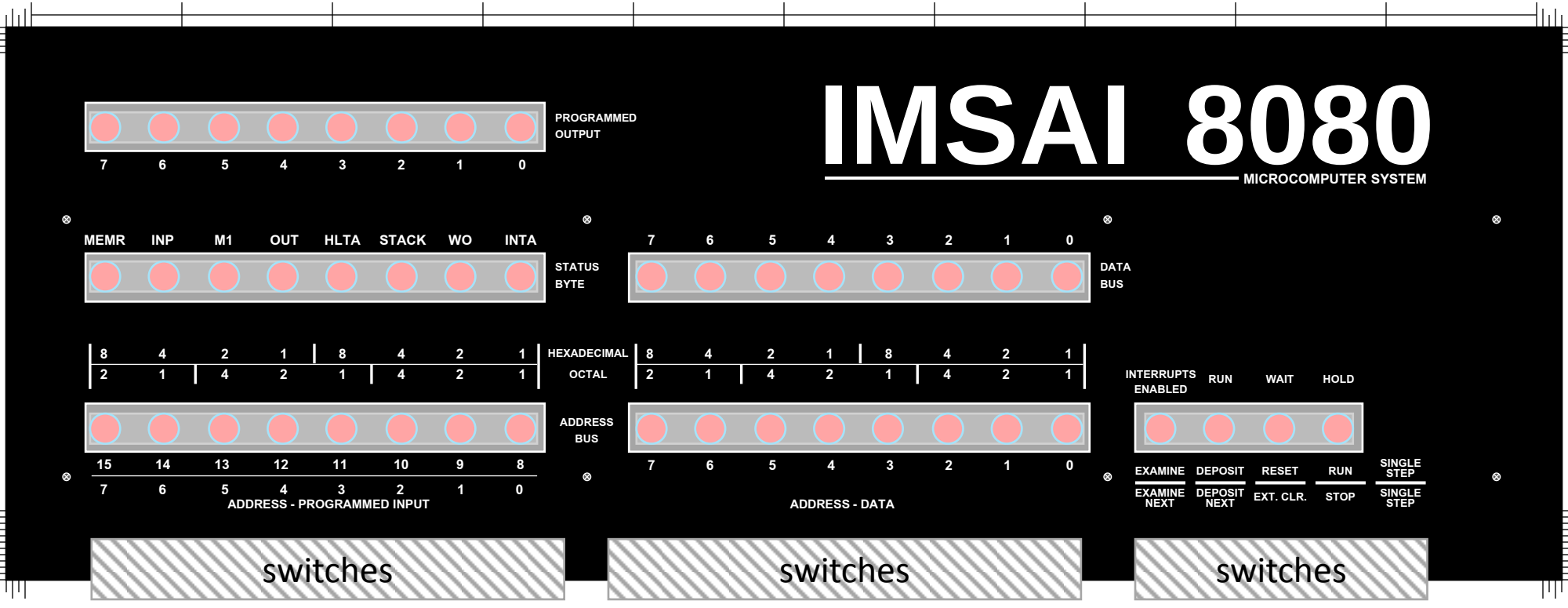


switches

switches

switches

Cable



switches

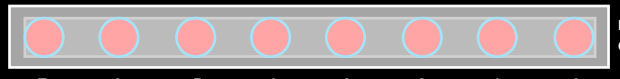
switches

switches

Cable

IMSAI 8080

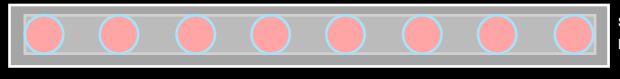
MICROCOMPUTER SYSTEM



7 6 5 4 3 2 1 0

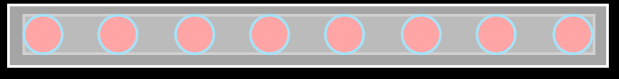
PROGRAMMED OUTPUT

MEMR INP M1 OUT HLTA STACK WO INTA



STATUS BYTE

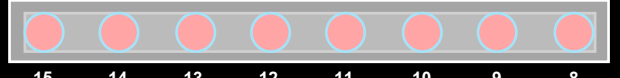
7 6 5 4 3 2 1 0



DATA BUS

8 4 2 1 | 8 4 2 1 HEXADECIMAL

2 1 | 4 2 1 | 4 2 1 OCTAL



15 14 13 12 11 10 9 8

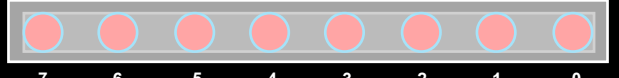
7 6 5 4 3 2 1 0

ADDRESS - PROGRAMMED INPUT

ADDRESS BUS

8 4 2 1 | 8 4 2 1 HEXADECIMAL

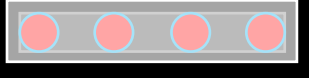
2 1 | 4 2 1 | 4 2 1 OCTAL



7 6 5 4 3 2 1 0

ADDRESS - DATA

INTERRUPTS ENABLED RUN WAIT HOLD



EXAMINE / EXAMINE NEXT

DEPOSIT / DEPOSIT NEXT

RESET / EXT. CLR.

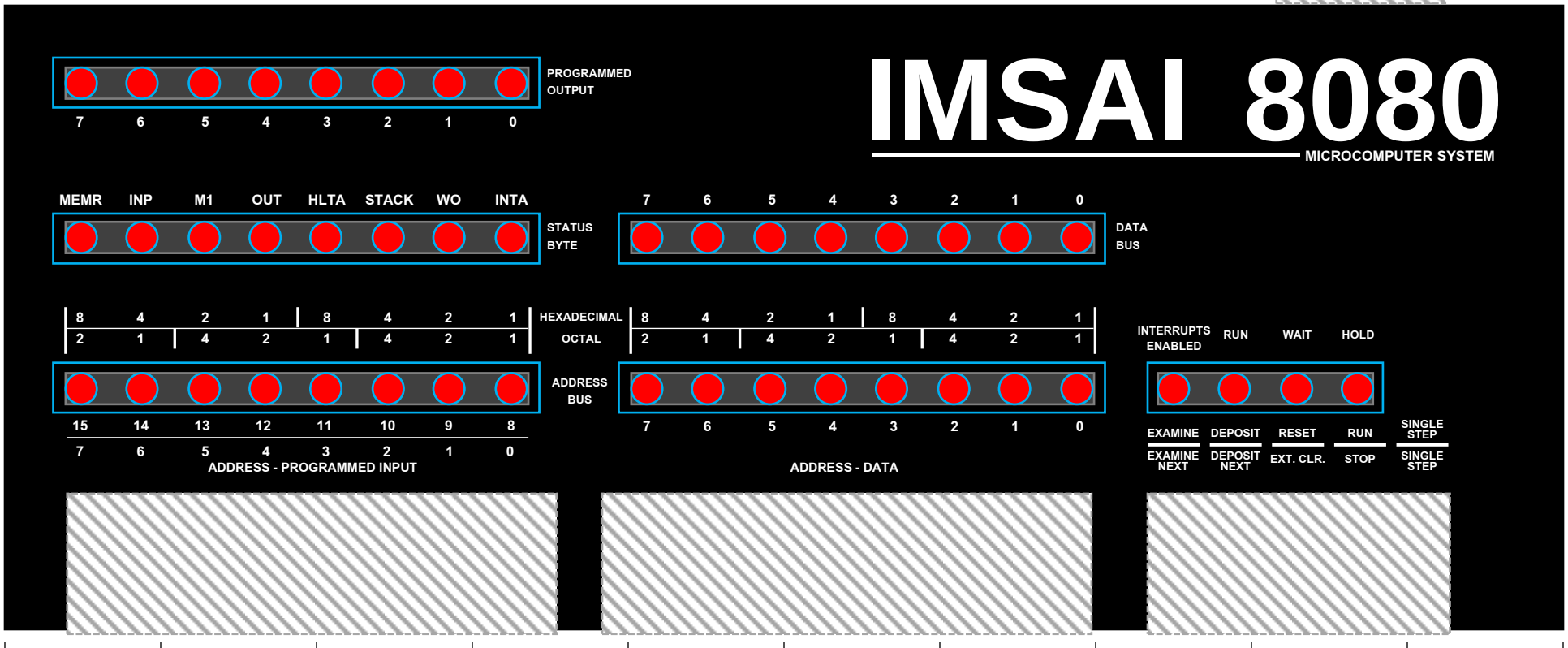
RUN / STOP

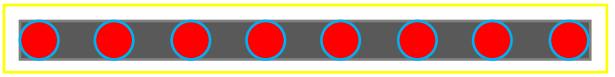
SINGLE STEP / SINGLE STEP

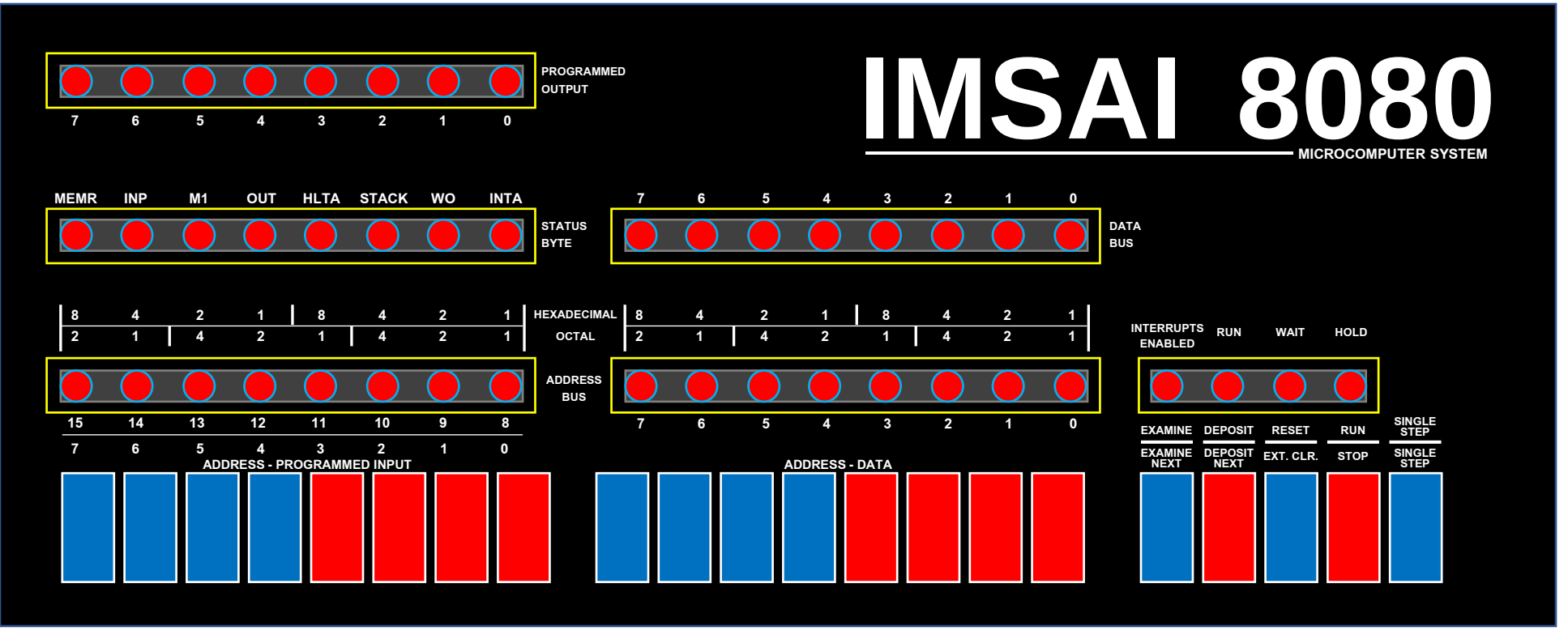
switches

switches

switches


IMSAI 8080
MICROCOMPUTER SYSTEM
PROGRAMMED OUTPUT
7 6 5 4 3 2 1 0
MEMR INP M1 OUT HLTA STACK WO INTA
STATUS BYTE
7 6 5 4 3 2 1 0
DATA BUS
8 4 2 1 8 4 2 1
HEXADECIMAL
2 1 4 2 1 4 2 1
OCTAL
ADDRESS BUS
15 14 13 12 11 10 9 8
7 6 5 4 3 2 1 0
ADDRESS - PROGRAMMED INPUT
8 4 2 1 8 4 2 1
2 1 4 2 1 4 2 1
7 6 5 4 3 2 1 0
ADDRESS - DATA
INTERRUPTS ENABLED
RUN
WAIT
HOLD
EXAMINE
EXAMINE NEXT
DEPOSIT
DEPOSIT NEXT
RESET
EXT. CLR.
RUN
STOP
SINGLE STEP
SINGLE STEP




IMSAI 8080
MICROCOMPUTER SYSTEM
PROGRAMMED OUTPUT
7 6 5 4 3 2 1 0
MEMR INP M1 OUT HLTA STACK WO INTA
STATUS BYTE
7 6 5 4 3 2 1 0
DATA BUS
8 4 2 1 8 4 2 1
HEXADECIMAL
2 1 4 2 1 4 2 1
OCTAL
8 4 2 1 8 4 2 1
2 1 4 2 1 4 2 1
ADDRESS BUS
15 14 13 12 11 10 9 8
7 6 5 4 3 2 1 0
ADDRESS - PROGRAMMED INPUT
7 6 5 4 3 2 1 0
ADDRESS - DATA
INTERRUPTS ENABLED
RUN
WAIT
HOLD
EXAMINE
EXAMINE NEXT
DEPOSIT
DEPOSIT NEXT
RESET
EXT. CLR.
RUN
STOP
SINGLE STEP
SINGLE STEP

IMSAI 8080

MICROCOMPUTER SYSTEM

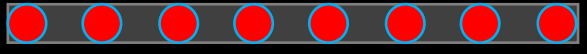
PROGRAMMED OUTPUT

7 6 5 4 3 2 1 0

MEMR INP M1 OUT HLTA STACK WO INTA

STATUS BYTE

7 6 5 4 3 2 1 0



DATA BUS

8 4 2 1 8 4 2 1 HEXADECIMAL

2 1 4 2 1 4 2 1 OCTAL

ADDRESS BUS

15 14 13 12 11 10 9 8

7 6 5 4 3 2 1 0

ADDRESS - PROGRAMMED INPUT

8 4 2 1 8 4 2 1 HEXADECIMAL

2 1 4 2 1 4 2 1 OCTAL

7 6 5 4 3 2 1 0

ADDRESS - DATA

INTERRUPTS ENABLED RUN WAIT HOLD

EXAMINE EXAMINE NEXT

DEPOSIT DEPOSIT NEXT

RESET EXT. CLR.

RUN STOP

SINGLE STEP SINGLE STEP

IMSAI 8080

MICROCOMPUTER SYSTEM

PROGRAMMED OUTPUT

7 6 5 4 3 2 1 0

MEMR INP M1 OUT HLTA STACK WO INTA

STATUS BYTE

DATA BUS

8 4 2 1 8 4 2 1
2 1 4 2 1 4 2 1

HEX
OCTAL

INTERRUPTS ENABLED RUN WAIT HOLD

ADDRESS BUS

ADDRESS BUS

15 14 13 12 11 10 9 8

7 6 5 4 3 2 1 0

ADDRESS-PROGRAMMED INPUT

ADDRESS-DATA

EXAMINE DEPOSIT RESET RUN SINGLE STEP

EXAMINE NEXT DEPOSIT NEXT EXT. CLR. STOP SINGLE STEP

IMSAI 8080


IMSAI 8080
MICROCOMPUTER SYSTEM


IMSAI 8080
MICROCOMPUTER SYSTEM