

DATE	REV	BY	DESCRIPTION
2024-07-03	0.1		First Revision
2024-08-26	0.2		MCU LDO 모드 추가 MCU 패키지 R11/R12 핀 오류 수정

6

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D

C

B

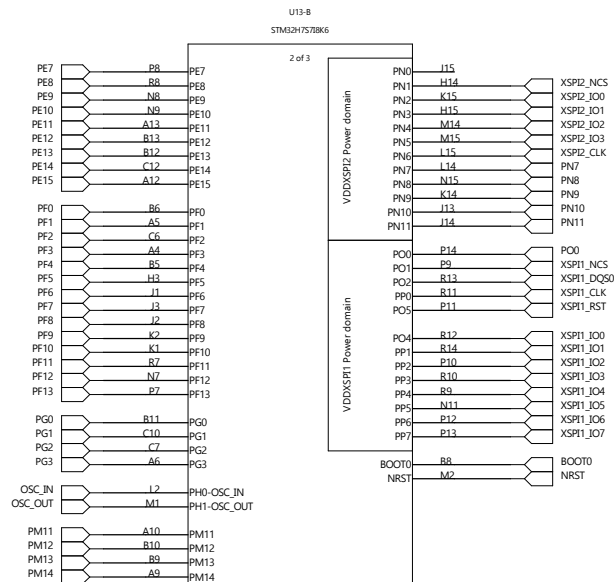
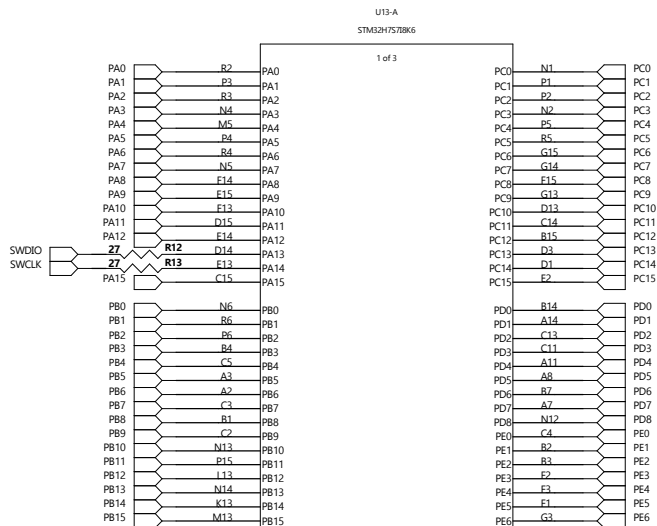
A

D

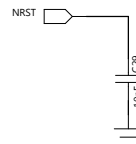
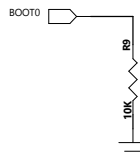
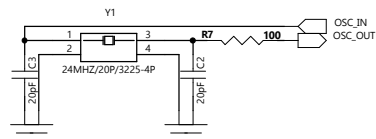
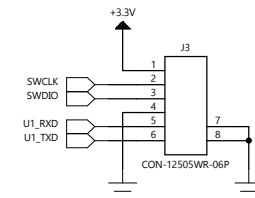
C

B

A



DEBUG



NAME:	HG_H7S_SOM	
SHEET:	MCU	
REV:	0.1	SHEET: 2 OF 4

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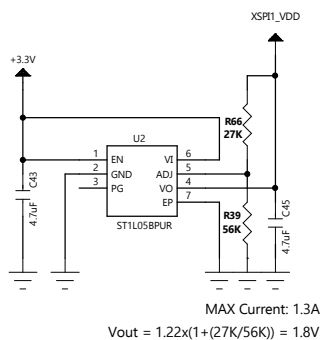
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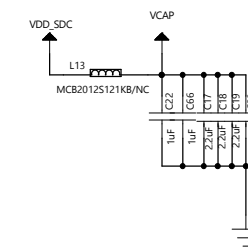
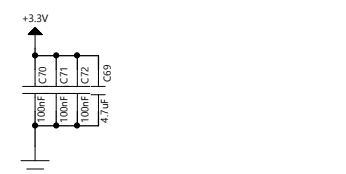
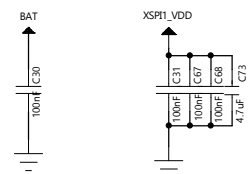
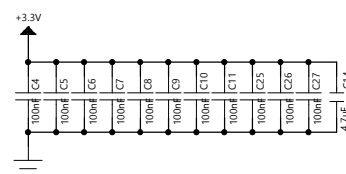
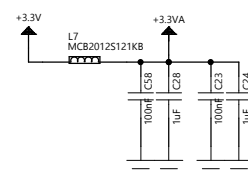
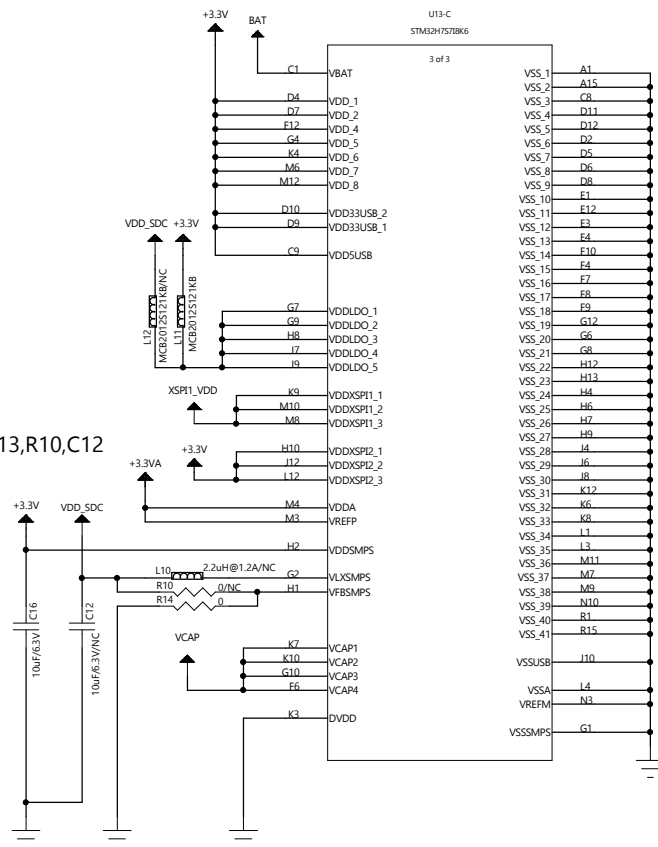
PSRAM POWER



LED



LDO Mode: L11,R14
SMPS Mode: L10,L12,L13,R10,C12



NAME:	HG_H7S_SOM	
SHEET:	POWER	
REV:	0.1	SHEET: 3 OF 4

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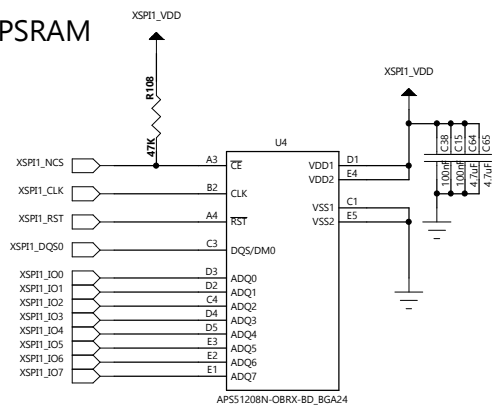
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OSPI PSRAM



- DQS0 with IO0..7 (Byte 0), and CLK, all in 50 mils
- NCS length can be in the range of up to CLK length +/-750 mils

QSPI FLASH

