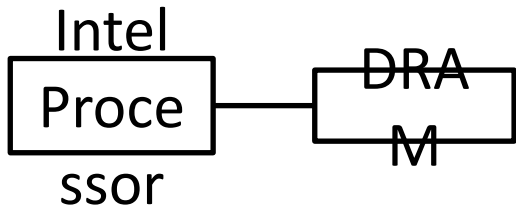


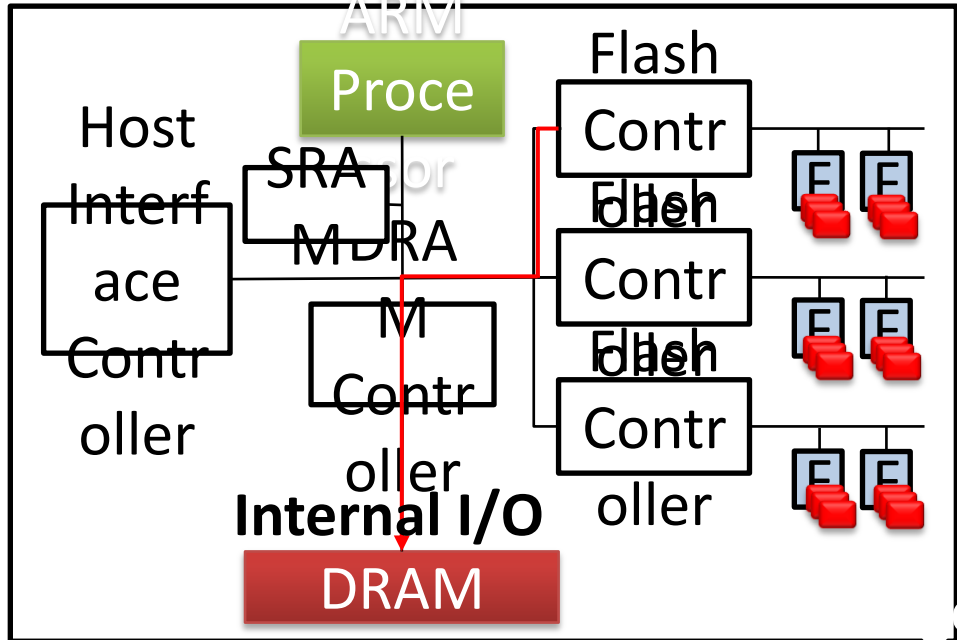
A diagram illustrating the connection between an Intel Processor and DRAM. On the left, a rectangular box is labeled "Intel Processor". A horizontal line connects this box to another rectangular box on the right, which is labeled "DRAM".



Host I/O

SSD Internals

The diagram illustrates the internal architecture of an SSD. It features a central **Processor** (green box) connected to a **Host Interface Controller** (white box) on the left. The Processor is also connected to a **Flash Controller** (white box) on the right. Below the Processor is a **DRAM** (red box) connected via **Internal I/O**. The Flash Controller is connected to multiple **Flash** (blue boxes) which are further connected to **Flash** (red boxes). The diagram also shows a **Host Interface Controller** connected to a **Flash Controller** and a **Flash** (blue box) which is further connected to **Flash** (red boxes). The diagram is labeled with **Host Interface Controller**, **Processor**, **Flash Controller**, **Flash**, **Internal I/O**, and **DRAM**.



Internal I/O