



PROFILE



YANG DONGDONG

Software Engineering Manager

Name: Yang Dongdong
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10 years software development experience in network system and product, across router, switch and firewall. With solid knowledge in C programming and debugging, expert in IP/MPLS and ethernet protocols. Now as a manager of SRX platform team, owns CPU/BSP, Driver, Kernel, Network stack, Forwarding engine on [SRX firewall](#).

A quick learner with pursuing new technique in industry. Willing try some idea or solve problem with programming, so my career target is to be a Full-stack Engineer from assembly to cloud.



RESUME



EMPLOYMENT

Juniper Networks

2013.4 - present

Software Engineering Manager

As main developer to transfer enterprise product from Indian R&D to China R&D, and quickly build team and ramp-up in branch firewall knowledge.

Successfully won the confidence with sustaining team from Indian, got promotion recommendation from their manager.

After stable in mataince, join in next-generation firewall design and development, works with U.S. team.

Promote as manager of platform team from 2015.9, works on CPU/BSP, Driver, Kernel, Network stack, Forwarding engine.

Huawei

2006.7 - 2013.3

Senior Software Engineer

Works in Huawei Beijing R&D for nearly 7 years in network system platform. As a technical leader in team for 4 years, project leader for 1 years. Be responsible for module design, coding, patent etc.

Chief designer and developer in TRILL project, it's a highlight feature in data-center switch, and got Beijing R&D designer award in 2012.

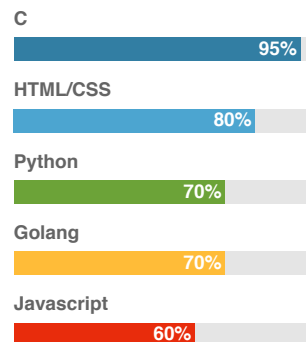
As technical leader, publish 5 patents as author or co-author in network and system area.

Optimize and tuning performance of routes converage, win top1 result serval time in comparison test with competitor.

Kickout nearly 10 projects as project leader and developer in our team with high quality, refer the details in LinkedIn.

Be a mentor and tutor in knowledge ramp-up.

PROGRAMMING SKILLS



NETWORK SKILLS

- ISIS/OSPF/BGP/LDP/PIM
- Security Flow
- MPLS/L3VPN/VPLS/QoS
- Datacenter/TRILL
- SDN/NFV





EDUCATION



Shenyang University of Technology

2002 - 2006

Computer Science & Technology

Got scholarship 3 times, all A grade in computer experiment course.

Startup computer association with roommates.

Participated many programming competitions online, like ACM/ICPC, Topcoder, Baidu Astar.

TOOLS SKILLS

Linux	<div></div>
GDB	<div></div>
Sublime	<div></div>
Django	<div></div>
AngularJS	<div></div>



PROJECT

All

Juniper

Huawei

Personal

Patent

[Daybreak](#)

An new SRX software architecture for cloud service. Control plane OS works in nested VM, security service runs on X86/Linux with DPDK. Provide same service as box, but all run in virtual machine.

[Forge](#)

X86 based security platform, with VT-x, KVM/QEMU. To improve the datapatch performance, leverage the DPDK, SRIOV, VirtIO tools.

[Branch SRX](#)

Branch SRX targets the enterprise market, the SRX platforms are from on-desk to 2U chassis, with MIPS architecture. It integrates the JUNOS and ScreenOS. Provide full features of security. Flow, Routing, NAT, Screen, ALG, VPN, UTM, IDP.

[wFilter](#)

wFilter is a mini full-stack personal project for blocking the webpage which contains keyword in database with highperformance design, with Aho-Corasick string matching and DPDK. Front-end is "MEAN"(MongoDB, ExpressJS, AngularJS, Node.js)

[Bitcoin Autotrade Robot](#)

Draft with Python and rewrite with Golang, it deploys on Raspberry PI, and do transaction automatically based on the latest price and strategy. The parameters can be configured anytime and anywhere with mobile phone. Implement APIs for TOP5 Bitcoin transaction site.

[Disk Usage Visualized](#)

Visualized the usage of disk with d3.js, data is caught by shell script.

[TRILL](#)

Led TRILL protocol requirements analysis, architecture and module design, as design leader, drive the TRILL overall design and participate in the process of development, it's key feature of Huawei CloudEngine series switches products.

[IP FRR](#)

Responsible for the IP FRR design. As project Leader, be responsible for project technical research, design, later stages of development and implementation of the work. Development project manager, responsible for project planning, tracking. and project delivery.

[Non-stop Routing](#)

VRP NSR prototyping project manager, responsible for organizing the prototype project technical discussion, prototype development plans, programs and decision-making work. Be the chief developer in following project development.

1

2

Page 1 of 2



CONTACTS