



PROFILE





YANG DONGDONG

Software Engineering Manager / High-performance System Architect

Name:

Yang Dongdong

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Nov, 1983

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19 years of software development experience in high-performance network systems and products, with deep expertise in Ethernet switching, routing, and security solutions. Solid skills in C, Golang programming and debugging. An expert in IP/MPLS, Ethernet protocols, and data center networking. Extensive experience in high-performance software design and architecture, particularly in Ethernet switching platforms.


I am also a full-stack software engineer with a strong focus on building scalable and efficient systems. Passionate about solving complex technical challenges and driving innovation in network technologies.

Here is my blog with some posts <http://donge.org> (Chinese) and Github: <https://github.com/donge> (commit 1500+ times in recent years average)



RESUME





EMPLOYMENT

ServiceWall.ai

2018.4 - present

Founding Engineer / Architect

Design and build the product from 0 to 1. The product is software-based, focusing on service & API level security, leveraging data and AI to analyze risks in customer services.

The software is mainly Golang stack with Docker deployment. It can be scaled out with multiple instances including infrastructure components.

Leverage many open-source projects to shorten the development cycle, including Nginx, Clickhouse, Spark, Redis, Kafka, Cube.js...

I led a 12 people R&D team, and we win 100+ customers with 15,000k+ revenue.

Juniper Networks

2013.4 - 2018.4

Software Engineering Manager

As manager of SRX platform team, responsible for CPU/BSP, Driver, Kernel, Network stack, and Forwarding engine on [SRX firewall](#).

The platform team works across different geo teams to support feature design, bug fixes, and customer support.

Our team received very positive feedback from India and headquarters, and I was promoted from MTS4 to Staff to Manager from 2013 to 2016.

The product has both hardware-based and virtualization-based solutions, with unified software architecture to deliver high-performance low-cost products. Our features include IP/MPLS Routing and Forwarding, Traffic Policer and Shaping, Kernel, Driver, and BSP maintenance.

Huawei

2006.7 - 2013.3

Senior Software Engineer

Worked in Huawei Beijing R&D for nearly 7 years in network system platform, with focus on Ethernet switching and routing technologies. As a technical leader in the team for 4 years and project leader for 1 year, responsible for module design, coding, and patents.

Chief designer and developer in TRILL project, a highlight feature in data-center switching, and received Beijing R&D designer award in 2012.

As technical leader, published 5 patents as author or co-author in network and system areas.

Optimized and tuned performance of route convergence, winning top results several times in comparison tests with competitors.

Successfully delivered nearly 10 projects as project leader and developer in our team with high quality, refer to the details in LinkedIn.

Served as a mentor and tutor in knowledge ramp-up.

PROGRAMMING SKILLS

C

95%

Golang

90%

SQL

80%

Python

70%

Javascript

60%

NETWORK SKILLS


ISIS/OSPF/BGP/LDP/PIM

Security Flow

MPLS/L3VPN/VPLS/QoS

Datacenter/TRILL

SDN/NFV



TOOLS SKILLS

Linux

GDB

Docker

Clickhouse

Spark

EDUCATION

Shenyang University of Technology


2002 - 2006

Computer Science & Technology


Got scholarship 3 times, all A grade in computer experiment course.

Startup computer association with roommates.

Participated in many programming competitions online, like ACM/ICPC, Topcoder, Baidu Astar.



PROJECT



AllServiceWallJuniperHuaweiPersonalPatent

Antibot-System

Cloud-Native, distributed HTTP Proxy and Security gateway, real-time HTTP header and body check and analysis with ML.

Daybreak

An new SRX software architecture for cloud service. Control plane OS works in nested VM, security service runs on X86/Linux with DPDK. Provide the same service as box, but all run in the virtual machine.

Forge

X86 based security platform, with VT-x, KVM/QEMU. To improve the datapath performance, leverage the DPDK, SRIOV, VirtIO tools.

Branch SRX

Branch SRX targets the enterprise market, the SRX platforms are from on-desk to 2U chassis, with MIPS architecture. It integrates the JUNOS and ScreenOS. Provide full features of security, Flow, Routing, NAT, Screen, ALG, VPN, UTM, IDP.

wFilter

wFilter is a mini full-stack personal project for blocking the webpage which contains keyword in data-base with high-performance design, with Aho-Corasick string matching and DPDK. The front-end is "MEAN"(MongoDB, ExpressJS, AngularJS, Node.js)

Bitcoin Autotrade Robot

Draft with Python and rewrite with Golang, it deploys on Raspberry PI, and do transaction automatically based on the latest price and strategy. The parameters can be configured anytime and anywhere with a mobile phone. Implement APIs for Top 5 Bitcoin transaction sites.

Disk Usage Visualized

Visualized the usage of a disk with d3.js, data is caught by a shell script.

TRILL

Led TRILL protocol requirements analysis, architecture and module design, as design leader, drive the TRILL overall design and participate in the process of development, it's key feature of Huawei CloudEngine series switches products.


IP FRR

Responsible for the IP FRR design. As project Leader, be responsible for project technical research, design, later stages of development and implementation of the work. Development project manager, responsible for project planning, tracking, and project delivery.


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CONTACTS



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