



Centre of Excellence in VLSI

# CERTIFICATE OF COMPLETION

*Celebrating*  
**10 YEARS**  
*Of Excellence*

*This is to certify that*

**Mayur Dongre**

*has successfully completed*

**VLSI SoC Design using Verilog HDL**

*Online Course on 22nd day of June 2022*



**SIVAKUMAR P R**  
CEO, MAVEN SILICON