

# BIPOLAR ANALOG INTEGRATED CIRCUIT

# $\mu$ PC842

## SINGLE SUPPLY VOLTAGE, HIGH SPEED, WIDE BAND, DUAL OPERATIONAL AMPLIFIERS

### DESCRIPTION

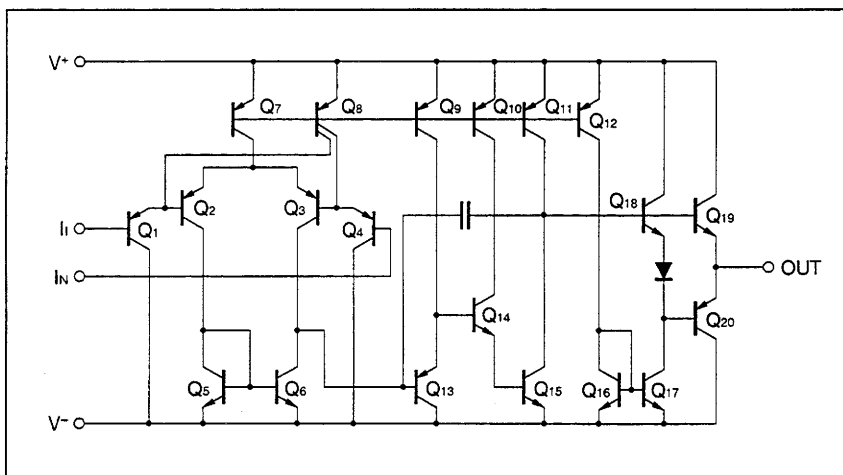
The  $\mu$ PC842 is dual high speed, wide band operational amplifier designed for single supply operation from +3 V to +32 V with low supply current drain. By using high speed PNP transistors for input and output circuits, the excellent AC performance is achieved without degrading capacitive load drive capability.

With no crossover distortion and wide output voltage range characteristics, the  $\mu$ PC842 is optimum choice for single supply AC amplifier, and active filters.

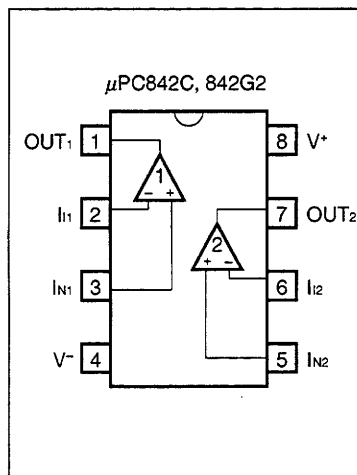
### FEATURES

- High slew rate: 7 V/ $\mu$ s TYP. ( $V^+ = +5$  V,  $V^- = \text{GND}$ )
- Wide gain band width product: 3.5 MHz TYP. ( $V^+ = +5$  V,  $V^- = \text{GND}$ )
- Wide supply voltage range: +3 V to +32 V
- Wide output voltage swing
- Common mode input voltage range includes  $V^-$
- Internal frequency compensation
- Output short circuit protection

### EQUIVALENT CIRCUIT (1/2 Circuit)



### PIN CONFIGURATION (Top View)



### ORDERING INFORMATION

Part Number	Package
$\mu$ PC842C	8-pin plastic DIP (300 mil)
$\mu$ PC842G2	8-pin plastic SOP (225 mil)

The information in this document is subject to change without notice.

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )**

Parameters		Symbol	Ratings	Unit
Voltage between $V^+$ and $V^-$ <b>Note 1</b>		$V^+ - V^-$	$-0.3$ to $+36$	V
Differential Input Voltage		$V_{ID}$	$\pm 36$	V
Input Voltage <b>Note 2</b>		$V_I$	$V^- - 0.3$ to $V^- + 36$	V
Output Voltage <b>Note 3</b>		$V_O$	$V^- - 0.3$ to $V^+ + 0.3$	V
Power Dissipation	C Package <b>Note 4</b>	$P_T$	350	mW
	G2 Package <b>Note 5</b>		440	mW
Output Short Circuit Duration <b>Note 6</b>			Indefinite	sec
Operating Ambient Temperature		$T_A$	$-40$ to $+85$	$^\circ\text{C}$
Storage Temperature		$T_{stg}$	$-55$ to $+125$	$^\circ\text{C}$

**Notes** 1. Reverse connection of supply voltage can cause destruction.

2. The input voltage should be allowed to input without damage or destruction independent of the magnitude of  $V^+$ . Either input signal should not be allowed to go negative by more than 0.3 V. The normal operation will establish when the both inputs are within the Common Mode Input Voltage Range of electrical characteristics.

3. This specification is the voltage which should be allowed to supply to the output terminal from external without damage or destructive. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The output voltage of normal operation will be the Output Voltage Swing of electrical characteristics.

4. Thermal derating factor is  $-5.0$  mW/ $^\circ\text{C}$  when operating ambient temperature is higher than  $55^\circ\text{C}$ .

5. Thermal derating factor is  $-4.4$  mW/ $^\circ\text{C}$  when operating ambient temperature is higher than  $25^\circ\text{C}$ .

6. Pay careful attention to the total power dissipation not to exceed the absolute maximum ratings, Note 4 and Note 5.

**RECOMMENDED OPERATING CONDITIONS**

Parameters	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage (Split)	$V^\pm$	$\pm 1.5$		$\pm 16$	V
Supply Voltage ( $V^- = \text{GND}$ )	$V^+$	$+3$		$+32$	V
Output Current	$I_O$			$\pm 10$	mA
Capacitive Load ( $A_v = +1$ , $R_f = 0\ \Omega$ )	$C_L$			1000	pF

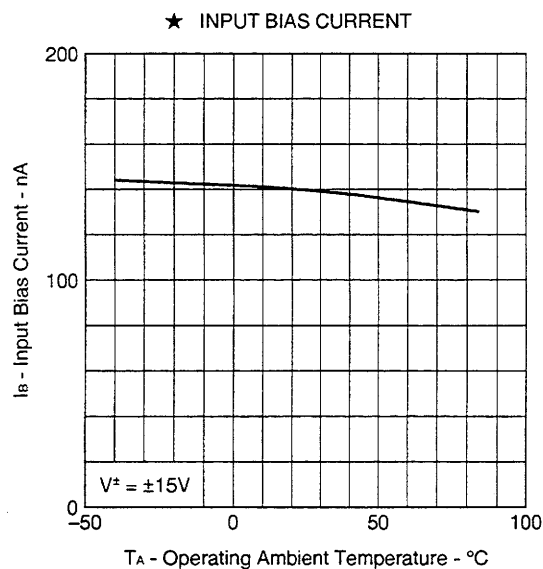
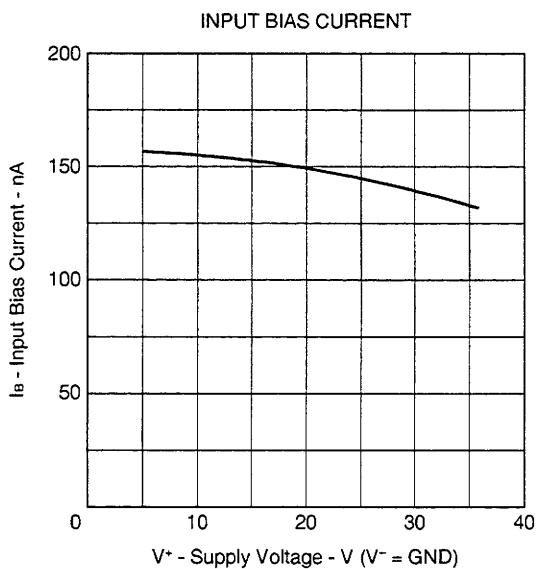
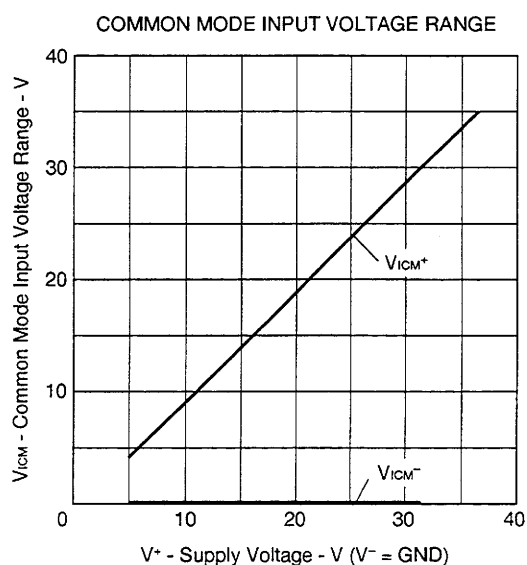
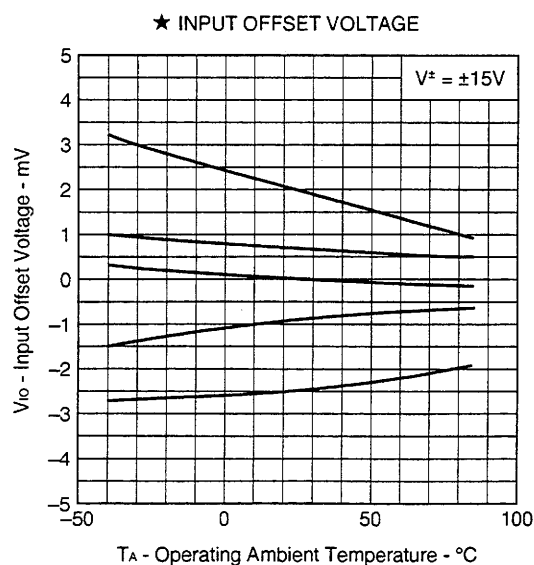
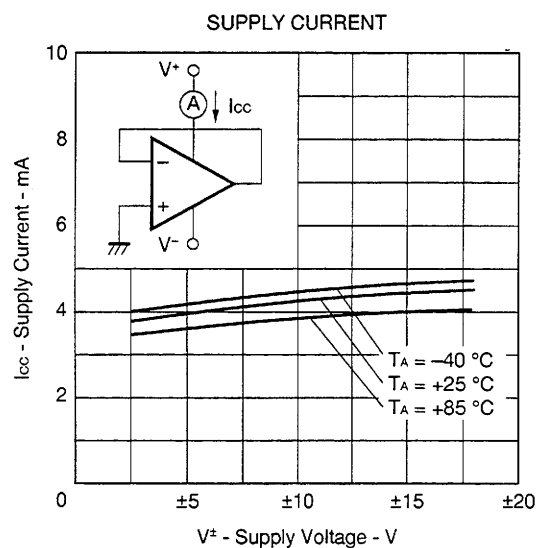
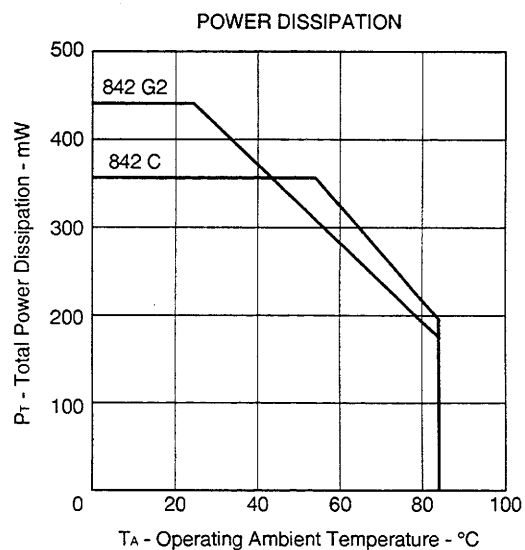
**ELECTRICAL CHARACTERISTICS ( $T_A = 25\text{ }^\circ\text{C}$ ,  $V^\pm = \pm 15\text{ V}$ )**

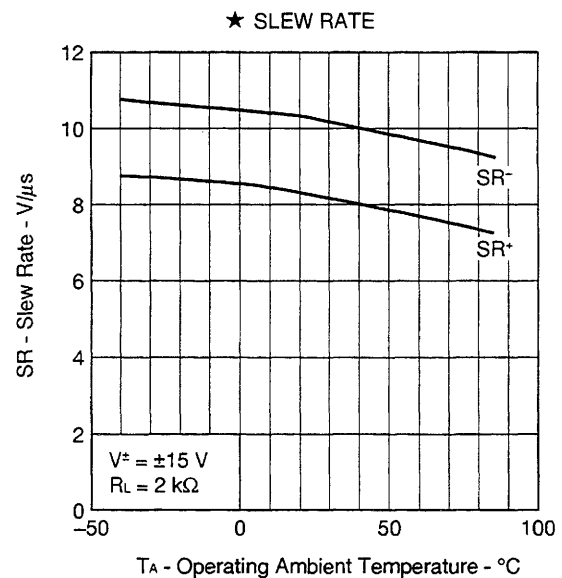
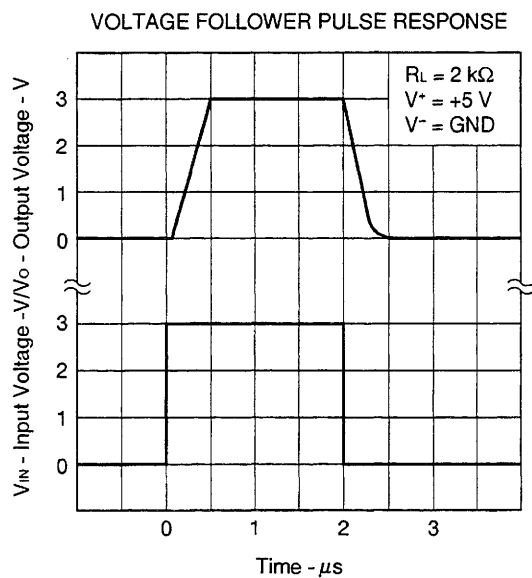
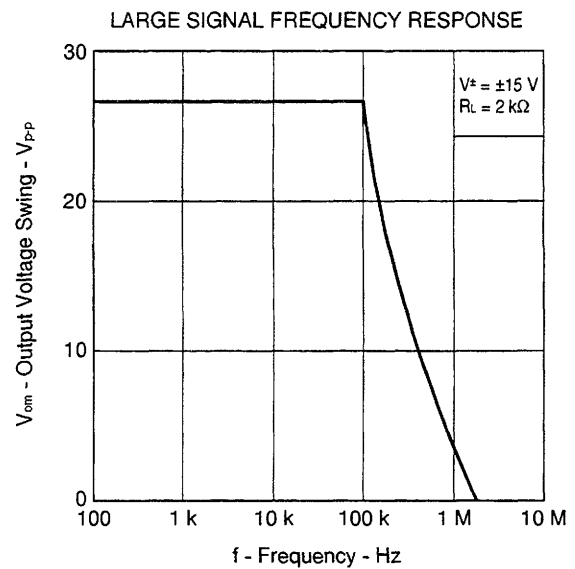
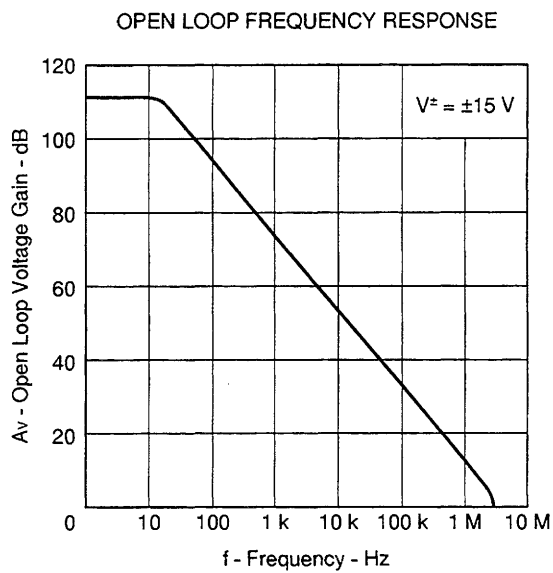
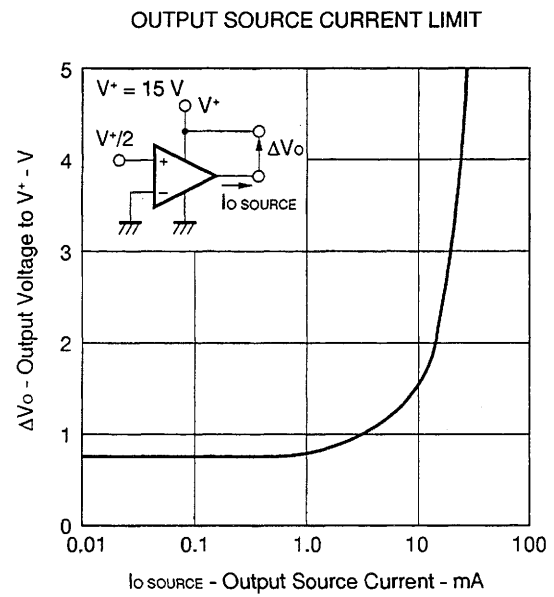
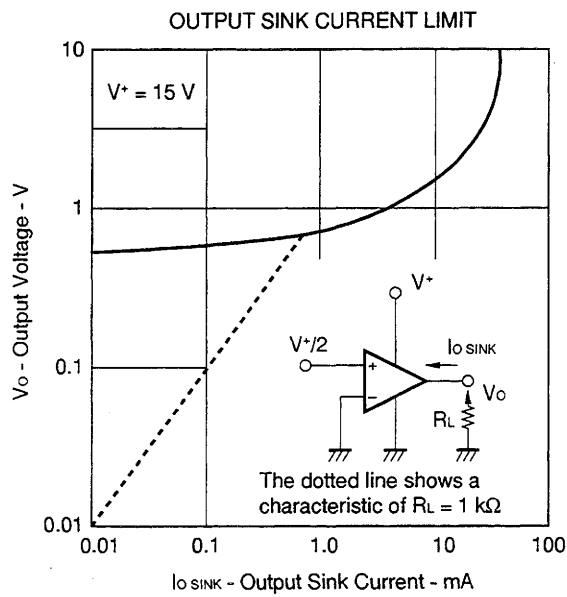
Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input Offset Voltage	$V_{io}$			$\pm 1.0$	$\pm 4.5$	mV
Input Offset Current	$I_{io}$			$\pm 6$	$\pm 75$	nA
Input Bias Current <b>Note 7</b>	$I_b$			140	500	nA
Large Signal Voltage Gain	$A_v$	$R_L \geq 2\text{ k}\Omega$ , $V_o = \pm 10\text{ V}$	25	300		V/mV
Supply Current	$I_{cc}$	$I_o = 0\text{ A}$ , Both Amplifiers		4.3	5.5	mA
Common Mode Rejection Ratio	CMR		70	86		dB
Supply Voltage Rejection Ratio	SVR		70	93		dB
Output Voltage Swing	$V_{om}$	$R_L \geq 10\text{ k}\Omega$	$\pm 13.7$	+14 -14.3		V
Output Voltage Swing	$V_{om}$	$R_L \geq 2\text{ k}\Omega$	$\pm 13.5$			V
Common Mode Input Voltage Range	$V_{icm}$		$V^-$		$V^+ - 1.8$	V
Slew Rate (Rise)	SR	$A_v = 1$ , $R_L \geq 2\text{ k}\Omega$		8.5		V/ $\mu$ s
Gain Band Width Product	GBW	$f_o = 100\text{ kHz}$		3.5		MHz
Channel Separation		$f = 20\text{ Hz}$ to $20\text{ kHz}$		120		dB

**ELECTRICAL CHARACTERISTICS ( $T_A = 25\text{ }^\circ\text{C}$ ,  $V^+ = 5\text{ V}$ ,  $V^- = \text{GND}$ )**

Parameters	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input Offset Voltage	$V_{io}$			$\pm 1.0$	$\pm 5$	mV
Input Offset Current	$I_{io}$			$\pm 6$	$\pm 75$	nA
Input Bias Current <b>Note 7</b>	$I_b$			160	500	nA
Large Signal Voltage Gain	$A_v$	$R_L \geq 2\text{ k}\Omega$	25	300		V/mV
Supply Current	$I_{cc}$	$I_o = 0\text{ A}$ , All Amplifiers		3.3	4.5	mA
Common Mode Rejection Ratio	CMR		70	80		dB
Supply Voltage Rejection Ratio	SVR		70	95		dB
Output Voltage Swing	$V_{om}$	$R_L \geq 2\text{ k}\Omega$ (Connect to GND)	3.7 0	4.0 0		V
Common Mode Input Voltage Range	$V_{icm}$		0		$V^+ - 1.8$	V
Output Current (SOURCE)	$I_{o\text{ SOURCE}}$	$V^+_{IN} = +1\text{ V}$ , $V^-_{IN} = 0\text{ V}$	10	30		mA
Output Current (SINK)	$I_{o\text{ SINK}}$	$V^+_{IN} = 0\text{ V}$ , $V^-_{IN} = +1\text{ V}$	10	30		mA
Slew Rate (Rise)	SR			7		V/ $\mu$ s

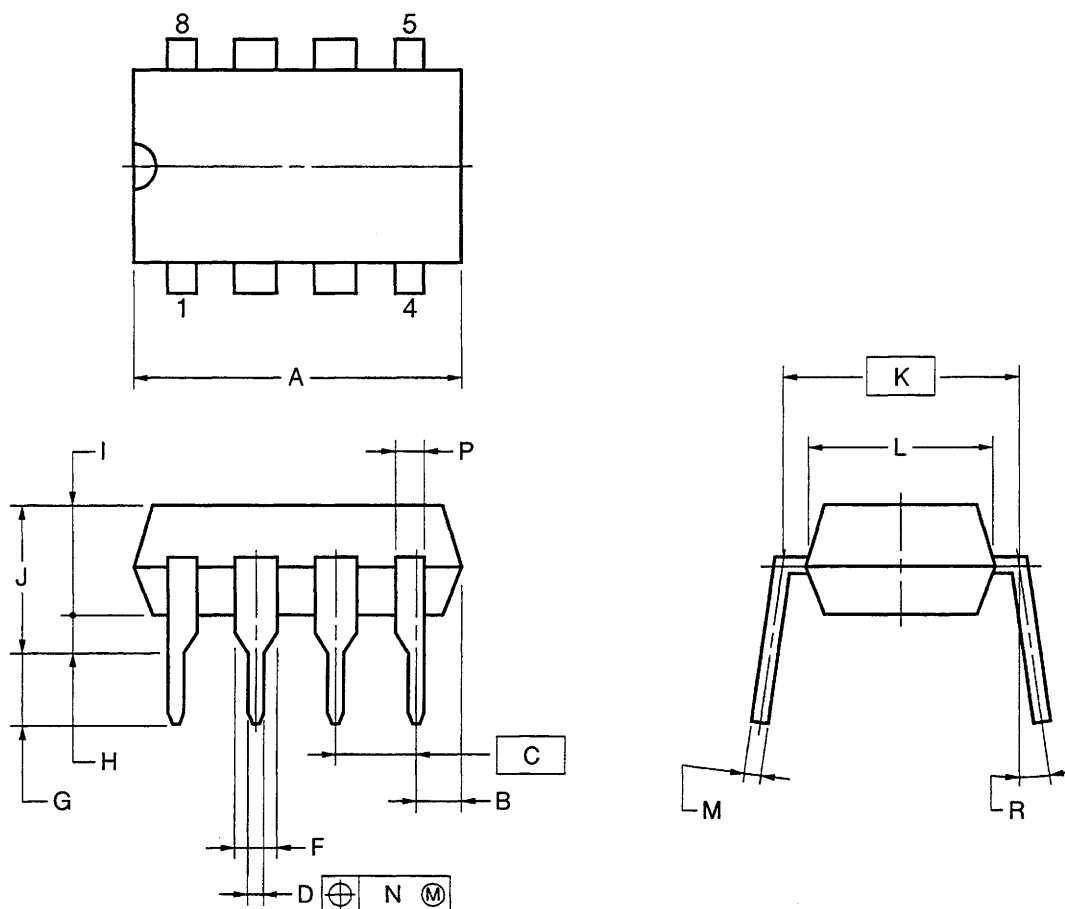
**Notes 7.** Input bias currents flow out from IC. Because each currents are base current of PNP-transistor on input stage.

TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ , TYP.)



## PACKAGE DRAWINGS

## 8PIN PLASTIC DIP (300 mil)



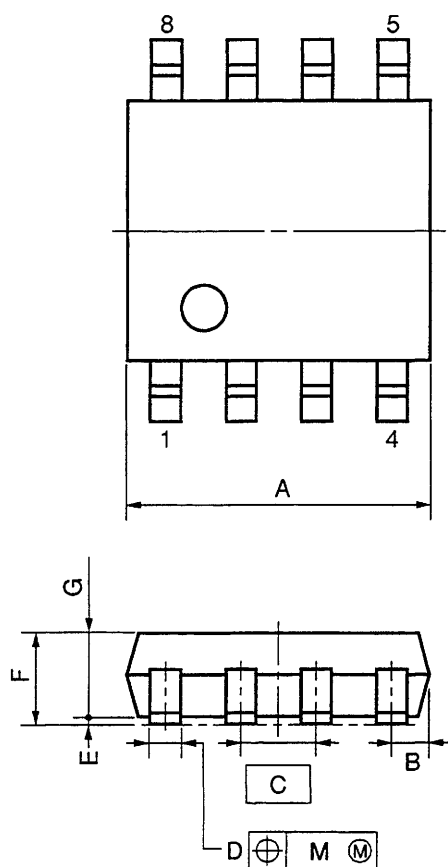
## NOTES

- 1) Each lead centerline is located within 0.25 mm (0.01 inch) of its true position (T.P.) at maximum material condition.
- 2) Item "K" to center of leads when formed parallel.

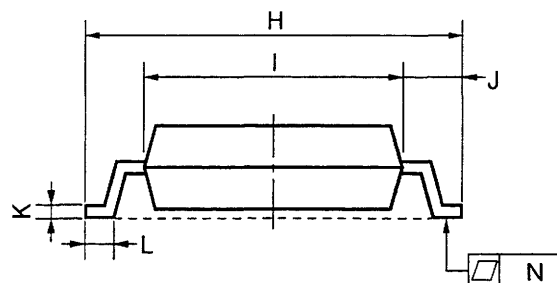
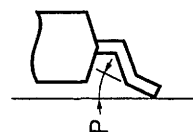
ITEM	MILLIMETERS	INCHES
A	10.16 MAX.	0.400 MAX.
B	1.27 MAX.	0.050 MAX.
C	2.54 (T.P.)	0.100 (T.P.)
D	0.50±0.10	0.020 <sup>+0.004</sup> <sub>-0.005</sub>
F	1.4 MIN.	0.055 MIN.
G	3.2±0.3	0.126±0.012
H	0.51 MIN.	0.020 MIN.
I	4.31 MAX.	0.170 MAX.
J	5.08 MAX.	0.200 MAX.
K	7.62 (T.P.)	0.300 (T.P.)
L	6.4	0.252
M	0.25 <sup>+0.10</sup> <sub>-0.05</sub>	0.010 <sup>+0.004</sup> <sub>-0.003</sub>
N	0.25	0.01
P	0.9 MIN.	0.035 MIN.
R	0~15°	0~15°

P8C-100-300B,C-1

# 8 PIN PLASTIC SOP (225 mil)



detail of lead end



## NOTE

Each lead centerline is located within 0.12 mm (0.005 inch) of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS	INCHES
A	5.37 MAX.	0.212 MAX.
B	0.78 MAX.	0.031 MAX.
C	1.27 (T.P.)	0.050 (T.P.)
D	$0.40^{+0.10}_{-0.05}$	$0.016^{+0.004}_{-0.003}$
E	$0.1 \pm 0.1$	$0.004 \pm 0.004$
F	1.8 MAX.	0.071 MAX.
G	1.49	0.059
H	$6.5 \pm 0.3$	$0.256 \pm 0.012$
I	4.4	0.173
J	1.1	0.043
K	$0.15^{+0.10}_{-0.05}$	$0.006^{+0.004}_{-0.002}$
L	$0.6 \pm 0.2$	$0.024^{+0.008}_{-0.009}$
M	0.12	0.005
N	0.10	0.004
P	$3^{\circ} + 7^{\circ}_{-3^{\circ}}$	$3^{\circ} + 7^{\circ}_{-3^{\circ}}$

S8GM-50-225B-4

## RECOMMENDED SOLDERING CONDITIONS

When soldering this product, it is highly recommended to observe the conditions as shown below. If other soldering processes are used, or if the soldering is performed under different conditions, please make sure to consult with our sales offices.

For more details, refer to our document “Semiconductor Device Mounting Technology Manual” (C10535E).

### Type of Surface Mount Device

#### μPC842G2: 8-pin plastic SOP (225 mil)

Process	Conditions	Symbol
Infrared ray reflow	Peak temperature: 230 °C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210 °C or higher), Maximum number of reflow processes: 1 time.	IR30-00-1
Vapor Phase Soldering	Peak temperature: 215 °C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200 °C or higher), Maximum number of reflow processes: 1 time.	VP15-00-1
Wave Soldering	Solder temperature: 260 °C or below, Flow time: 10 seconds or less, Maximum number of flow processes: 1 time, Pre-heating temperature: 120 °C or below (Package surface temperature).	WS60-00-1
Partial heating method	Pin temperature: 300 °C or below, Heat time: 3 seconds or less (Per each side of the device).	—

**Caution** Apply only one kind of soldering condition to a device, except for “partial heating method”, or the device will be damaged by heat stress.

### Type of Through-hole Device

#### μPC842C: 8-pin plastic DIP (300 mil)

Process	Conditions
Wave soldering (only to leads)	Solder temperature: 260 °C or below, Flow time: 10 seconds or less.
Partial heating method	Pin temperature: 300 °C or below, Heat time: 3 seconds or less (per each lead).

**Caution** For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.



**REFERENCE DOCUMENTS**

QUALITY GRADES ON NEC SEMICONDUCTOR DEVICES	C11531E
SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL	C10535E
IC PACKAGE MANUAL	C10943X
GUIDE TO QUALITY ASSUARANCE FOR SEMICONDUCTOR DEVICES	MEI-1202
SEMICONDUCTORS SELECTION GUIDE	X10679E
NEC SEMICONDUCTOR DEVICE RELIABILITY/	IEI-1212
QUALITY CONTROL SYSTEM - STANDARD LINEAR IC	

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Anti-radioactive design is not implemented in this product.