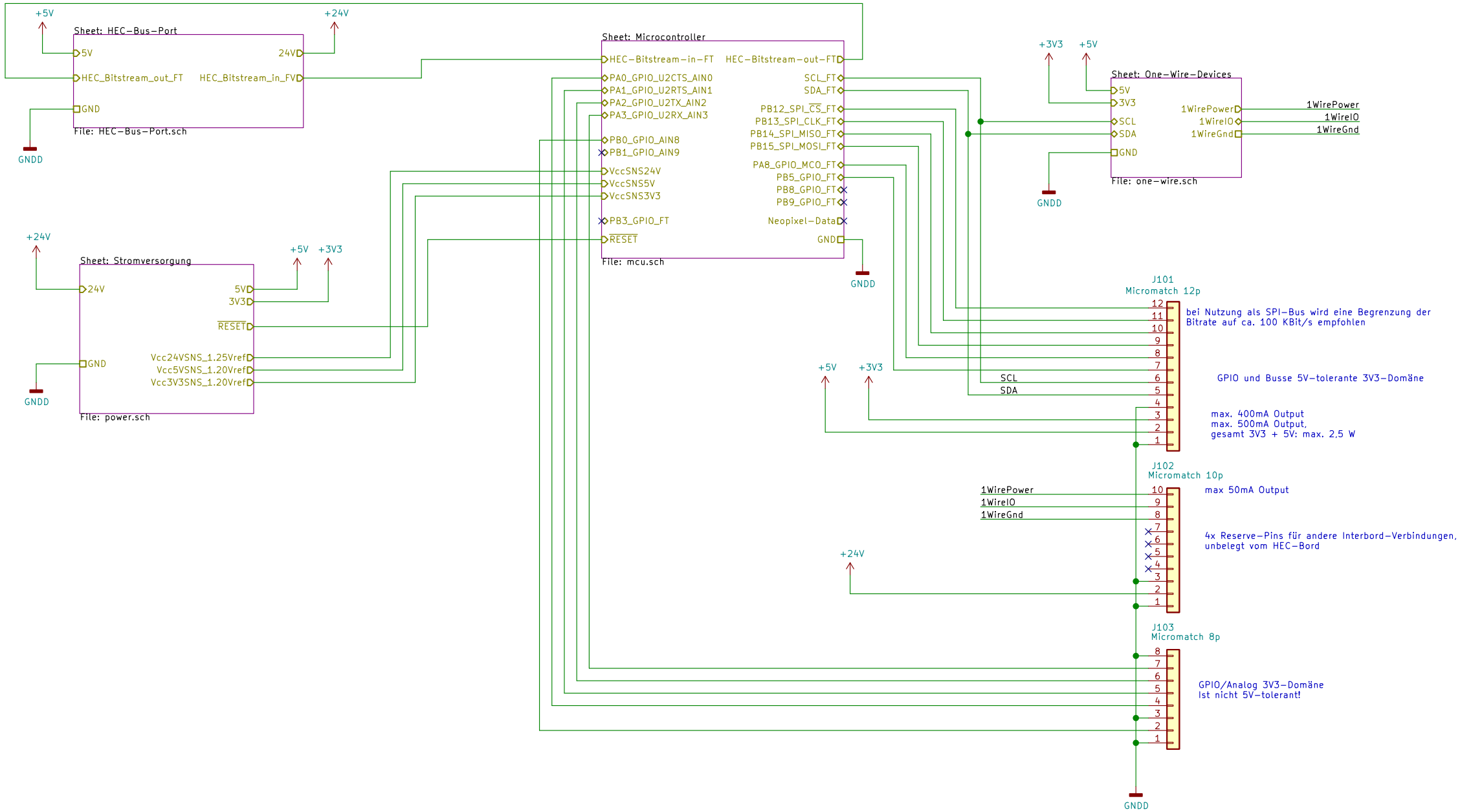
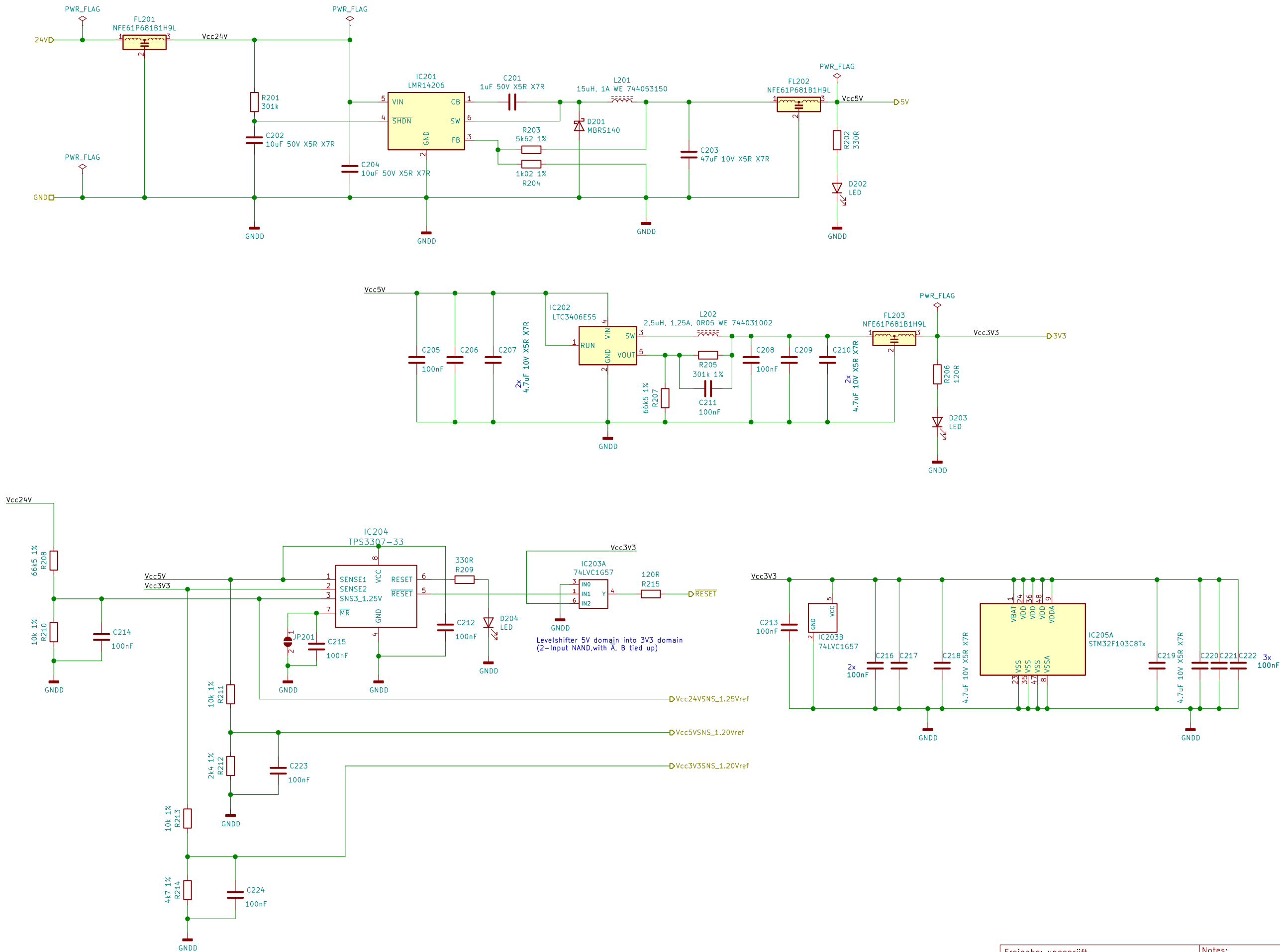


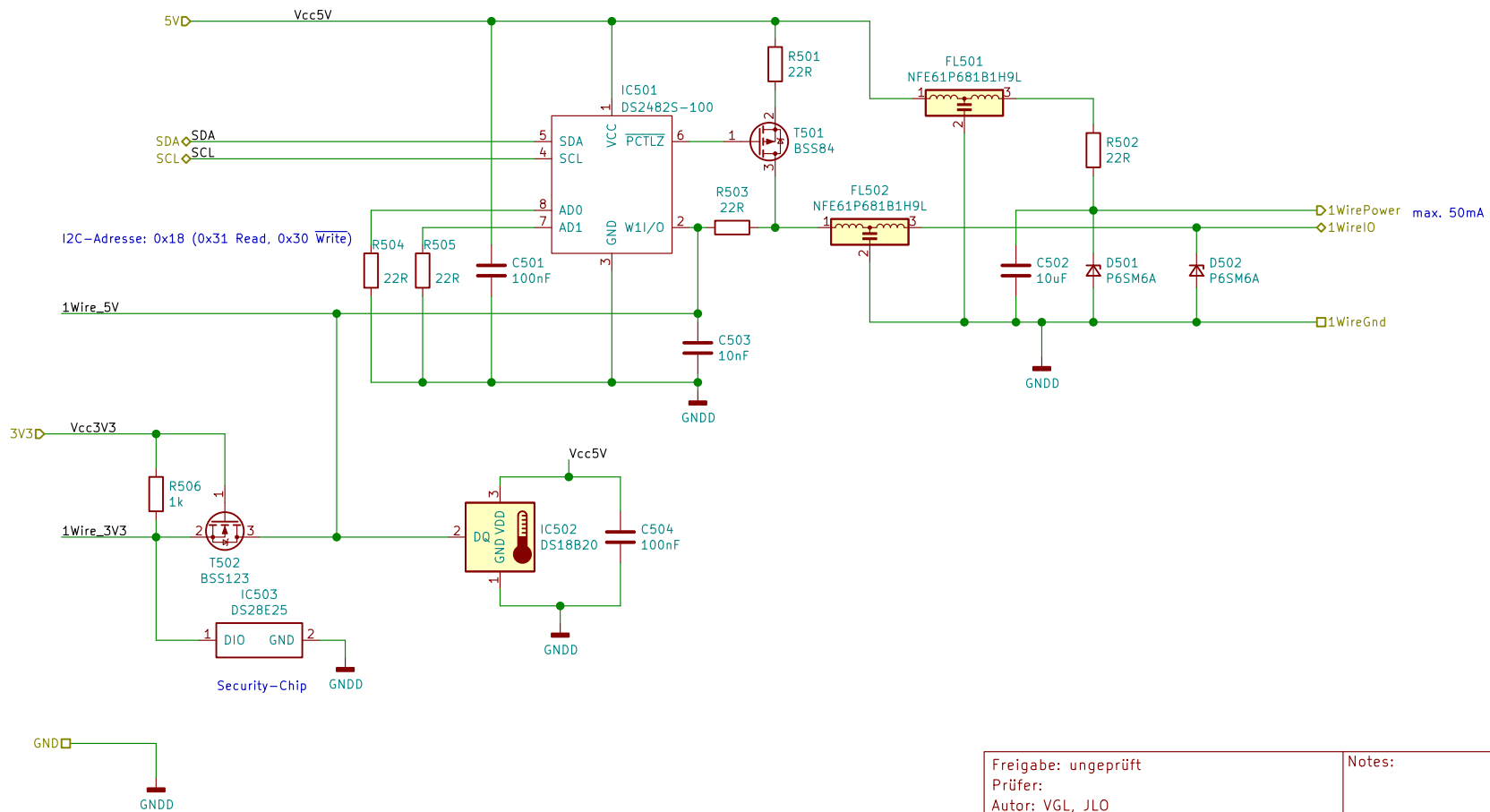
TODO:
o TODO's aller Schaltplan-Blätter abarbeiten



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Title: Marienkaefer HEC-Basis-Bord: One-Wire-Interface

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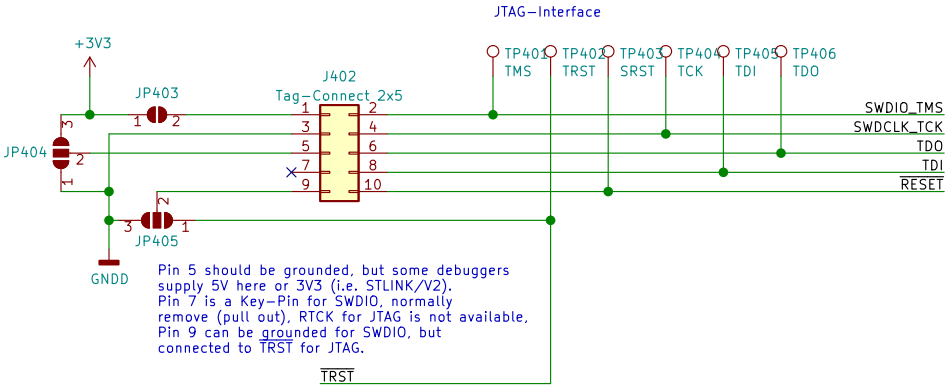
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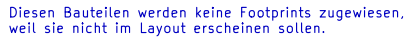
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Autor: VGL, JLO		
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Freigabe: ungeprüft	Notes:	
Prüfer:		
Autor: VGL, JLO		
Title: Marienkaefer HEC–Basis–Bord: Bus–Anschaltung		
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