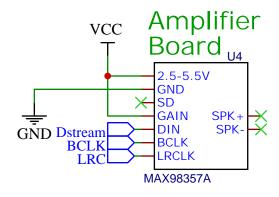
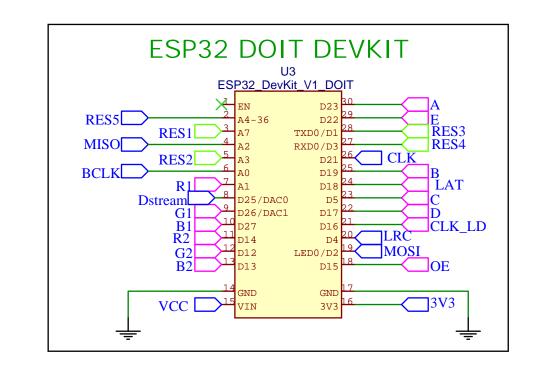
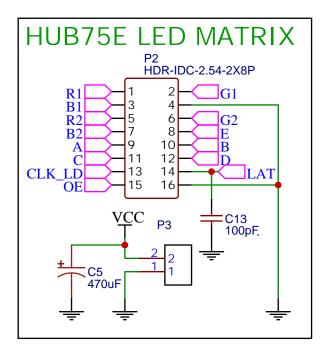


SD	SPI
DAT2	X
CD/DAT3	CS
CMD	DI
VDD	VDD
CLK	SCLK
VSS	VSS
DAT0	DO
DAT1	Х
	DAT2 CD/DAT3 CMD VDD CLK VSS DAT0









TITLE: Talking Clock			REV: 4.1	
(Company:	The Electronic Engineer	Sheet: 1/1

EasyEDA

Date: 2022-04-03 Drawn By: Mark Donners