Chimei-Innolux Corporation

BT140GW01 V.9 LCD MODULE SPECIFICATION

( ) Preliminary Specification
(●) Final Specification

Customer	Checked & Approved by
ASUS	

Approved by	Checked by	Prepared by
MKT	PD	PM
		Tony Ouyang

Date: 2010/07/16

Chimei-Innolux Corporation

No.160 Kesyue Rd., Chu-Nan Site, Hsinchu Science Park,

Chu-Nan 350, Miao-Li County, Taiwan

Tel: 886-37-586000   Fax: 886-37-586060

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Record of Revision

Version	Revise Date	Page	Content
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1. General Specifications

NO.	Item	Specification	Unit
1	Display resolution (pixel)	1366(H) X 768(V), HD resolution	
2	Active area	309.40(H) X 173.95(V)	mm
3	Screen size	14.0 inches diagonal	Inches
4	Pixel pitch	0.2265(H) X 0.2265(V)	mm
5	Color configuration	Stripe	
6	Overall dimension	324(W) X 192.5(H) X 5.2(D) (max)	mm
7	Weight	350Max.	Grams
8	Surface treatment	Glare, 3H	
9	Input color signal	6 bit LVDS	
10	Display colors	262K (6 bit)	
11	Optimum viewing direction	6 o'clock	
12	Backlight	W-LED	
13	Glass thickness	0.5	mm
14	LED life time with LCM	12,000 (min.), T = 25°C	Hours
15	RoHS	RoHS compliance	

2. Electrical Specifications
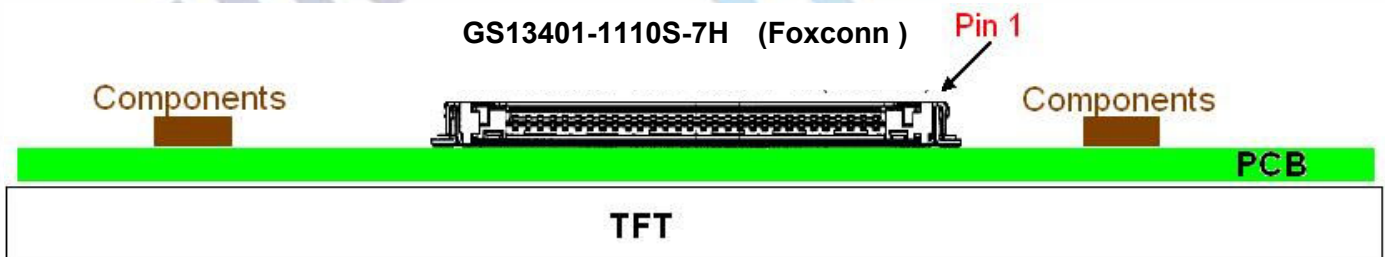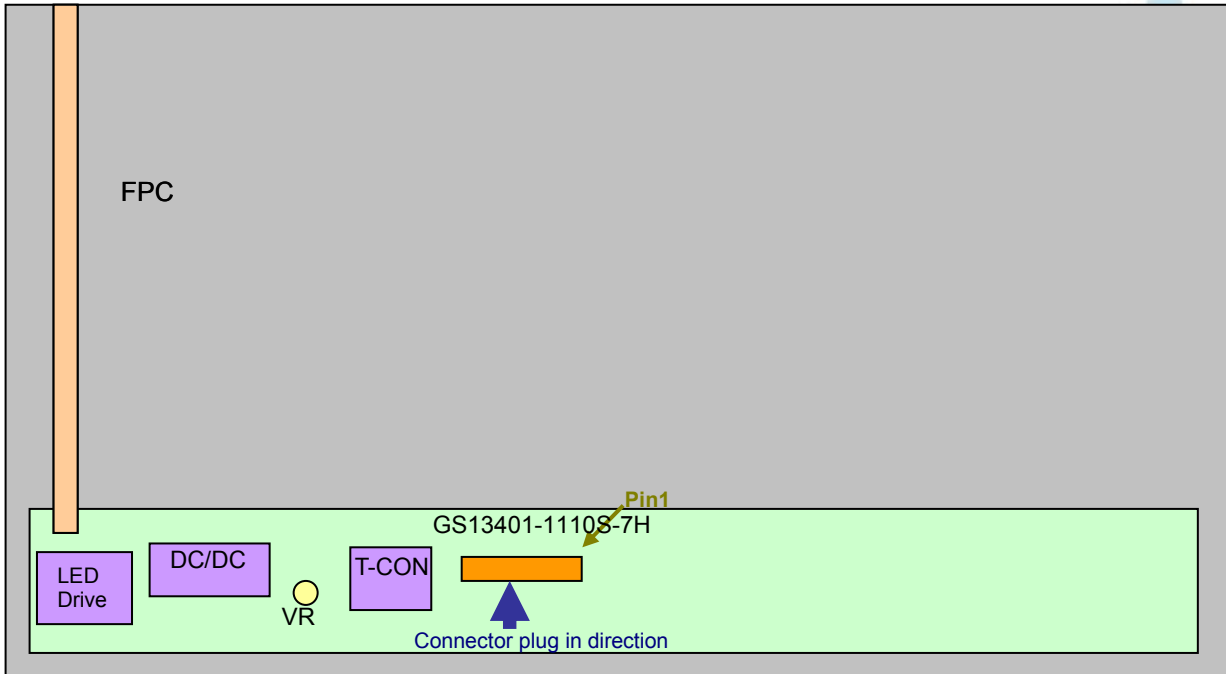
2-1 Pin Assignment

a. Panel connector

Connector Part No: GS13401-1110S-7H (Foxconn)

User's connector Part No: 20453-040T-12(I-PEX) or equivalent

Pin No	Symbol	Description	Remark
1	NC	No connection (Reserve)	
2	V_{CC}	Power Supply (+3.3V)	
3	V_{CC}	Power Supply (+3.3V)	
4	V_{EDID}	DDC Power +3.3V	
5	NC	No connection (Reserve)	
6	Clk_{EDID}	DDC Clock	
7	$DATA_{EDID}$	DDC Data	
8	Rxin0-	Differential Data Input	R0~R5,G0
9	Rxin0+	Differential Data Input	
10	GND	Ground	
11	Rxin1-	Differential Data Input	G1~G5,B0,B1
12	Rxin1+	Differential Data Input	
13	GND	Ground	
14	Rxin2-	Differential Data Input	B2~B5,DE,Hsync,Vsync
15	Rxin2+	Differential Data Input	
16	GND	Ground	
17	CLK-	Differential Clock Input	
18	CLK+	Differential Clock Input	
19	GND	Ground	
20	NC	No connection (Reserve)	
21	NC	No connection (Reserve)	
22	GND	Ground	
23	NC	No connection (Reserve)	
24	NC	No connection (Reserve)	
25	GND	Ground	
26	NC	No connection (Reserve)	
27	NC	No connection (Reserve)	
28	GND	Ground	
29	NC	No connection (Reserve)	
30	NC	No connection (Reserve)	
31	LED_GND	LED Ground	
32	LED_GND	LED Ground	
33	LED_GND	LED Ground	
34	NC	No connection (Reserve)	
35	LED_PWM	PWM dimming signal input	
36	LED_EN	LED enable pin (3.3V)	
37	NC	No connection (Reserve)	
38	V_LED	LED power supply 6V~21V	
39	V_LED	LED power supply 6V~21V	
40	V_LED	LED power supply 6V~21V	

b. General block diagram （Rear-side）

FPC

GS13401-1110S-7H

Pin1

LED Drive

DC/DC

VR

T-CON

Connector plug in direction

GS13401-1110S-7H **(Foxconn)**

Pin 1

Components

Components

PCB

TFT

2-2. Absolute Maximum Ratings

Parameter	Symbol	Values		Unit	Remark
		Min.	Max.		
Power input voltage	V_{CC}	- 0.3	4.0	V	At 25℃
Signal input voltage	V_{IN}	- 0.3	4.0	V	At 25℃
LED input voltage	V_{LED}	- 0.3	30	V	At 25℃
Operating temperature	T_{OP}	0	50	℃	Note 1
Storage temperature	T_{ST}	- 20	60	℃	Note 2
Re-screw		-	5	Times	
Assured torque at side mount		-	2	kgf.cm	

Note 1: The relative humidity must not exceed 90%, non-condensing at temperatures of 40℃ or less. At temperatures greater than 40℃, the wet bulb temperature must not exceed 39℃.

Note 2: The unit should not be exposed to corrosive chemicals.

Relative Humidity (%RH)
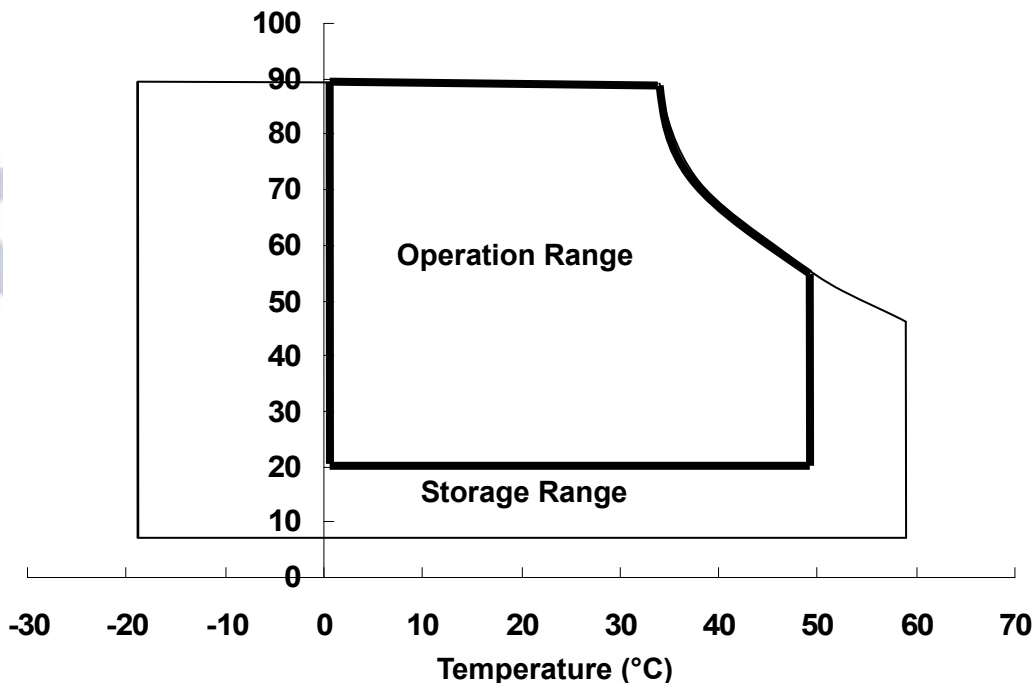
2-3. Electrical Characteristics

a. Typical operating conditions

Item		Symbol	Min.	Typ.	Max.	Unit	Remark
Power input voltage		V_{CC}	3	3.3	3.6	V	
Permissive power input ripple		V_{RF}	-	-	0.1	V	
Power input current		I_{CC}	-	300	330	mA	Note 1
Power consumption		P_C	-		1	Watts	Note 1
LVDS interface	Differential input high threshold voltage	V_{LVTH}	-	-	+100	mV	LVDS interface
	Differential input low threshold voltage	V_{LVTL}	-100	-	-	mV	
	Common input voltage	V_{LVC}	1.0	1.2	1.4	V	
	Terminating resistor	R_T	90	100	110	ohm	
Rush current		I_{Rush}	-	-	1.5	A	Note 3
LED rush current		$I_{LED-Rush}$	-	-	3.0	A	Note 4

Note 1: The specified input current and power consumption are under the V_{cc} =3.3 V, 25°C, f_V=60Hz (frame frequency) condition whereas mosaic pattern is displayed.
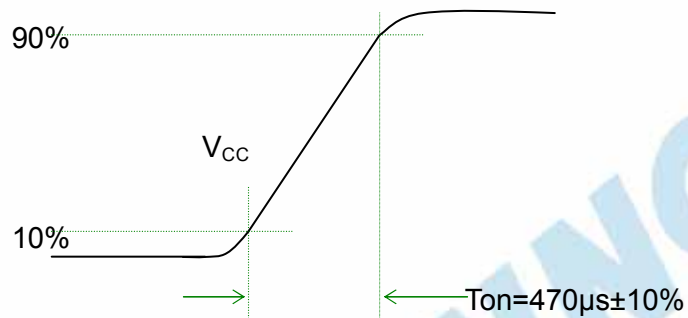
Note 2: LVDS waveform diagram

Rxin+/CLK+

Rxin-/CLK-

V_{LVTH}

V_{LVTL}

V_{LVC}

GND
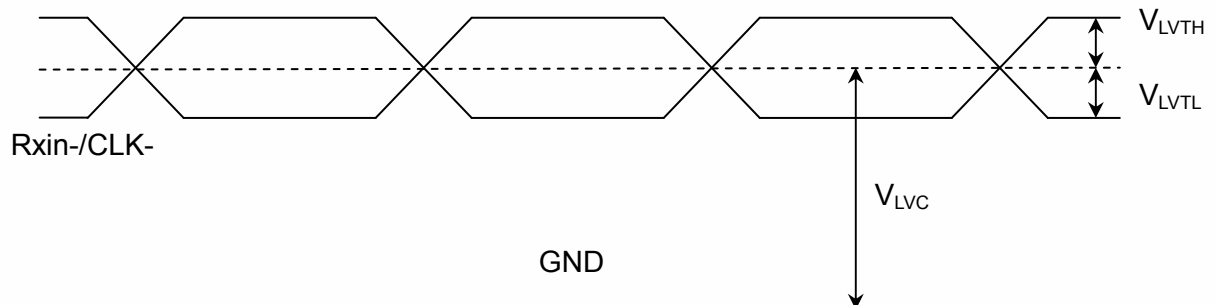
Note 3: Test condition

(1) Pattern: Black pattern

(2) V_{CC} = 3.3 V, V_{CC} rising time = 470 μs ± 10%

90%

V_{CC}

10%

Ton=470µs±10%

(3) Test circuit

3.3 V

M1
2SK1059

V_{CC} (LCD input)

R1
47K

FUSE

C1
1uF

Control signal
(High to low)

R2
1K

M2
2SK1399

12V

R3
47K

C2
10000pF

C3
1uF
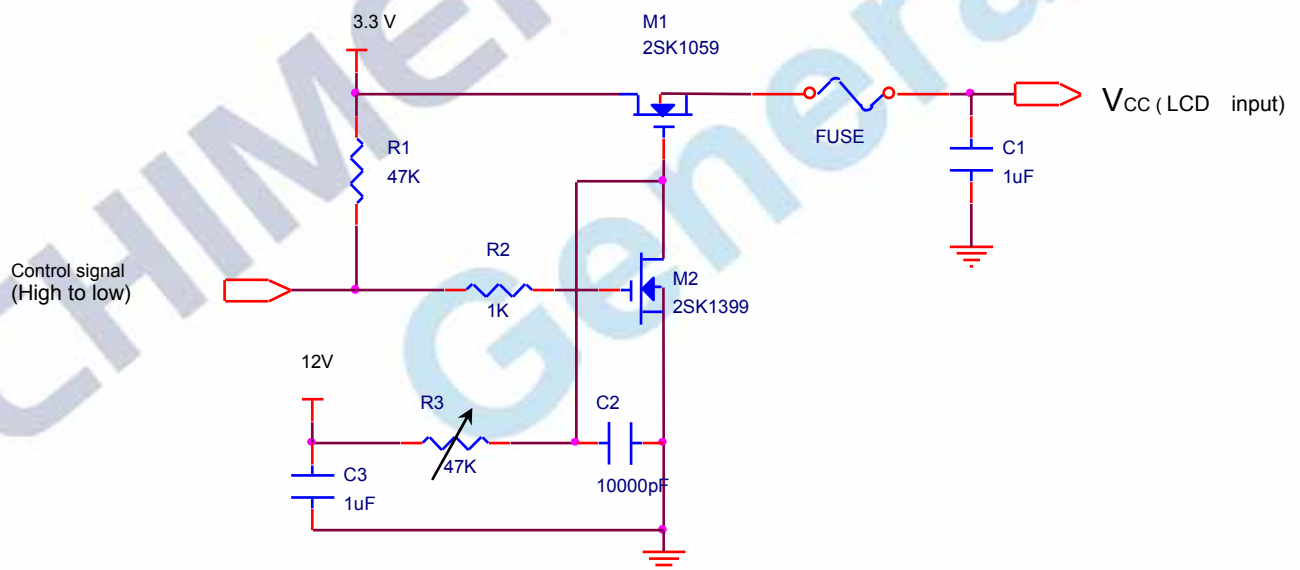
Note 4: Test condition

    (1) Pattern: LED duty 100％

    (2) V_LED = 12.0V, V_LED rising time = 470 μs ± 10%

90%

V_LED

10%

Ton=470µs±10%
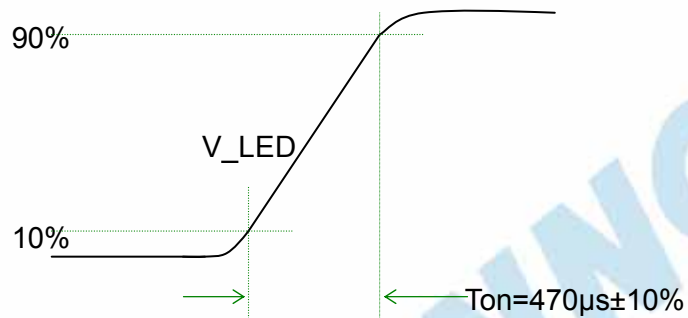
    (3) Test circuit
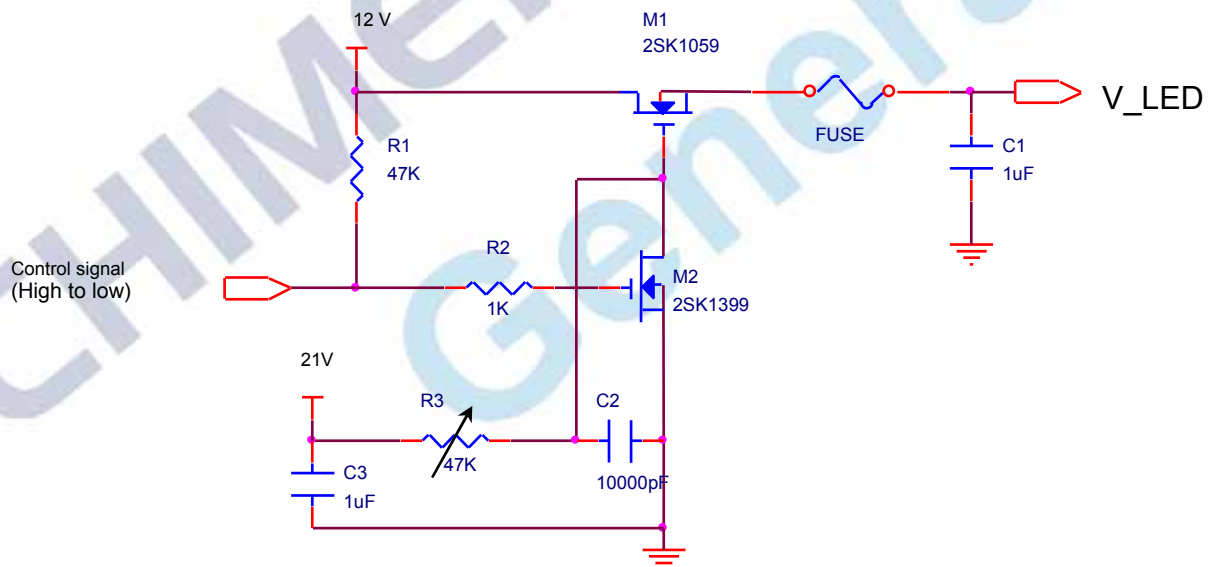
b. Power sequence

Power supply for LCD, V_{CC}

90%

10%

0V

90%

10%

T_1 T_2

T_5 T_6 T_7

Interface data signal, V_i
(LVDS signal of transmitter)

0V

Valid Data

T_3

T_4

Backlight on/off

OFF

Backlight On

OFF
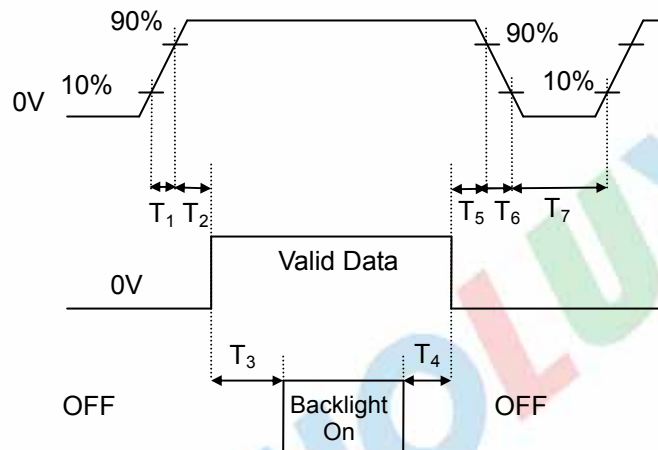
Power sequence timing table

Parameter	Value			Units
	Min.	Typ.	Max.	
T_1	0.5	-	10	ms
T_2	0	-	50	ms
T_3	200	-	-	ms
T_4	200	-	-	ms
T_5	0	-	50	ms
T_6	0.5	-	10	ms
T_7	400	-	-	ms

c. Display color vs. input data signals

Signal Name	Description	Remark
R5	Red Data 5 (MSB)	Red-pixel data. Each red pixel's brightness data
R4	Red Data 4	consists of these 6 bits pixel data.
R3	Red Data 3	
R2	Red Data 2	
R1	Red Data 1	
R0	Red Data 0 (LSB)	
	Red-pixel Data	
G5	Green Data 5 (MSB)	Green-pixel data. Each green pixel's brightness
G4	Green Data 4	data consists of these 6 bits pixel data.
G3	Green Data 3	
G2	Green Data 2	
G1	Green Data 1	
G0	Green Data 0 (LSB)	
	Green-pixel Data	
B5	Blue Data 5 (MSB)	Blue-pixel data. Each blue pixel's brightness data
B4	Blue Data 4	consists of these 6 bits pixel data.
B3	Blue Data 3	
B2	Blue Data 2	
B1	Blue Data 1	
B0	Blue Data 0 (LSB)	
	Blue-pixel Data	

Signal for 1 DCLK cycle (t_{CLK})
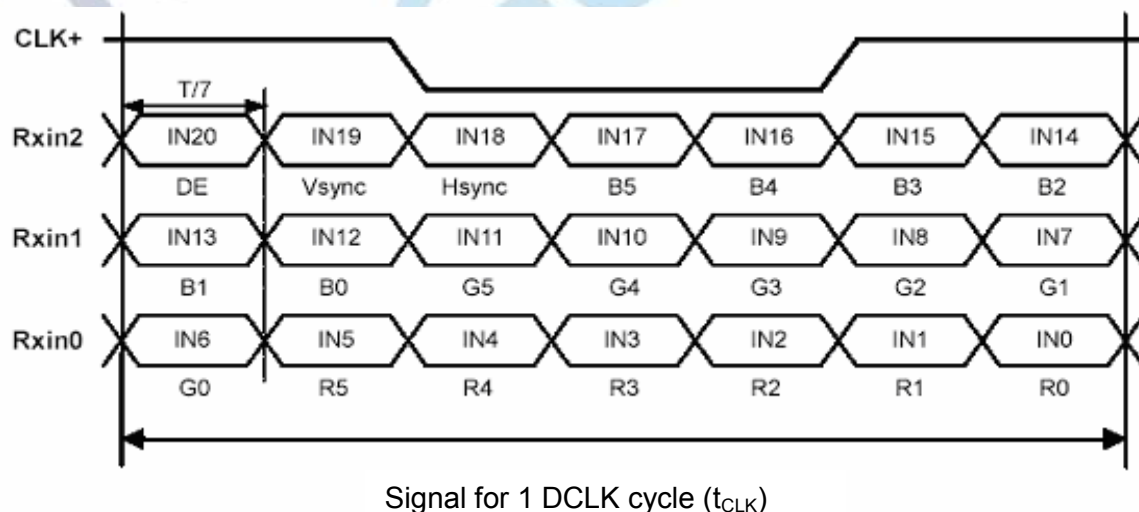
d. Input signal timing

Timing table

Description	Symbol	Min	Typ	Max	Unit
Frame rate	--	40	60	--	Hz
Clock freq.	$1/t_{CLK}$	45	71	85	MHz
Line cycle time	t_H	1400	1498	1800	t_{CLK}
Line width-active	t_{Hd}	1366	1366	1366	t_{CLK}
Frame cycle time	t_V	780	790	900	t_H
V width-active	t_{Vd}	768	768	768	t_H
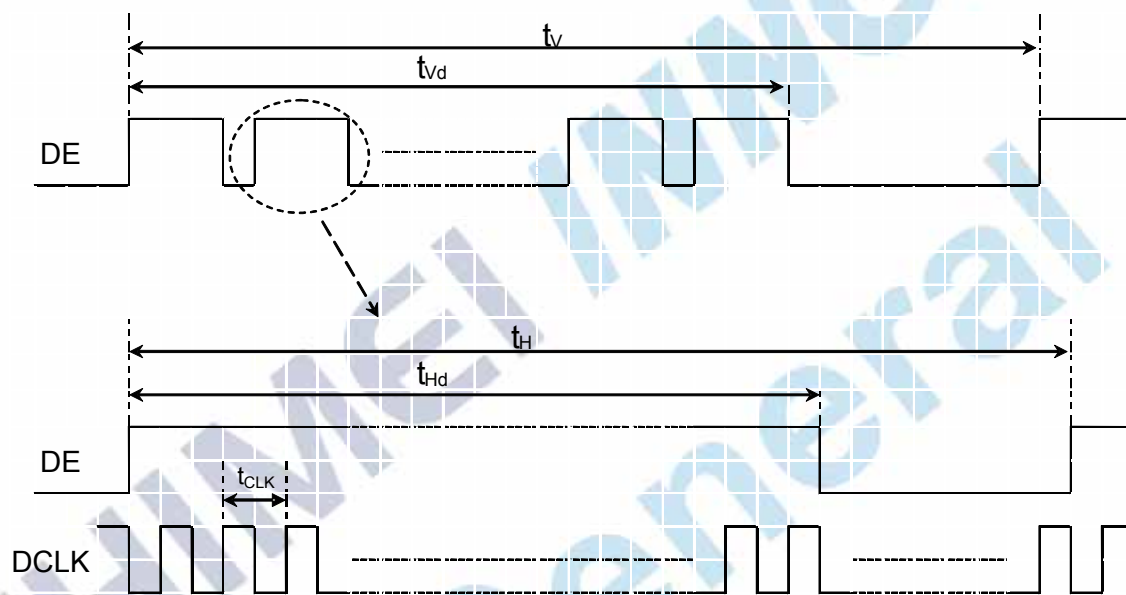
e. Display position

D(1, 1)	D(2, 1)	……	D(683, 1)	……	D(1365, 1)	D(1366, 1)
D(1, 2)	D(2, 2)	……	D(683, 2)	……	D(1365, 2)	D(1366, 2)
⋮		……	⋮	……	⋮	⋮
D(1, 384)	D(2, 384)	……	D(683, 384)	……	D(1365, 384)	D(1366, 384)
⋮		……	⋮	……	⋮	⋮
D(1, 767)	D(2, 767)	……	D(683, 767)	……	D(1365, 767)	D(1366, 767)
D(1, 768)	D(2, 768)	……	D(683, 768)	……	D(1365, 768)	D(1366, 768)

f. Backlight driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED forward voltage	V_F	3	3.2	3.4	V_{rms}	T = 25°C
LED forward current	I_F		20		mA_{rms}	T = 25°C
LED power consumption	P_{LED}		2.8	3	W	T = 25°C
	P_{LED-G}			1.2	W	Note 1
Input PWM frequency	F_{PWM}	190	1000	2000	Hz	T = 25°C
Duty ratio	-	5		100	%	Note 2
LED life time (LED only)	-	15,000			Hr	T = 25°C , Note 3

Note 1: The BL power consumption @100 nits with full white pattern under the V_{cc} =3.3 V, 25 °C, f_V=60Hz (frame frequency) condition
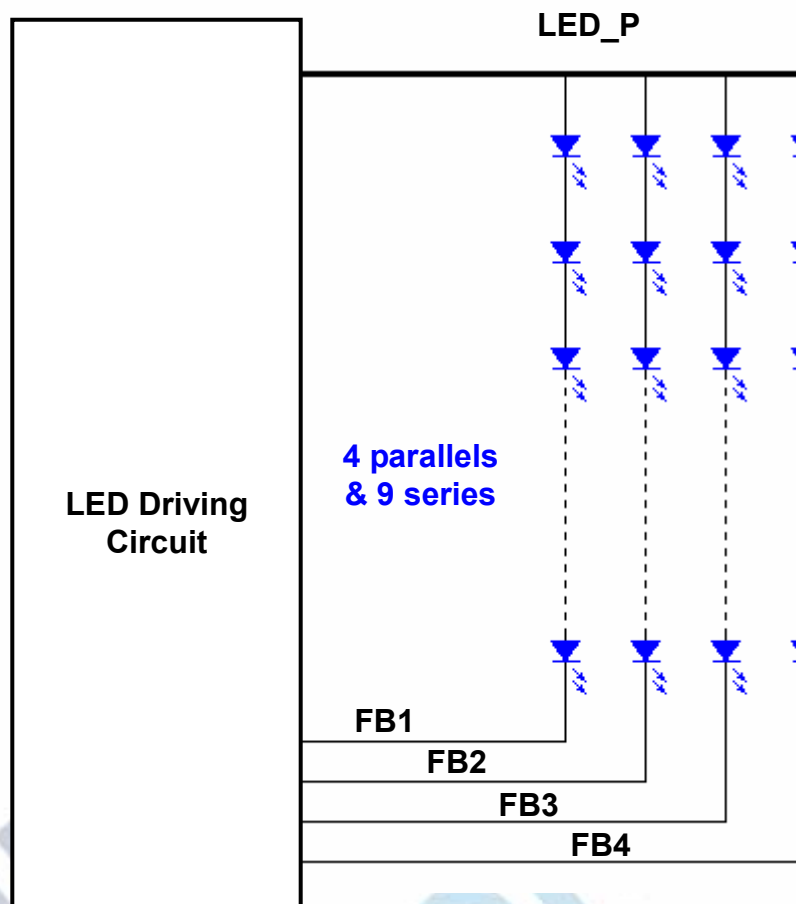
Note 2: PWM duty ratio linearity guarantees 10~100%.

Note 3: LED life time definition is brightness decrease to 50% of initial or abnormal lighting.

g. Module function block

LVDS DATA BUS ➡ mini-LVDS & CONTROL BUS ➡ POWER ➡ I2C BUS ↔

h. LED circuit block

LED_P

LED Driving Circuit

4 parallels & 9 series

FB1
FB2
FB3
FB4
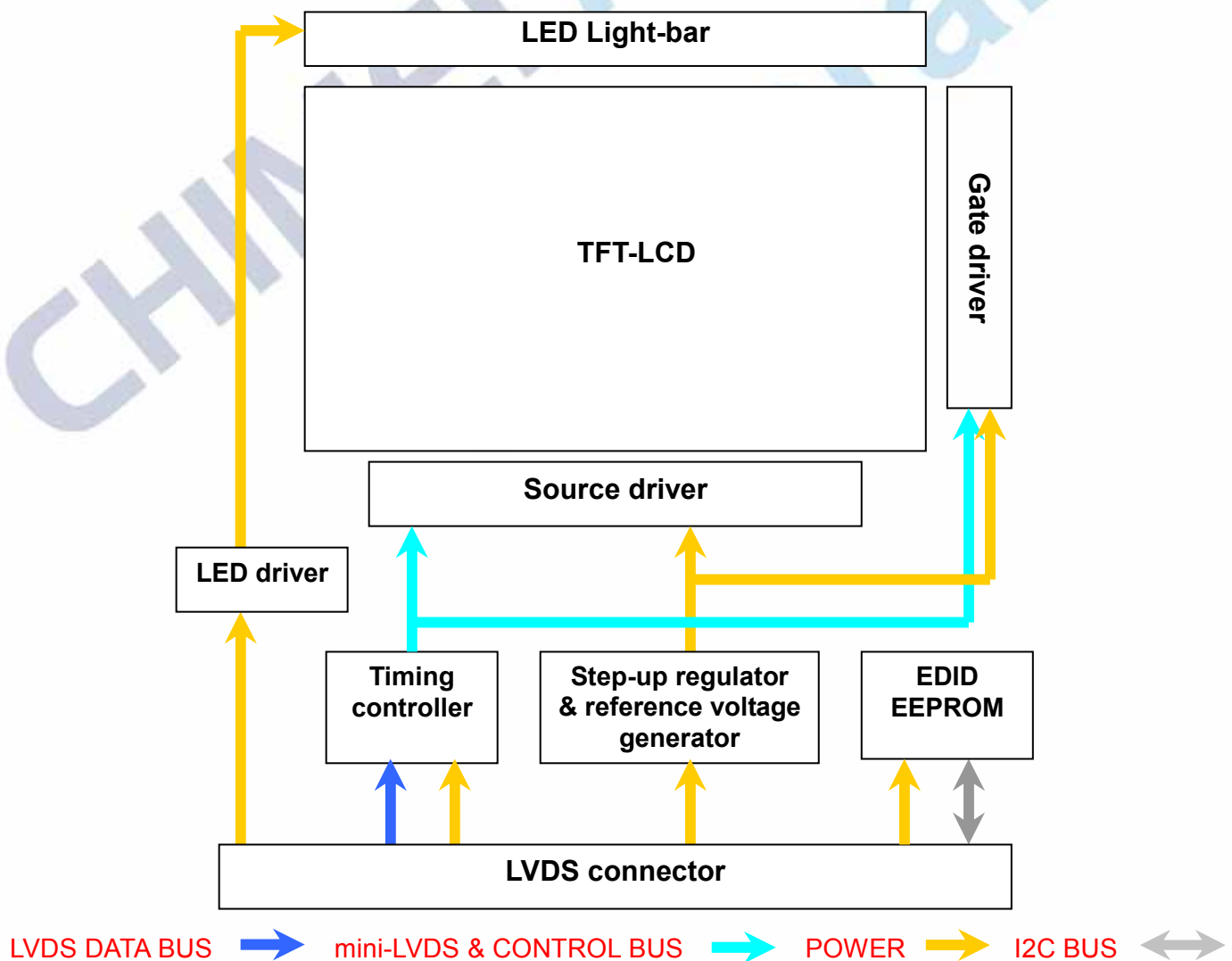
3. Optical specifications

Ambient temperature = 25℃

Item	Symbol	Condition	Specification			Unit	Remark
			Min.	Typ.	Max.		
Response time	Tr+Tf	θ= 0°		8	15	ms	Note 3
Contrast ratio	CR	θ= 0°	500	600			Note 2,4
Viewing angle	Top	CR≧10	15			deg	Note 2,4,6
	Bottom		30				
	Left		40				
	Right		40				
	Top	CR≧100	6				
	Bottom		11				
	Left		25				
	Right		25				
Brightness (5 points average)	Y_L		190	220		nit	Note 2,5
Color chromaticity (CIE)	W_x	θ= 0°	-0.03	0.313	+0.03		Note 2
	W_y			0.329			
	R_x			0.586			
	R_y			0.355			
	G_x			0.323			
	G_y			0.57			
	B_x			0.163			
	B_y			0.141			
Color gamut	NTSC	CIE1931		45		%	-
White uniformity	$\delta_{W(5)}$				1.25		Note 2,7
	$\delta_{W(13)}$				1.5		
Cross talk	Ct				2%		Note 8

Note 1: To be measured in dark room.

Note 2: To be measured with a viewing cone of 2°by Topcon luminance meter BM-5A.

Note 3: Definition of response time:

The output signals of BM-7 are measured when the input pattern are changed from "Black" to "White" (falling time) and from "White" to "Black" (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Refer to figure as below.
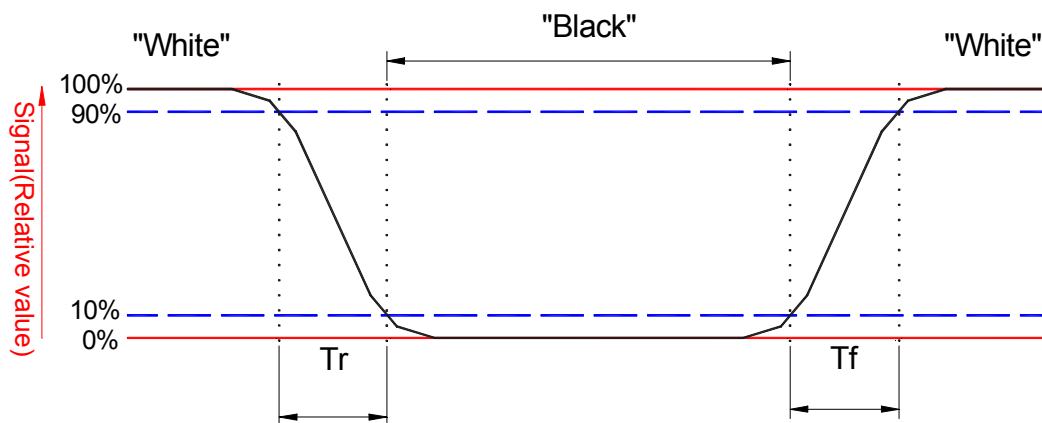
Note 4: Definition of contrast ratio:

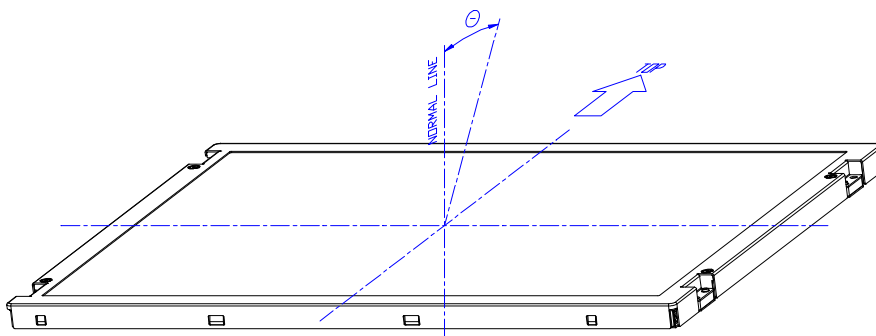Contrast ratio is calculated with the following formula:

$$\text{Contrast ratio (Avg of 5pts)} = \frac{L_{\text{white (Avg of 5pts.)}}}{L_{\text{Black (Avg of 5pts.)}}}$$

Note 5: Driving current for LED should be 20 mA.

Luminance is measured at the following thirteen points (1~13):

$Y_L = (Y5+ Y10+Y11+Y12 +Y13) / 5$

Note 6: Definition of viewing angle

Note 7: Definition white uniformity

Luminance is measured at the following thirteen points (1~13):

$$\delta_{W(13)} = \frac{\text{Maximum brightness of thirteen points}}{\text{Minimum brightness of thirteen points}}$$

$$\delta_{W(5)} = \frac{\text{Maximum brightness of five points}}{\text{Minimum brightness of five points}}$$

13 point measuring locations refer to the point 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12 and 13.
5 point measuring locations refer to the point 5, 10, 11, 12 and 13.

Note 8:

32 gray level

32 gray level

0 gray level
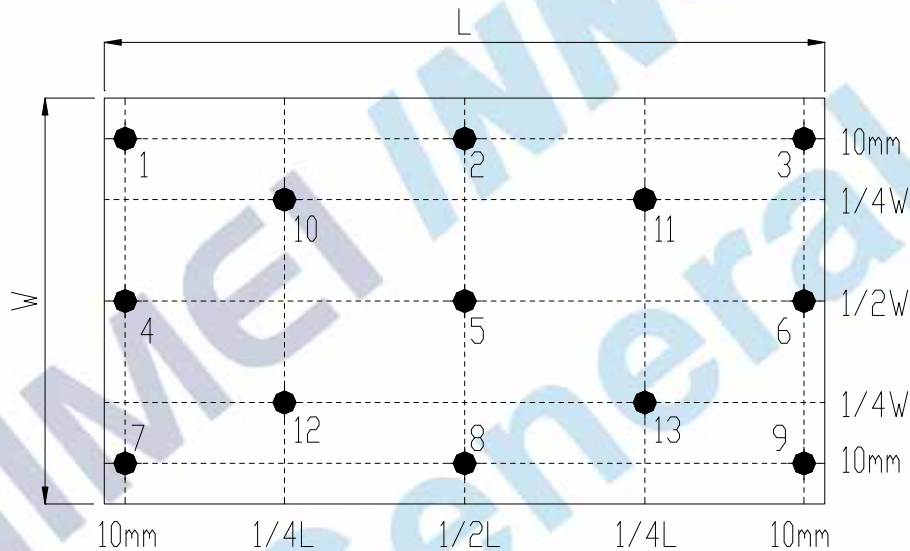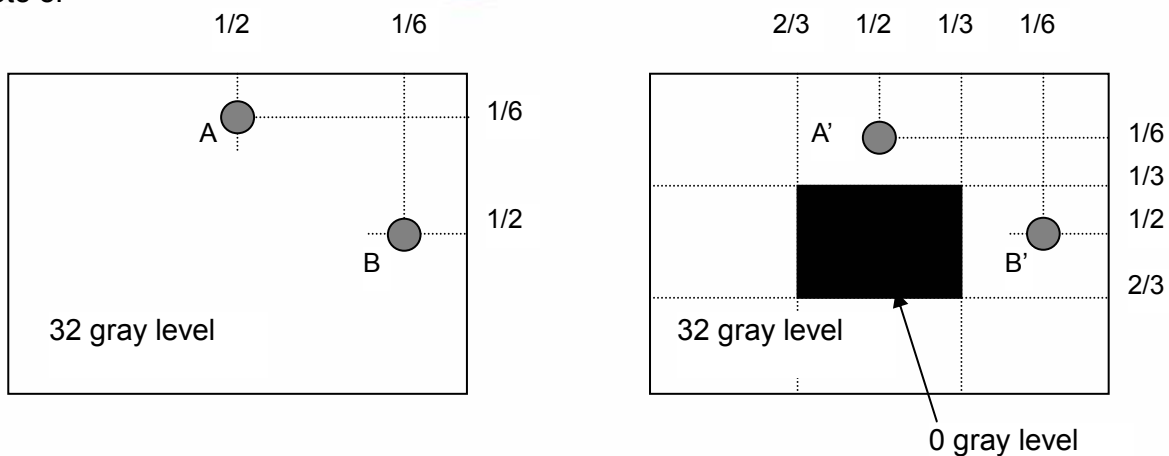
Unit: percentage of dimension of display area

I L_A-$L_{A'}$ I / L_A x 100%= 2% max., L_A and $L_{A'}$ are brightness at location A and A'

I L_B-$L_{B'}$ I / L_B x 100%= 2% max., $L_{B'}$ and $L_{B'}$ are brightness at location B and B'

4. Reliability test items

Test Item	Test Condition	Judgment	Remark
High temperature storage	60℃, 240 hours	Note 1	Note 2
Low temperature storage	-20℃, 240 hours	Note 1	Note 2
High temperature & high humidity operation	40℃, 90% RH, 240 hours (No condensation)	Note 1	Note 2
High temperature operation	50℃, 240 hours	Note 1	Note 2
Low temperature operation	0℃, 240 hours	Note 1	Note 2
Thermal shock (Non-operation)	-25℃ / 30 mins ~ 65℃ / 30 mins 100 cycles	Note 1	Note 2
Electrostatic discharge (ESD)	150 pF, 330Ω, Contact: ±8kV, Air: ±15kV	Note 1	
Vibration (Non-operation)	1.5G, 10 to 500 Hz random; 0.5hr in each perpendicular axes (X, Y, Z).	Note 1	Note 2
Mechanical shock (Non-operation)	220G/2ms, Half sine wave, ±X, ±Y, ±Z one time for each direction	Note 1	Note 2

Note 1: Pass: Normal display image with no obvious non-uniformity and no line defect.
    Fail: No display image, obvious non-uniformity, or line defects.
     Partial transformation of the module parts should be ignored.

Note 2: Evaluation should be tested after storage at room temperature more than one hour.

5. Safety

5-1. Sharp edge requirements

There will be no sharp edges or corners on the display assembly that could cause injury.

5-2. Materials

a. Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible InnoLux Toxicologist.

b. Flammability

All components including electrical components that do not meet the flammability grade UL94-V0 in the module will complete the flammability rating exception approval process. The printed circuit board will be made from material rated 94-V0 or better. The actual UL flammability rating will be printed on the printed circuit board.

c. Capacitors

If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.

6. Display quality

The display quality of the color TFT-LCD module should be in compliance with the InnoLux incoming inspection standard.

7. Handling precaution

(1) Do not apply rough force such as bending or twisting to the module during assembly.
(2) To assemble or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
(3) It's not permitted to have pressure or impulse on the module because the LCD panel and backlight will be damaged.
(4) Always follow the correct power sequence when LCD module is connecting and operating.
(5) Do not pull the I/F connector in or out while the module is operating.
(6) Do not disassemble the module.
(7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
(8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
(9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.

8. Label Definition

8-1. Module label

BT140GW01   **V.9**
AB1400001   **90X**

INNOLUX ™
cRUus GP
E253847 00

$Z_1 Z_2 Z_3 Z_4 Z_5 Z_6 - Z_7 - Z_8 Z_9 Z_{10} Z_{11} Z_{12}$

23mm

$Z_1 Z_2 - Z_3 Z_4 Z_5 Z_6 Z_7 - Z_8 - Z_9 - Z_{10} - Z_{11} Z_{12} - Z_{13} - Z_{14} Z_{15} - Z_{16} - Z_{17} Z_{18} Z_{19} - Z_{20} Z_{21} Z_{22} Z_{23}$

MADE IN CHINA

73mm
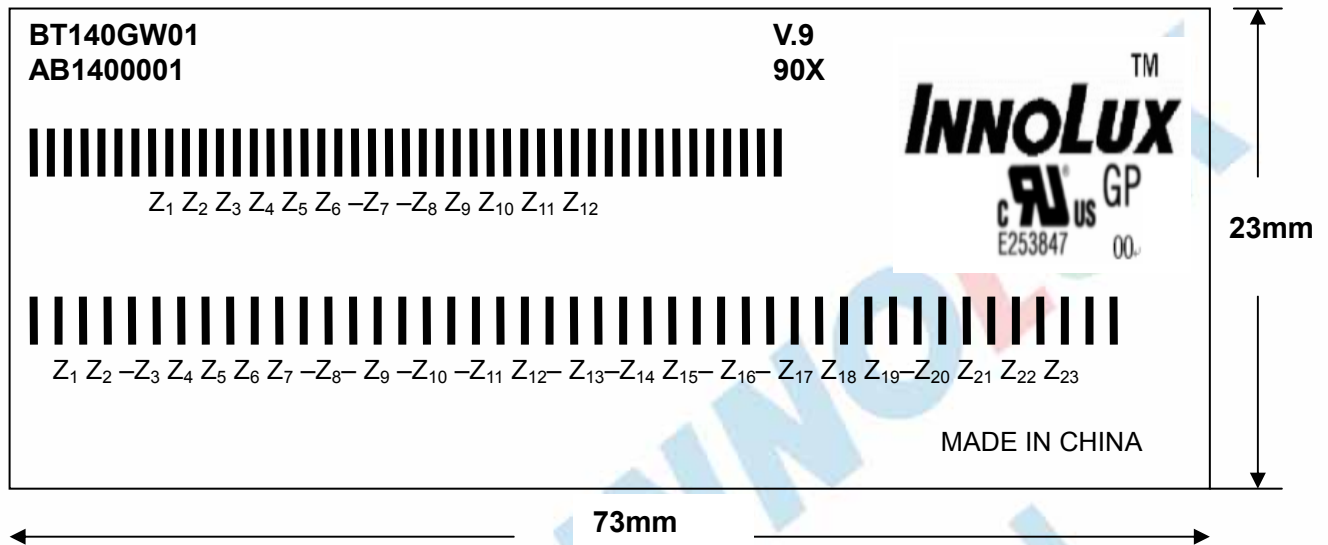
(a) Model Number: BT140GW01 V.9
(b) Product Number: AB140000190X
(c) Serial ID (INL Internal Use):
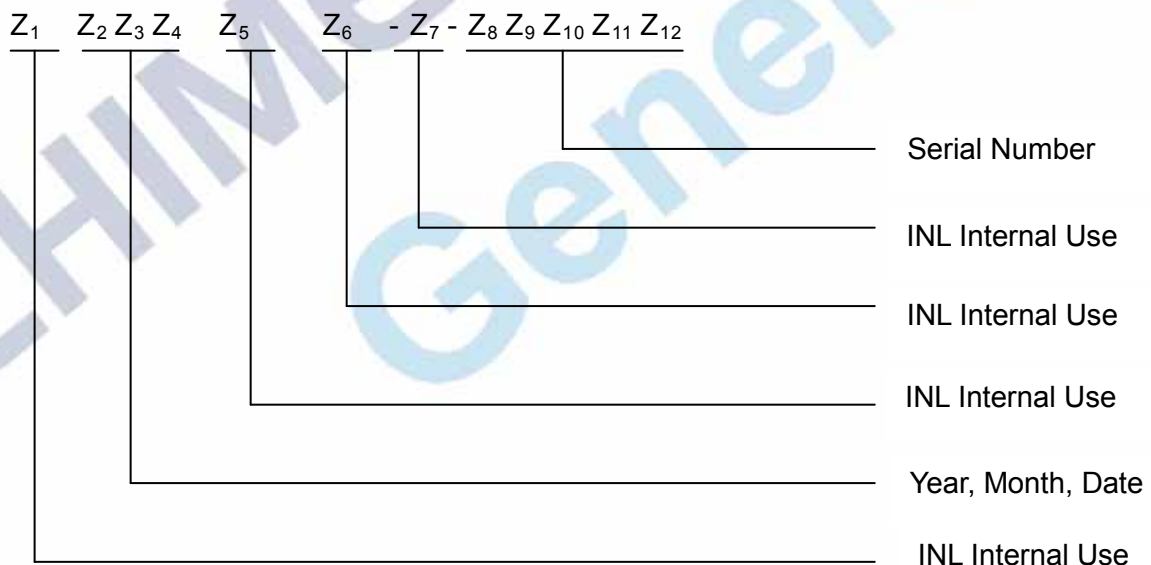
Z_1 $Z_2 Z_3 Z_4$ Z_5 Z_6 $- Z_7 -$ $Z_8 Z_9 Z_{10} Z_{11} Z_{12}$

Serial Number

INL Internal Use

INL Internal Use

INL Internal Use

Year, Month, Date

INL Internal Use

Serial ID includes the information as below:

→Manufactured Date:

Year: 0~9, for 2000~2009;

Month: 1~9 & A~C for Jan.~Dec.;

Date: 1~9 & A~V (exclude I, O, Q, U) for 1st~31st.

→Serial Number: Module packing sequence number

(d) Serial ID II (INL Internal Use):

$Z_1 Z_2 - Z_3 Z_4 Z_5 Z_6 Z_7 - Z_8 - Z_9 - Z_{10} - Z_{11} Z_{12} - Z_{13} - Z_{14} Z_{15} - Z_{16} - Z_{17} Z_{18} Z_{19} - Z_{20} Z_{21} Z_{22} Z_{23}$

8-2. Carton label

|← ——————————————————— 90.5mm ————————————————————— →|

INNOLUX DISPLAY

M

BOX ID:

|← 20mm →|

$Z_1 Z_2 Z_3 Z_4 - Z_5 - Z_6 Z_7 Z_8 Z_9$

|← 55mm →|

53.5mm

Part No. LK.1400N.001

Model No.　BT140GW01　　V.9　　　　　　AB1400001　　90X

Quantity:　XXPCS

MFG Date: XXXX/XX/XX

QC:

MADE IN CHINA
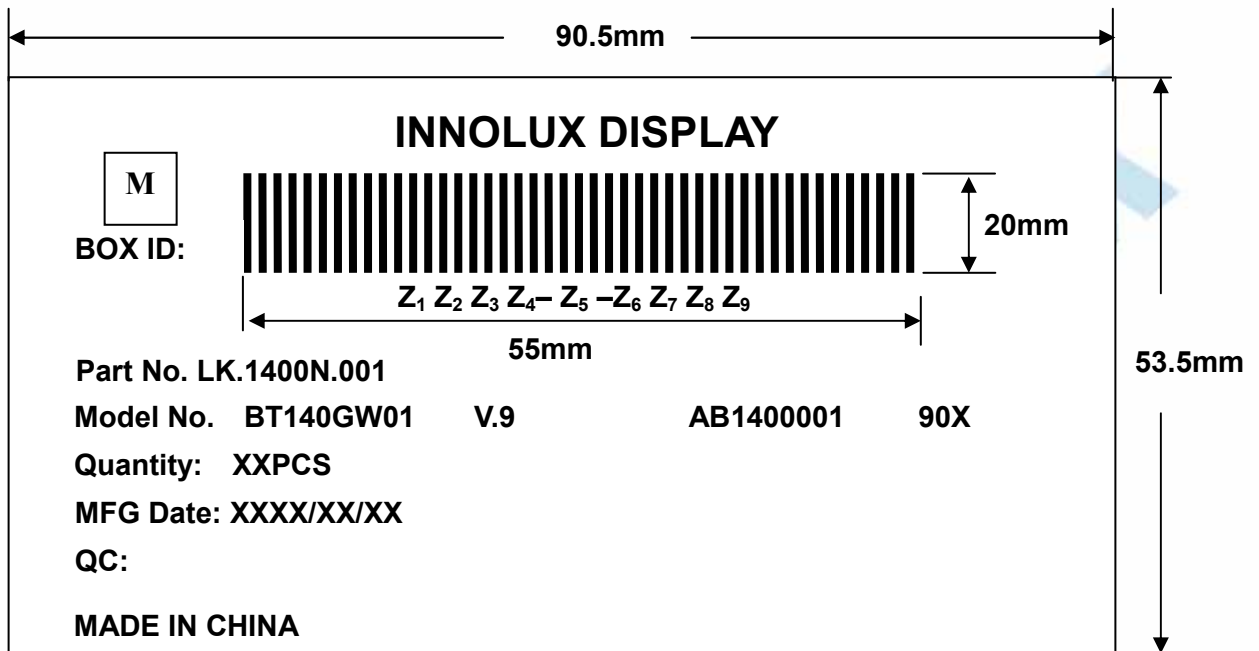
(a) Model No. : BT140GW01 V.9
(b) Package Quantity: XXPCS
(c) Serial ID:

Z_1　　$Z_2 Z_3 Z_4 - Z_5 - Z_6 Z_7 Z_8 Z_9$
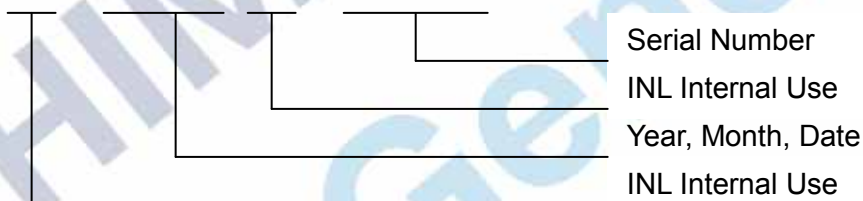
　　　　　　　　　　　Serial Number

　　　　　　　　　　　INL Internal Use

　　　　　　　　　　　Year, Month, Date

　　　　　　　　　　　INL Internal Use

　Serial ID includes the information as below:
　→Manufactured Date:
　　Year: 0~9, for 2000~2009;
　　Month: 1~9 & A~C for Jan.~Dec.;
　　Date: 1~9 & A~V (exclude I, O, Q, U) for 1st~31st.
　→Serial Number: Module packing sequence number

9. Packing Form

LCM

⇩ Step A
Put LCM into A/S bag

A/S Bag

⇩ Step B
Turn back A/S bag

⇩ Step C

↳ Step D
Put LCM with A/S bag into

25 pcs
Lower Cushion

Carton

Step E ⇩

Step F ⇩

Upper Cushion
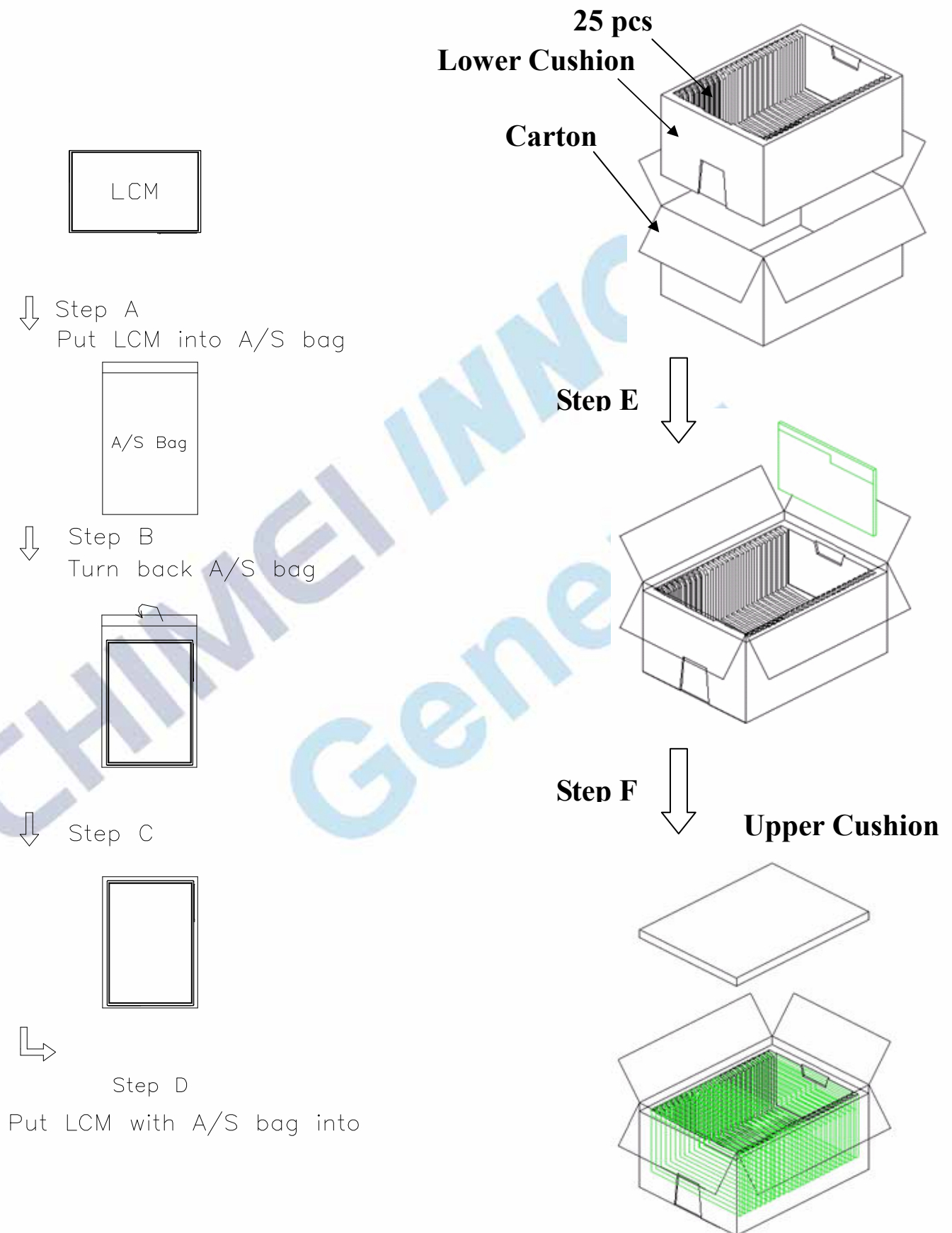
10. Mechanical Drawings
10-1. Front side

10-2 Rear side

HIGH VOLTAGE CAUTION
RISK OF ELECTRIC SHOCK
DISCONNECT THE ELECTRIC
POWER BEFORE SERVICE

BT140HGW01 V.X 8YB14005-00

INNOLUX
cⓊus GP
E83847

MADE IN
CHINA

V.6
60X

BT140GW01
AB1400001

Z1 Z2 Z3 Z4 Z5 Z6 Z7 -Z8 Z9 Z10 Z11
Z12

Z1 Z2 -Z3 Z4 Z5 Z6 Z7 -Z8- Z9 -Z10 -Z11 Z12- Z13 -Z14 Z15- Z16 -Z17 Z18
Z19 -Z20 Z21 Z22 Z23

Z1 Z2 Z3 Z4 Z5 Z6 -Z7 -Z8 Z9 Z10 Z11

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Ⓦ Don't Touch!

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>PC<   M1
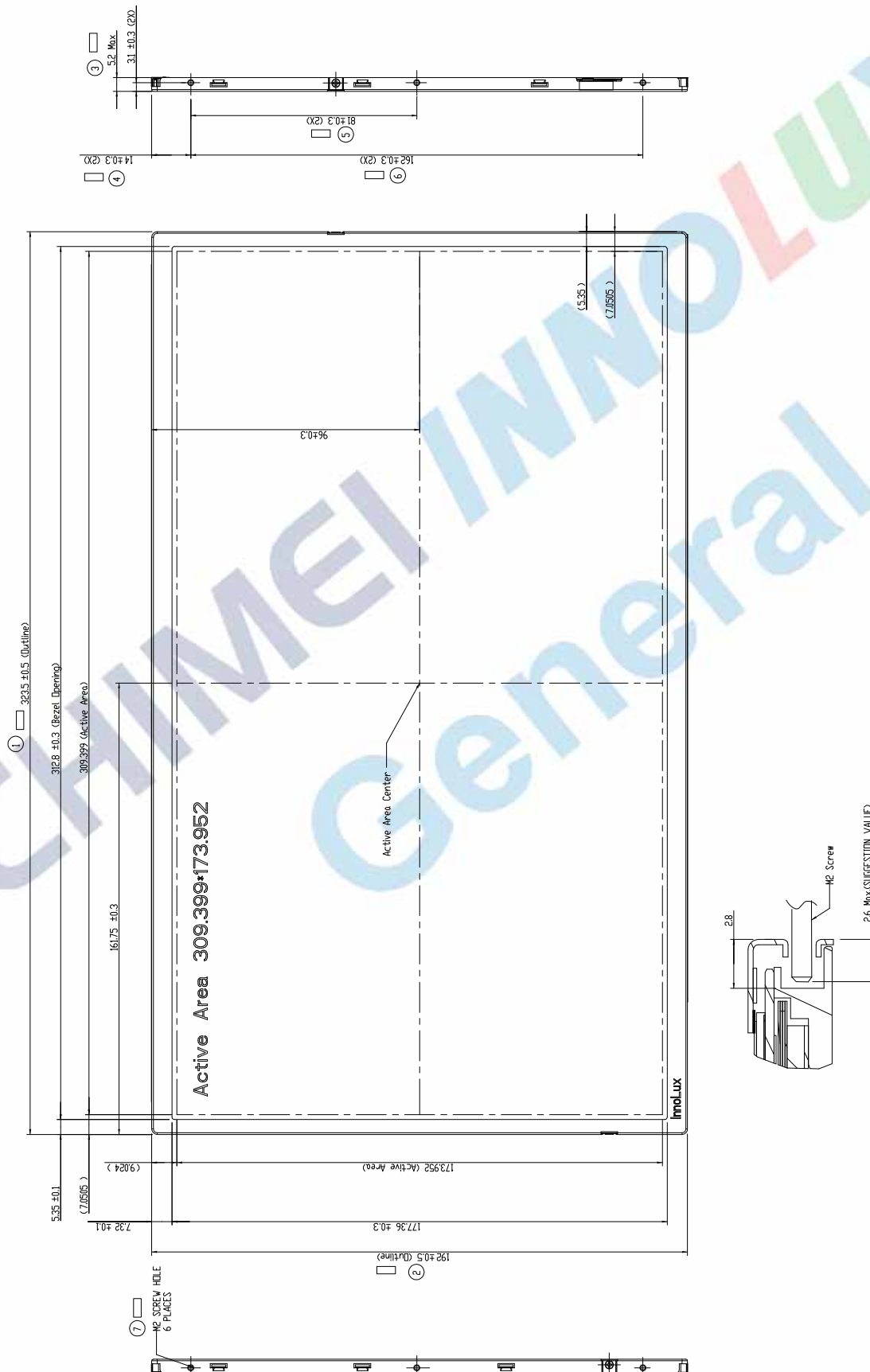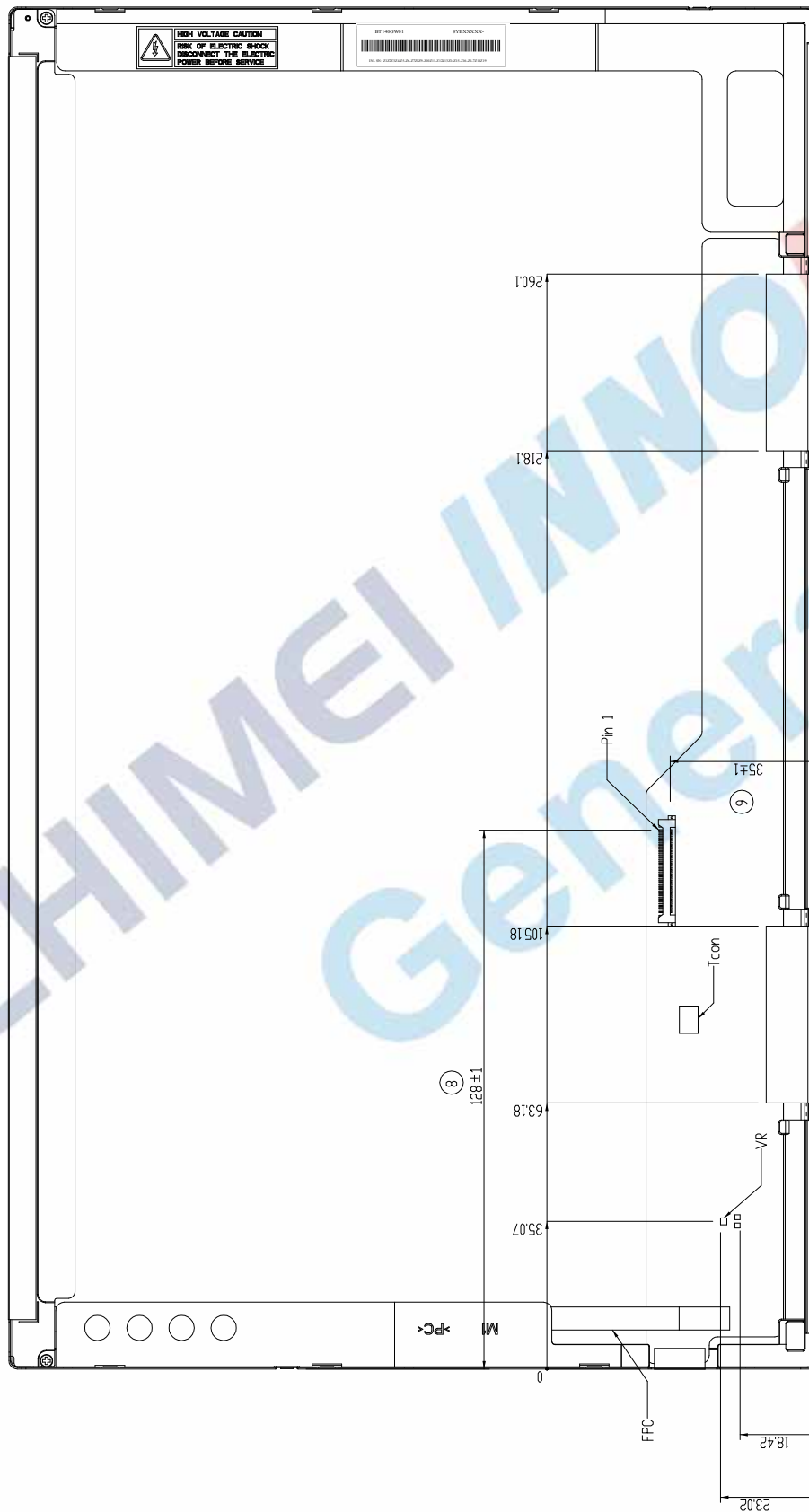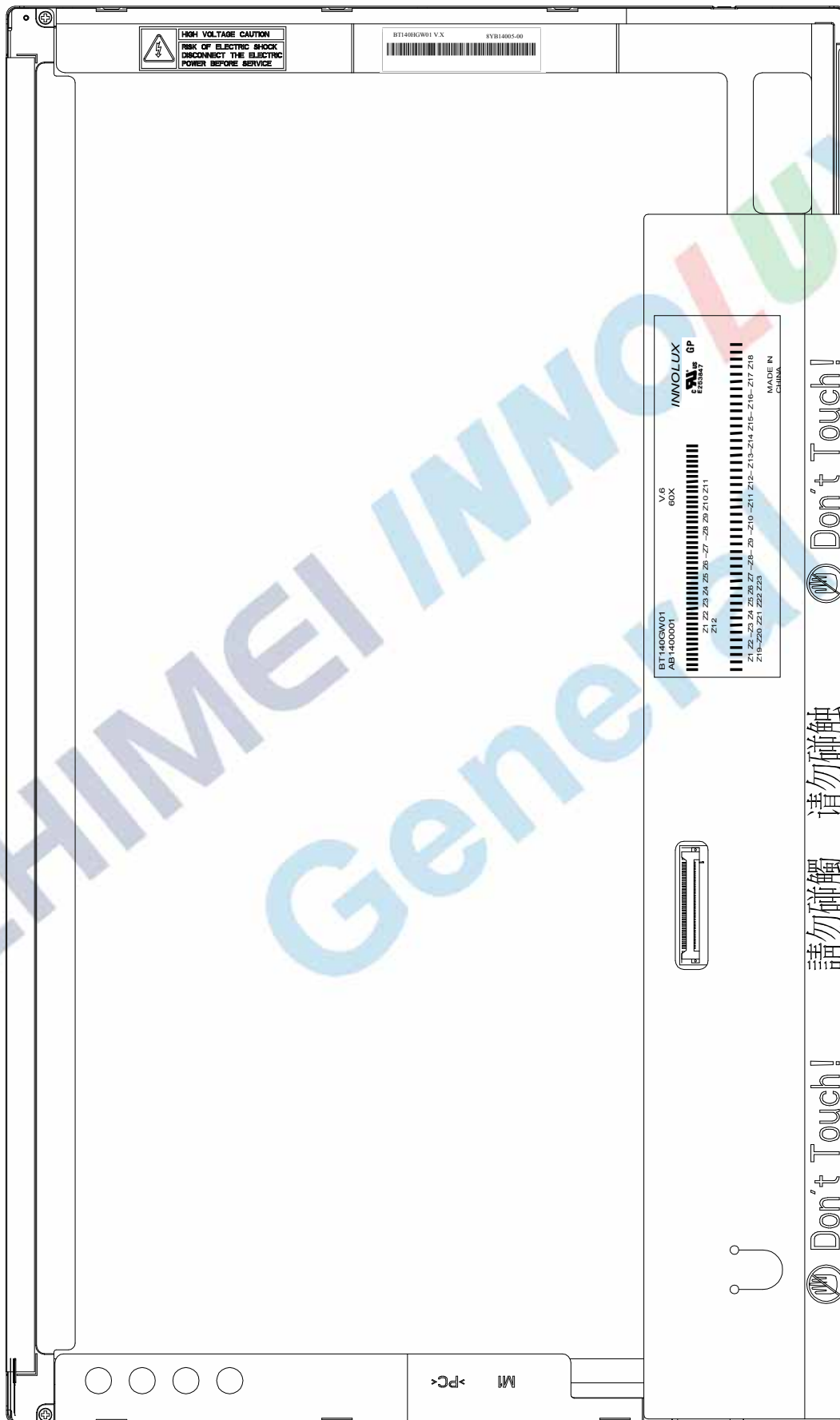
Appendix: EDID Code

	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
Header	00	Header	**00**	00000000
	01	Header	**FF**	11111111
	02	Header	**FF**	11111111
	03	Header	**FF**	11111111
	04	Header	**FF**	11111111
	05	Header	**FF**	11111111
	06	Header	**FF**	11111111
	07	Header	**00**	00000000
Vendor / Product EDID Version	08	EISA manufacture code (3 Character ID)　　　　" CMI"	**0D**	00001101
	09	EISA manufacture code (Compressed ASCⅡ)	**A9**	10101001
	0A	Panel Supplier Reserved - Product Code　　　　"27"	**1B**	00000000
	0B	(Hex. LSB first)	**00**	00000000
	0C	LCD Module Serial No - Preferred but Optional ("0" If not used)	**00**	00000000
	0D	LCD Module Serial No - Preferred but Optional ("0" If not used)	**00**	00000000
	0E	LCD Module Serial No - Preferred but Optional ("0" If not used)	**00**	00000000
	0F	LCD Module Serial No - Preferred but Optional ("0" If not used)	**00**	00000000
	10	Week of Manufacture　　　　　　　　00 weeks	**00**	00000000
	11	Year of Manufacture　　　　　　　　2010 years	**14**	00010100
	12	EDID structure version # =　　1	**01**	00000001
	13	EDID revision # =　　3	**03**	00000011
Display Parameters	14	Video input Definition = Digital signal	**80**	10000000
	15	Max H image size (Rounded cm) =　31 cm	**1F**	00011111
	16	Max V image size (Rounded cm) =　18 cm	**12**	00010010
	17	Display gamma = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	**78**	01111000
	18	Feature Support (no_DPMS, no_Active Off/Very Low Power, RGB color display, Timing BLK 1,no_ GTF)	**0A**	00001010
Panel Color Coordinates	19	Red/Green Low Bits (RxRy/GxGy)	**87**	10000111
	1A	Blue/White Low Bits (BxBy/WxWy)	**F5**	11110101
	1B	Red X　　　　Rx = 0.580	**94**	10010100
	1C	Red Y　　　　Ry = 0.340	**57**	01010111
	1D	Green X　　　Gx = 0.310	**4F**	01001111
	1E	Green Y　　　Gy = 0.550	**8C**	10001100
	1F	Blue X　　　Bx = 0.155	**27**	00100111
	20	Blue Y　　　By = 0.155	**27**	00100111
	21	White X　　　Wx = 0.313	**50**	01010000
	22	White Y　　　Wy = 0.329	**54**	01010100

Established Timings	23	Established timing 1 (00h if not used)	**00**	00000000
	24	Established timing 2 (00h if not used)	**00**	00000000
	25	Manufacturer's timings (00h if not used)	**00**	00000000
Standard Timing ID	26	Standard timing ID1 (01h if not used)	**01**	00000001
	27	Standard timing ID1 (01h if not used)	**01**	00000001
	28	Standard timing ID2 (01h if not used)	**01**	00000001
	29	Standard timing ID2 (01h if not used)	**01**	00000001
	2A	Standard timing ID3 (01h if not used)	**01**	00000001
	2B	Standard timing ID3 (01h if not used)	**01**	00000001
	2C	Standard timing ID4 (01h if not used)	**01**	00000001
	2D	Standard timing ID4 (01h if not used)	**01**	00000001
	2E	Standard timing ID5 (01h if not used)	**01**	00000001
	2F	Standard timing ID5 (01h if not used)	**01**	00000001
	30	Standard timing ID6 (01h if not used)	**01**	00000001
	31	Standard timing ID6 (01h if not used)	**01**	00000001
	32	Standard timing ID7 (01h if not used)	**01**	00000001
	33	Standard timing ID7 (01h if not used)	**01**	00000001
	34	Standard timing ID8 (01h if not used)	**01**	00000001
	35	Standard timing ID8 (01h if not used)	**01**	00000001
Timing Descriptor #1	36	Pixel Clock/10,000 (LSB) 71 MHz @ 60Hz	**BC**	10111100
	37	Pixel Clock/10,000 (MSB)	**1B**	00011011
	38	Horizontal Active (lower 8 bits) 1366 Pixels	**56**	01010110
	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 132 Pixels	**84**	10000100
	3A	Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits)	**50**	01010000
	3B	Vertical Avtive 768 Lines	**00**	00000000
	3C	Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels) 22 Lines	**16**	00010110
	3D	Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits)	**30**	00110000
	3E	Horizontal Sync. Offset (Thfp) 48 Pixels	**30**	00110000
	3F	Horizontal Sync Pulse Width (HSPW) 32 Pixels	**20**	00100000
	40	Vertical Sync Offset(Tvfp) : Sync Width (VSPW) 1 Lines : 4 Lines	**14**	00010100
	41	Horizontal Vertical Sync Offset/Width (upper 2bits)	**00**	00000000
	42	Horizontal Image Size (mm) 309 mm	**35**	00110101
	43	Vertical Image Size (mm) 174 mm	**AE**	10101110
	44	Horizontal Image Size / Vertical Image Size	**10**	00010000
	45	Horizontal Border = 0 (Zero for Notebook LCD)	**00**	00000000
	46	Vertical Border = 0 (Zero for Notebook LCD)	**00**	00000000
	47	Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, Hsync_NEG), DE only note : LSB is set to 'l' if panel is DE-timing only. H/V can be ignored.	**18**	00011000

Timing Descriptor #2	48	Flag		**00**	00000000
	49	Flag		**00**	00000000
	4A	Flag		**00**	00000000
	4B	Data Type Tag (Descriptor Defined by manufacturer)		**00**	00000000
	4C	Flag		**00**	00000000
	4D	Descriptor Defined by manufacturer		**20**	00100000
	4E	Descriptor Defined by manufacturer		**20**	00100000
	4F	Descriptor Defined by manufacturer		**20**	00100000
	50	Descriptor Defined by manufacturer		**20**	00100000
	51	Descriptor Defined by manufacturer		**20**	00100000
	52	Descriptor Defined by manufacturer		**20**	00100000
	53	Descriptor Defined by manufacturer		**20**	00100000
	54	Descriptor Defined by manufacturer		**20**	00100000
	55	Descriptor Defined by manufacturer		**20**	00100000
	56	Descriptor Defined by manufacturer		**20**	00100000
	57	Descriptor Defined by manufacturer		**20**	00100000
	58	Descriptor Defined by manufacturer		**20**	00100000
	59	Descriptor Defined by manufacturer		**20**	00100000
Timing Descriptor #3	5A	Flag		**00**	00000000
	5B	Flag		**00**	00000000
	5C	Flag		**00**	00000000
	5D	Data Type Tag (ASCII String)		**FE**	11111110
	5E	Flag		**00**	00000000
	5F	ASCII String "C"		**43**	01000011
	60	ASCII String "M"		**4D**	01001101
	61	ASCII String "I"		**49**	01001001
	62	ASCII String		**0A**	00001010
	63	ASCII String		**20**	00100000
	64	ASCII String		**20**	00100000
	65	ASCII String		**20**	00100000
	66	ASCII String		**20**	00100000
	67	ASCII String		**20**	00100000
	68	ASCII String		**20**	00100000
	69	Manufacturer P/N(If<13 char--> 0Ah, then terminate with ASCⅡ code 0Ah,set remaining char = 20h)		**20**	00100000
	6A	Manufacturer P/N(If<13 char--> 0Ah, then terminate with ASCⅡ code 0Ah,set remaining char = 20h)		**20**	00100000
	6B	Manufacturer P/N(If<13 char--> 0Ah, then terminate with ASCⅡ code 0Ah,set remaining char = 20h)		**20**	00100000

Timing Descriptor #4	6C	Flag		00	00000000
	6D	Flag		00	00000000
	6E	Flag		00	00000000
	6F	Data Type Tag (ASCII String)		FE	11111100
	70	Flag		00	00000000
	71	Monitor Name, stored as ASCII	"B"	42	01000010
	72	Monitor Name, stored as ASCII	"T"	54	01010100
	73	Monitor Name, stored as ASCII	"1"	31	00110001
	74	Monitor Name, stored as ASCII	"4"	34	00110100
	75	Monitor Name, stored as ASCII	"0"	30	00110000
	76	Monitor Name, stored as ASCII	"G"	47	01000111
	77	Monitor Name, stored as ASCII	"W"	57	01010111
	78	Monitor Name, stored as ASCII	"0"	30	00110000
	79	Monitor Name, stored as ASCII	"1"	31	00110011
	7A	Monitor Name, stored as ASCII	"V"	56	01010110
	7B	Monitor Name, stored as ASCII	"9"	39	00110100
	7C	Monitor Name, stored as ASCII		0A	00001010
	7D	Monitor Name, stored as ASCII		20	00100000
Checksum	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)		00	00000000
	7F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)		6E	01101110