Zach Dubinsky
CSC-270-01
Prof. Reiffel
3/17/22

I affirm that this, and all included work, is my own in accordance with the class syllabus and the Union College Honor Code [Signed: Zach Dubinsky, 3/17/22].

1. **Summary:** I write this to give a little feedback on an ARM course, to give a roster for my files, and to provide the Honor Code statement for everything in one place.
2. **Roster:** I included the .o and executable files in all lab folders because I was nervous that it would affect my code if I deleted them.
    a. Writeups: contains three labeled write ups for labs 5, 6, and 7.
    b. Assignments: contains three labeled assignment statements for labs 5, 6, and 7.
    c. Lab_5: sum.s is the starter code, sum2.s and swap.s are the solution codes.
    d. Lab_6: array-sum.s is the starter code, my-array-sum.s and insertion_sort.s are my solution codes.
    e. Lab_7: LL_starter.s is the starter code and LL.s is the solution code.
    f. The grade sheet I had submitted is included.
3. **Feedback:** This was one of the most fun projects I have ever done. I really enjoyed learning a new language and applying what I learned in the course. ARM does have a lot of features that just make it more robust than MIPS. That said, and my assignment documents reflect this, we learned MIPS at a different pace than could be done with ARM. Things like pointers, calling convention, and stack use happen much earlier than we talked about them. This is also due to the various architectures of the language, it was hard to find all-encompassing documentation. To use ARM in place of MIPS for this course could be done with clever presentation and an emulator. Otherwise, a full course could be spent on learning ARM, Linux, and Raspberry Pi. Both would be interesting courses, but the latter (which was the scope of my project) could go further into topics of system architecture than we touched on in this class. I think it would be a great class, but something to play around with.