HD44780U

Table 12 4-Bit Operation, 8-Digit \times 1-Line Display Example with Internal Reset

Step	Instruction					uction		
No.	RS	R/W	DB7	DB6	DB5	DB4	— Display	Operation
1	Power supply on (the HD44780U is initialized by the internal reset circuit)							Initialized. No display.
2	Fund 0	ction se 0	t 0	0	1	0		Sets to 4-bit operation. In this case, operation is handled as 8 bits by initialization, and only this instruction completes with one write.
3	Fund 0 0	otion se 0 0	t 0 0	0 0	1 *	0 *		Sets 4-bit operation and selects 1-line display and 5×8 dot character font. 4-bit operation starts from this step and resetting is necessary. (Number of display lines and character fonts cannot be changed after step #3.)
4	Display on/off control							Turns on display and cursor.
	0	0 0	0 1	0 1	0 1	0	_	Entire display is in space mode because of initialization.
5	Entry mode set							Sets mode to increment the
	0	0	0	0	0	0		address by one and to shift the
	0	0	0	1	1	0		cursor to the right at the time of write to the DD/CGRAM. Display is not shifted.
6	Write data to CGRAM/DDRAM						Н	Writes H.
	1	0	0	1	0	0	_ <u> </u>	The cursor is incremented by
	1	0	1	0	0	0		one and shifts to the right.

Note: The control is the same as for 8-bit operation beyond step #6.