

		RS	R/ \overline{W}	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	
Clear display	Code	0	0	0	0	0	0	0	0	0	1	
		RS	R/ \overline{W}	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	
Return home	Code	0	0	0	0	0	0	0	0	1	*	Note: * Don't care.
		RS	R/ \overline{W}	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	
Entry mode set	Code	0	0	0	0	0	0	0	1	I/D	S	
		RS	R/ \overline{W}	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	
Display on/off control	Code	0	0	0	0	0	0	1	D	C	B	
		RS	R/ \overline{W}	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	
Cursor or display shift	Code	0	0	0	0	0	1	S/C	R/L	*	*	Note: * Don't care.
		RS	R/ \overline{W}	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	
Function set	Code	0	0	0	0	1	DL	N	F	*	*	
		RS	R/ \overline{W}	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	
Set CGRAM address	Code	0	0	0	1	A	A	A	A	A	A	
						Higher order bit				Lower order bit		

Figure 11 Instruction (1)