

DWEZIL D'SOUZA

Objective: Looking for a full-time Software Engineer position starting in January 2018

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400 N River Road, Apt #911, West Lafayette, IN - 47906 (765)714-5622

EDUCATION

Purdue University, West Lafayette, IN

Graduating in December, 2017

Bachelor of Science in **Computer Engineering**

GPA: 3.48/4.00

LANGUAGES / TOOLS

Proficient: C, Java, Verilog **Familiar:** Python, JavaScript, HTML, CSS, C++, C#, MATLAB

INTERNSHIPS / EXPERIENCE

Software Development Engineer Intern, Amazon

Summer, 2017

- **Extended the Alexa Skills Kit SDK** to expose new APIs for third-party skill developers.
- **Developed an Alexa cloud service** to serve as the backend for the new features.
- **Performed end-to-end testing** on Alexa devices and presented the product at a showcase event.

Software Development Engineer Intern, Amazon

Summer, 2016

- **Designed a new backend data flow model** for the Kindle ebooks platform to optimize latency.
- **Implemented service API migration** to deliver price information for ebooks across all surfaces.

Senior Project, Engineering Projects In Community Service

Fall, 2017

- **Designing and implementing** an IoT glove which converts American Sign Language to English.

Project Manager, Engineering Projects In Community Service

Fall, 2016

- **Lead a group of 15 undergraduate engineering students** on a mobile development project.
- **Developed in Android Studio** to implement a step-tracker integrated within a larger application.

Software Verification Engineer, Alliance Group Technologies *Summer, 2015 – Spring, 2016*

- **Designed a software testing suite** using the Arduino platform for hybrid vehicle components which allowed clients to conduct thorough tests on their products before their release.
- **Developed bus-monitoring software** as part of a team, which decoded signals on the CAN bus in real-time to accurately display the state of the entire bus at any point during bus usage.

System-on-Chip Research Team, PU ECE department

Spring, 2015 – Fall 2016

- Contributed to an industry-style design flow for a custom SoC built around an ARM core.
- Designed an AMBA 2D graphics processor using SystemVerilog to be incorporated into the chip.

Teaching Assistant for ECE 270 – Introduction to Digital Systems

Fall, 2015

- Conducted weekly laboratory sessions and graded homework assignments.
- Debugged students' code, circuits and designs during laboratory experiments.

Grader for ECE 369 – Discrete Mathematics

Fall, 2016

Student Associate at Ford Dining Court, Purdue University

Fall, 2013 – Spring, 2015

LEADERSHIP / VOLUNTEERING

- Leads a faith-sharing and community-focused Bible study group that meets every week.
- Educated children on a United Way volunteer trip. Mentored a school child for 4 semesters.