

Coursework Report

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Software Development 3 (SET09101)

1 Introduction

Realistic Real time physics simulation is highly sought after in interactive applications, especially games. Achieving high-accuracy while maintaining performance in often resource restricted environments (i.e. a games console) requires the highest level of optimisations and often results in a trade-off with simulation speed against Accuracy. This project attempts to record and analyse the performance of various optimisations on a simulated scene. This will be taken further by applying the project to various different processing architectures. The scene that will be simulated is a large set of Bouncy balls, travelling down a hill.

2 Design Patterns

Factory Trying to regain performance from an external physics engine can be a hard task, diving into the source code requires expert knowledge of the inner-workings of the whole system. A common path is to shape the design of the game code to conform better to the demands of the physics engine and hope that the internal optimisations will be sufficient. Often enough, they are not.

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3 Threads

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4 Networking and Webgui

Optimising for Physics Engines Trying to regain performance from an external physics engine can be a hard task, diving into the source code requires expert knowledge of the inner-workings of the whole system. A common path is to shape the design of the game code to conform better to the demands of the physics engine and hope that the internal optimisations will be sufficient. Often enough, they are not.

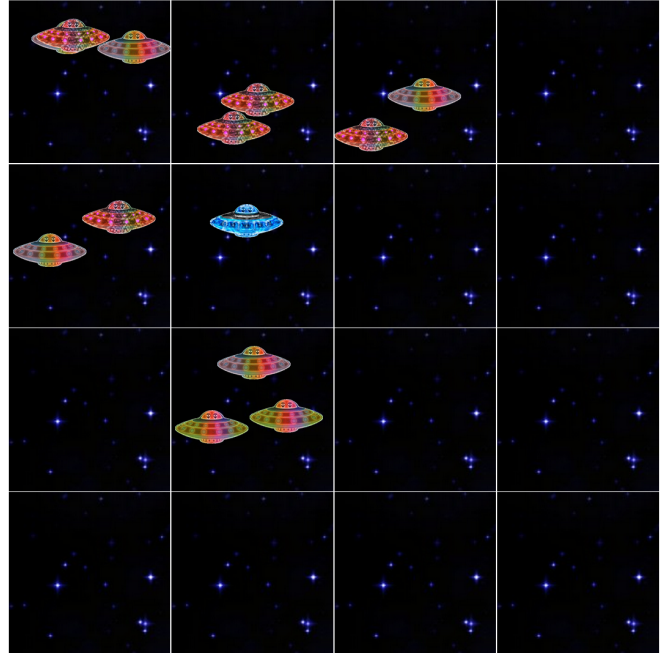
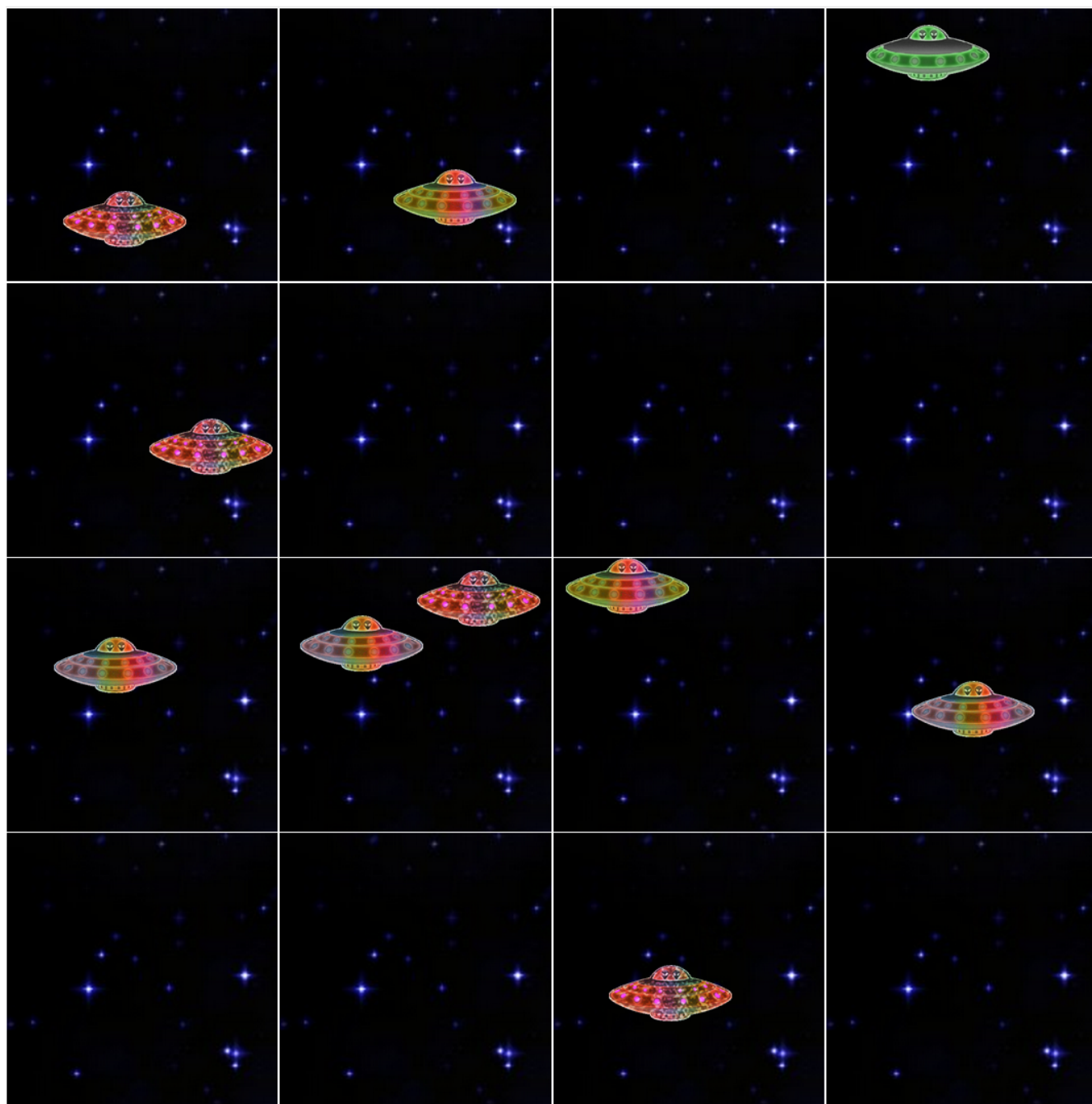


Figure 1: Bullet Physics PS3 Pipeline - Requires Intermediate Data Swapping Between PPU and SPU

5 Conclusions

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Passive ☐

Next Turn

Restart

Undo

Server Address:

Connect

Disconnect

Connection Status: Server connected

Figure 2: *Sequential Clark Wright implementation results - Requires Intermediate Data Swapping Between PPU and SPU*

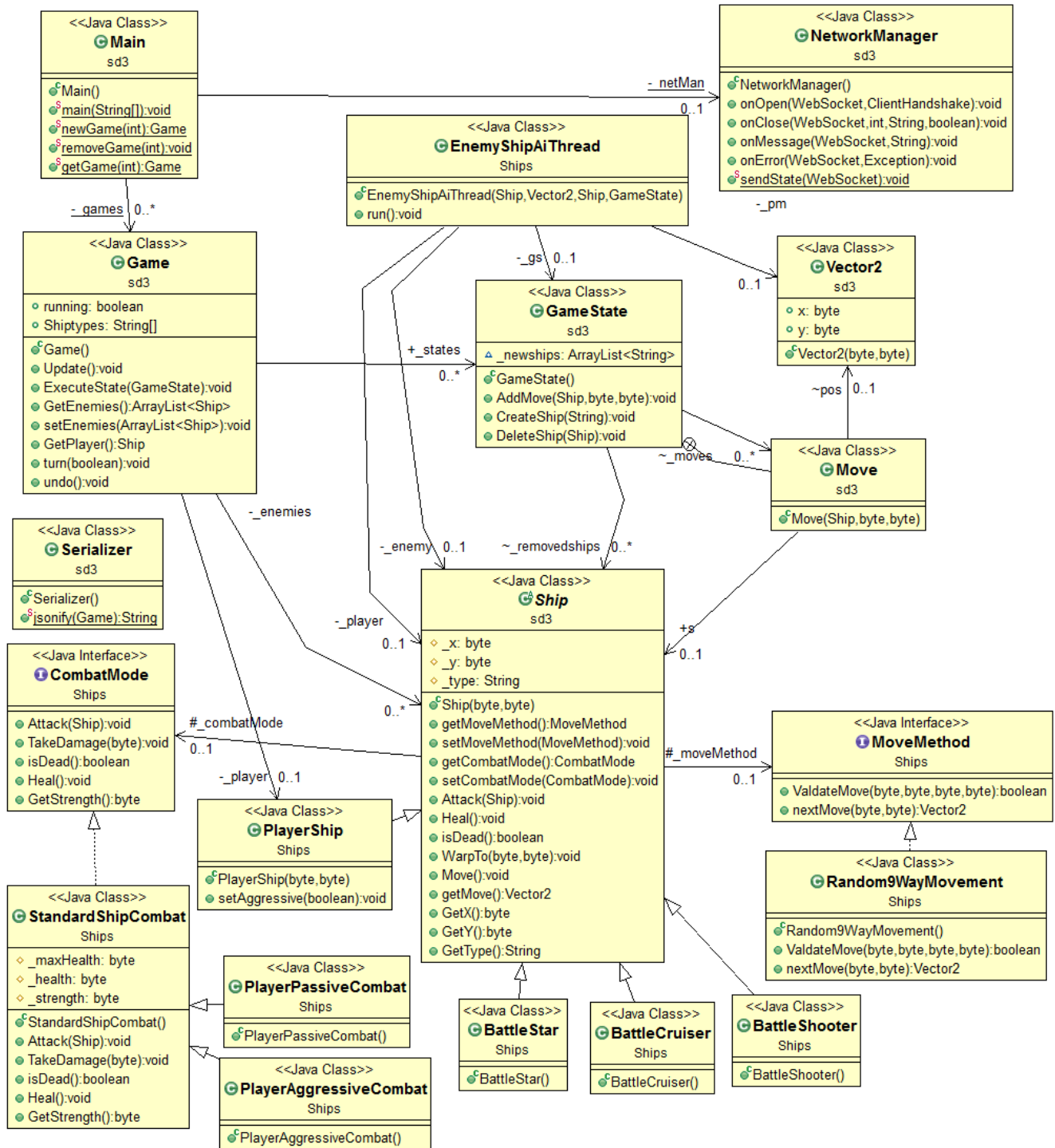


Figure 3: Sequential Clark Wright implementation results - Requires Intermediate Data Swapping Between PPU and SPU