



BRANDON DOOLEY (#16327446)

CS1025 Laboratory Experiment 1

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*All circuit diagrams created using CircuitDiagram
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Brandon Dooley
Dooleyb1@tcd.ie

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Objective

The objective of this experiment was to verify Kirchoff's Voltage and Current Laws using the following circuits below. It is the objective of this report to prove the following hypotheses to be true:

- **Kirchoffs Voltage Law:** The algebraic sum of branch voltages around any loop in an electrical circuit is equal to zero at every instant in time.
- **Kirchoffs Current Law:** The algebraic sum of electrical currents at any node in an electrical circuit is equal to zero at every instant in time.

Method (Part One)

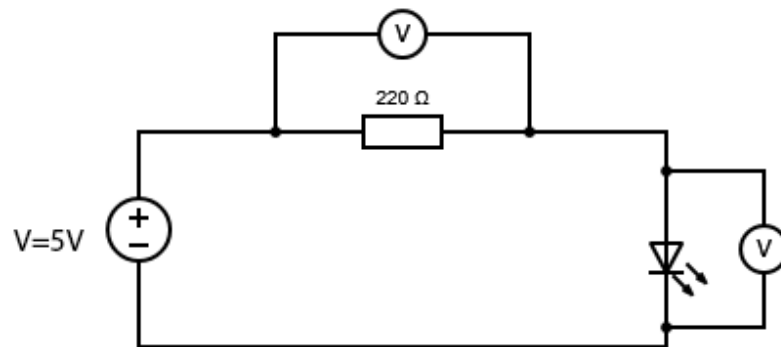


Fig 1.1

The apparatus were set up as shown in Fig 1.1, with a 220Ω resistor connected in series with a standard LED. The circuit was then connected to a 5V d.c power supply and current was allowed to flow through the circuit. Using a voltmeter connected in parallel with both the resistor and the LED the respective voltage drops were measured.

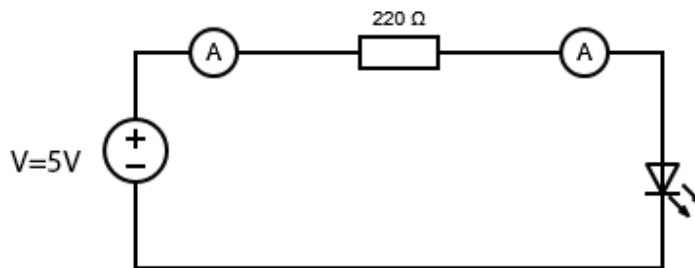


Fig 1.2

The voltmeters were then disconnected and two ammeters placed in series with the components as seen in *Fig 1.2*. The respective currents flowing through each component were then measured and the results recorded.

Results (Part One)

$V_{in} (V)$	$V_{Resistor} (V)$	$V_{LED} (V)$	$I_{in} (mA)$	$I_{Resistor} (mA)$	$I_{LED} (mA)$
5	3.19	1.78	14.6	14.1	13.8

Analysis (Part One)

- **Kirchoffs Voltage Law:** The algebraic sum of branch voltages around any loop in an electrical circuit is equal to zero at every instant in time.

i.e

$$\sum_{loop} V = 0$$

$$\therefore V_{in} + (-V_{LED}) + (-V_{Resistor}) = 0$$

$$V_{in} = V_{LED} + V_{Resistor}$$

$$5 = 3.19 + 1.78$$

$$5V \approx 4.97V$$

- **Kirchoffs Current Law:** The algebraic sum of electrical currents at any node in an electrical circuit is equal to zero at every instant in time.

i.e

$$\sum I (node) = 0$$

$$\therefore I_{in} = I_{Resistor} = I_{LED}$$

$$0.0146A \approx 0.0141A \approx 0.0138A$$

Uncertainty & Error (Part One)

- Tolerance errors in the ammeter/voltmeter not giving results to desired degree of accuracy
- Internal resistance of the ammeter/voltmeter impacting on the results as it has a direct impact on the voltages and currents
- Power lost through other energy conversions e.g heat
- Human errors such as inaccuracy taking measurements and mechanical errors whilst taking the actual measurements

Conclusions(Part One)

- **Kirchoffs Voltage Law:** It can be said that the above circuit holds true for **Kirchoffs Voltage Law** and proves the stated hypothesis within 0.006% tolerance of experimental errors.
- **Kirchoffs Current Law:** It can also be said that the above circuit holds true for **Kirchoffs Current Law** and proves the stated hypothesis within reason and taking into account experimental errors.

Method (Part Two)

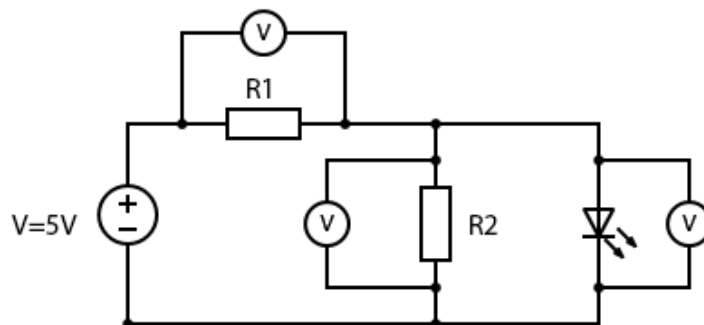


Fig 2.1

The apparatus was set up as shown in Fig 2.1 with a 220Ω resistor (R1) connected in series to a power supply and also connected to a resistor (R2) in parallel with an LED. The circuit was then connected to a 5V d.c power supply and current was allowed to flow through the circuit. Using a voltmeter connected in parallel with both of the resistors and the LED the respective voltage drops were measured. The voltmeters were then disconnected and two ammeters placed in series with the components as seen in

Fig 2.2. The respective currents flowing into each component were then measured and the results recorded.

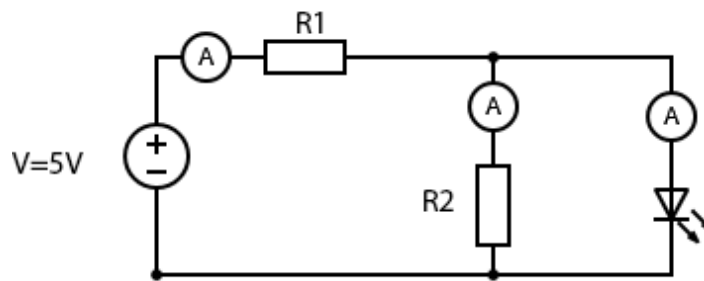


Fig 2.2

The resistor R2 was varied between 50Ω, 100Ω and 220Ω and the above steps were repeated with the voltage drops across, and the current through all elements being measured and recorded each time.

Results (Part Two)

R_{R1}	R_{R2}	$V_{in} (V)$	$V_{R1} (V)$	$V_{R2} (V)$	$V_{LED} (V)$
220	50	5	4.07	0.922	0.921
220	100	5	3.46	1.53	1.55
220	220	5	3.21	1.77	1.77

R_{R1}	R_{R2}	$V_{in} (V)$	$I_{R1} (mA)$	$I_{R2} (mA)$	$I_{LED} (mA)$
220	50	5	18.5	18.1	0.0
220	100	5	15.9	15.4	0.2
220	220	5	14.6	8.1	6.6

Analysis (Part Two)

- **Kirchoffs Voltage Law:** The algebraic sum of branch voltages around any loop in an electrical circuit is equal to zero at every instant in time.

i.e

$$\sum_{loop} V = 0$$

$$\therefore V_{in} + (-V_{R1}) + (-V_{R2} || V_{LED}) = 0$$

$$V_{in} = V_{R1} + (V_{R2} || V_{LED})$$

R_{R2}	$V_{in} (V)$	$V_{R1} (V)$	$V_{R2} (V)$	$V_{LED} (V)$	$V_{R1} + (V_{R2} V_{LED}) (V)$
50	5	4.07	0.922	0.921	4.99
100	5	3.46	1.53	1.55	5.01
220	5	3.21	1.77	1.77	4.98

- **Kirchoffs Current Law:** The algebraic sum of electrical currents at any node in an electrical circuit is equal to zero at every instant in time.

i.e

$$\sum I_{(node)} = 0$$

$$\therefore I_{R1} + (-I_{R2}) + (-I_{LED}) = 0$$

$$I_{R1} = I_{R2} + I_{LED}$$

R_{R2}	$V_{in} (V)$	$I_{R1} (mA)$	$I_{R2} (mA)$	$I_{LED} (mA)$	$I_{R1} = I_{R2} + I_{LED} (mA)$
50	5	18.5	18.1	0.0	18.1
100	5	15.9	15.4	0.2	15.6
220	5	14.6	8.1	6.6	14.7

Uncertainty and Error (Part Two)

- Tolerance errors in the ammeter/voltmeter not giving results to desired degree of accuracy
- Internal resistance of the ammeter/voltmeter impacting on the results as it has a direct impact on the voltages and currents
- Power lost through other energy conversions e.g heat
- Human errors such as inaccuracy taking measurements and mechanical errors whilst taking the actual measurements

Conclusions(Part Two)

- **Kirchoffs Voltage Law:** From the above tables and results it is safe to say that Kirchoffs Voltage Law does hold true for the circuits seen in *Fig 2.1* and *Fig 2.2*. Although the results are not entirely what the hypothesis stated, taking into account experimental error's it can be said that the hypothesis has been proven true. Therefore, the algebraic sum of branch voltages around any loop in an electrical circuit is equal to zero at every instant in time.
- **Kirchoffs Current Law:** Similar to Kirchoffs Voltage Law it is safe to say that Kirchoffs Current Law also holds true for the circuits seen in *Fig 2.1* and *Fig 2.2*. Allowing for reasonable experimental errors it can be stated with confidence that the hypothesis has been proven true; the algebraic sum of electrical currents at any node in an electrical circuit is equal to zero at every instant in time.
- **LED Operation:** It was also noted that in part two of the experiment the intensity of the light emitted from the LED directly depended on the resistor placed in parallel with it. The initial 50Ω resistor caused little if any light to be emitted. This was because the current took the path of least resistance and flowed through the resistor rather than the LED. However, when the 220Ω resistor was put in instead, the LED emitted a large amount of light as more current now flowed through the LED. Therefore it can be stated that current flows through desirable paths of least resistance throughout a circuit.
- **Voltage Drops Across Parallel Branches:** It can also be concluded that voltage is evenly distributed or dropped across parallel branches in a circuit. i.e

$$(V_{R2} || V_{LED}) \\ V_{R2} = V_{LED}$$

- **Current Through Parallel Branches:** It can also be concluded that the sum of currents flowing through a parallel branch is equal to the current entering the branch (*Principle Conservation of Energy*) i.e

$$I_{in} = I_{R2} + I_{LED}$$