



**Trinity College Dublin**

Coláiste na Tríonóide, Baile Átha Cliath

The University of Dublin

# 00–Introduction

**CS1022 – Introduction to Computing II**

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School of Computer Science and  
Statistics

On successful completion of **CS1022** you will be able to:

Describe the characteristics, structure and operation of a microprocessor system, including the execution of subroutines and interfacing with external components and devices;

Translate between high-level programming language constructs, including data structures and subroutines, and their assembly language equivalents;

Design, construct, document and test assembly language programs to solve small-scale problems of moderate complexity by decomposing the problems into smaller parts and implementing solutions consisting of one or more assembly language subroutines;

Construct assembly language programs that can process input from and output to external devices.

## CS1021

- Introduction to Computing I
- 5 credits  
(out of 60 for the full year of your degree course)
- Labs (4)
- Assignments (2)
- 2 hour examination  
(worth 70% of CS1021)

## CS1022

- Introduction to Computing II
- 5 credits  
(out of 60 for the full year of your degree course)
- Labs (4)
- Mid-Term Assignment
- 2 hour examination  
(worth 70% of CS1022)

## Lectures

Monday 14:00 in LB01

Wednesday 09:00 in LB04

## Tutorials

Wednesday 11:00 in Joly

## Labs

Friday 10:00, 11:00, 12:00 in LG35/LG36

Check which lab you should attend on Blackboard in the Labs section

New Lab Pairs (if your name is missing contact me)

Attendance at all lectures, labs and tutorials is compulsory

In practice ... catch up as quickly as you can (e.g. by working through lecture notes, tutorials and lab exercises in your own time)

obtain material not available on-line from other students

inform your tutor if you miss (or will miss) a major deadline or will be absent for more than a day

Zero marks for late coursework without explanation

**You may be returned as Non-Satisfactory (see College Calendar) if you miss more than one-third of your Lectures, Labs or Tutorials, if you fail to submit more than one third of your Lab Exercises or if you fail to submit either term Assignment.**



When you submit work as part of an exercise or assignment, you are implying that it is your own work

DO indicate where you received help from someone other than a lecturer, teaching assistant or demonstrator

DO indicate where you have used other sources of information (e.g. websites or text books)

DON'T share your work with other students – in your year or any other year – you will make it harder for them to succeed in College

DO discuss your work with each other, ask another student for hints to solve problems, ask for assistance fixing bugs, etc.

DO be prepared to explain any work that you submit and expect us to use plagiarism detection tools such as TurnItIn

Taking credit for someone else's work without giving them due recognition is a serious academic offence (plagiarism, see your course handbook)

Do all of the coursework (tutorials / labs / assignments)

Do the coursework yourself

If you don't understand something, spend extra time studying it

If necessary, get help from classmates demonstrators or lecturer

Don't wait before seeking help

Don't wait for someone to tell you that ...

- you haven't handed in coursework

- you don't understand something

- you haven't been attending lectures / tutorials / labs

Don't think that if you don't understanding something now, you can fix it later before the exam ... this module doesn't work that way!

Laptops, tablets, phones etc. may not be used during lectures

There are exceptions (e.g. if you are registered with the College Disability Service and require the use of a laptop, just let me know)

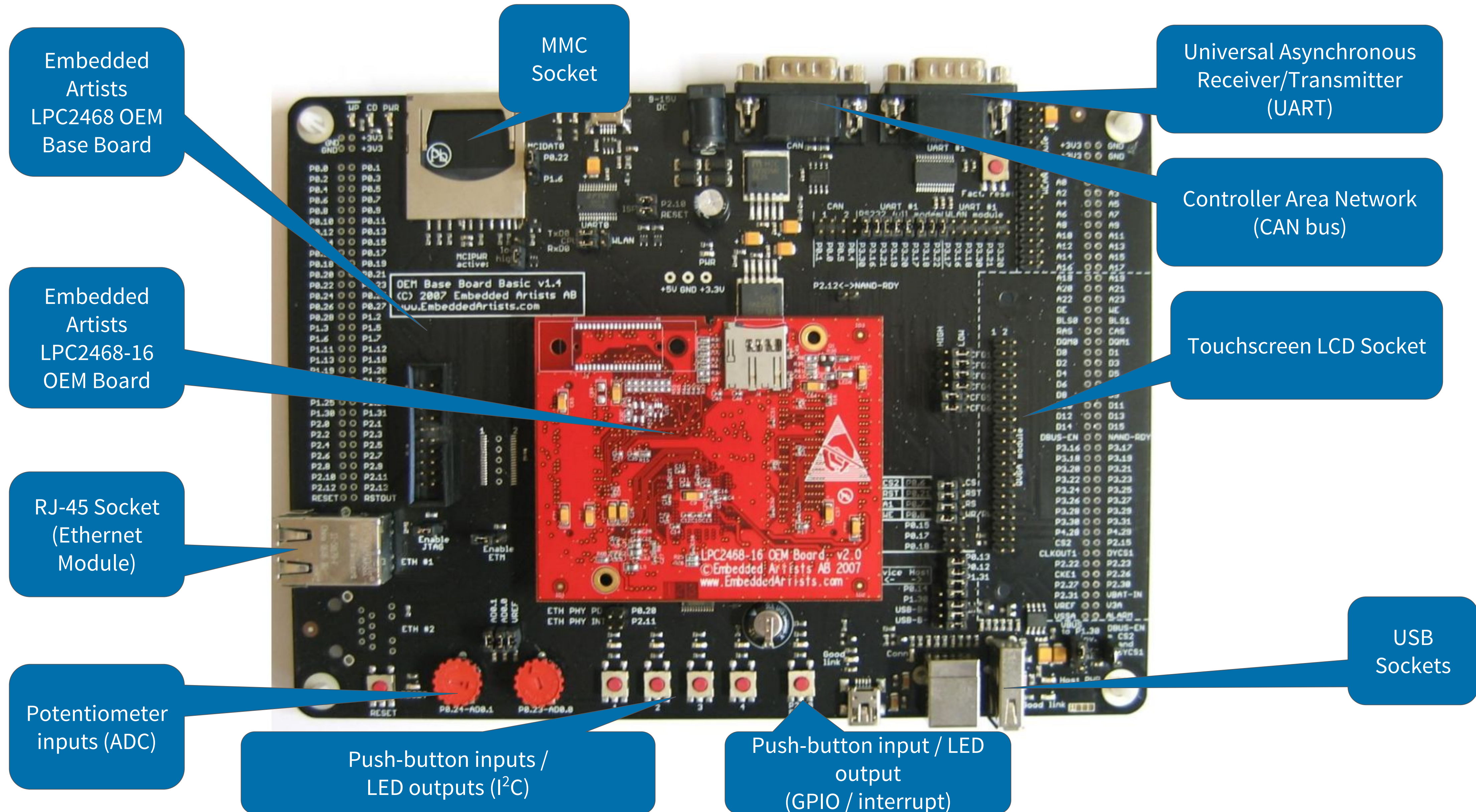
You may use a laptop / tablet during tutorials for referring to lecture material, documentation, etc. only

You may use your laptop / tablet during labs



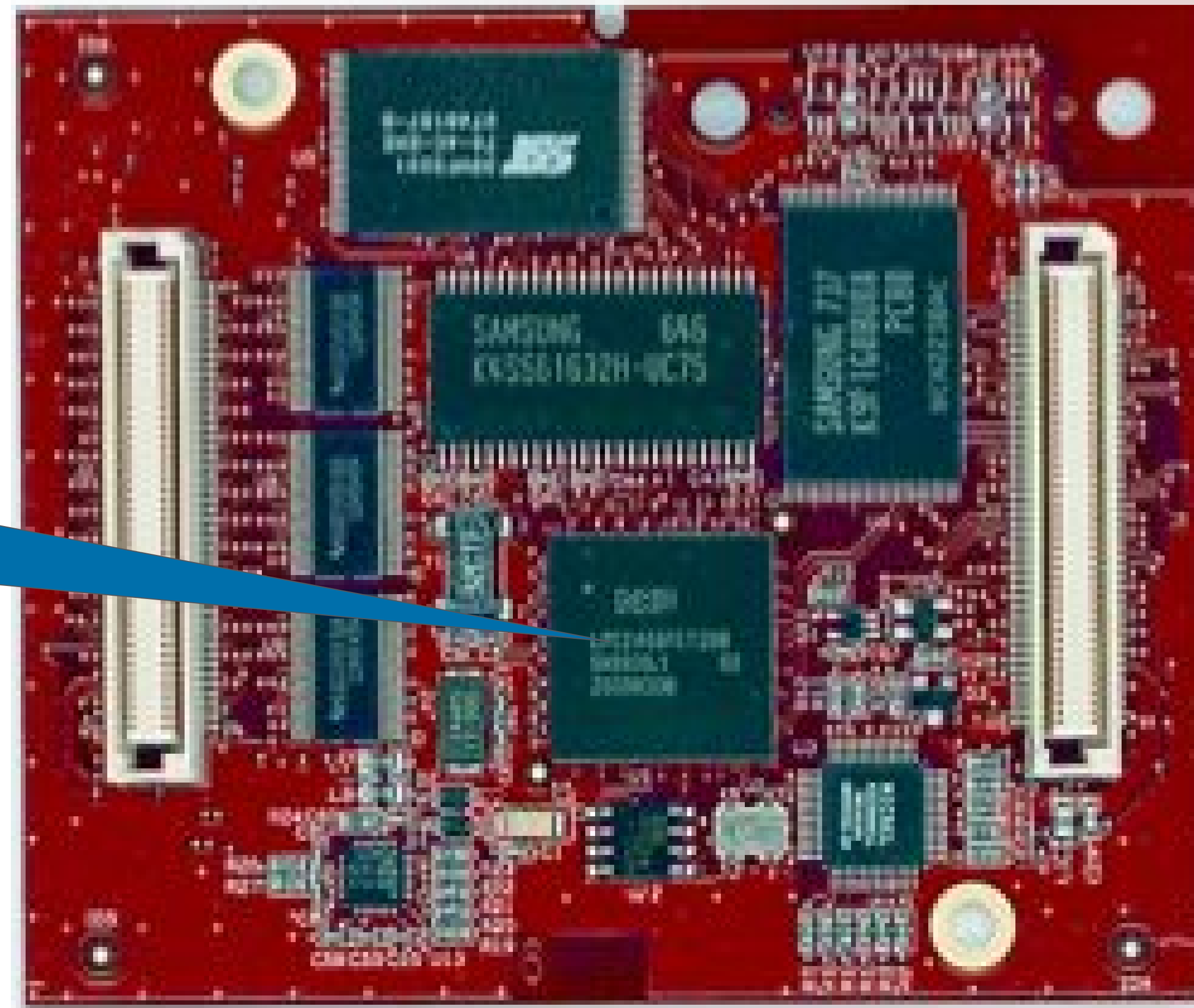
# LPC2468 Development Kit

9





NXP LPC2468  
Microcontroller



## Single chip integrates:

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- The diagram illustrates the internal architecture of the LPC2468 microcontroller. At the top, it shows the ARM7TDMI-S core connected to various system components. Key components include:
- System Functions:** Includes the PLL, system clock, and internal RC oscillator. It also manages power domains (VDD(3V3), VDDA, VDD(1V8)) and provides VREF, VSSA, VSSIO, VSSCORE, and VDD(DCDC)(3V3).
  - Test/Debug Interface:** Features ARM7TDMI-S, EMULATION, and TRACE MODULE blocks. It interfaces with TMS, TDI, TDO, and EXTIN0 pins, and provides trace signals.
  - Memory:** Includes 64 kB SRAM, 512 kB FLASH, and 16 kB SRAM. It also features a 2 kB BATTERY RAM and a REAL-TIME CLOCK.
  - Peripherals:** Includes HIGH-SPEED GPIO (160 PINS TOTAL), INTERNAL CONTROLLERS (SRAM, FLASH), ETHERNET MAC WITH DMA, USB DEVICE/HOST/OTG WITH 4 kB RAM AND DMA, GP DMA CONTROLLER, I<sup>2</sup>S INTERFACE, SPI, SSP0 INTERFACE, SSP1 INTERFACE, SD/MMC CARD INTERFACE, UART0, UART2, UART3, UART1, CAN1, CAN2, I<sup>2</sup>C0, I<sup>2</sup>C1, I<sup>2</sup>C2, WATCHDOG TIMER, and SYSTEM CONTROL.
  - Interfaces:** The diagram shows AHB2, AHB1, and AHB bridges connecting the core to various peripherals. It also includes an AHB TO APB BRIDGE and a MASTER AHB TO SLAVE PORT APB BRIDGE PORT.
  - Pins:** The diagram lists various pins including P0, P1, P2, P3, P4, EINT3 to EINT0, P0, P2, 2 x CAP0/CAP1, CAP2/CAP3, 4 x MAT2/MAT3, 2 x MAT0, 3 x MAT1, 6 x PWM0/PWM1, 1 x PCAP0, 2 x PCAP1, P0, P1, 8 x AD0, AOUT, VBAT, RTCX1, RTCX2, ALARM, XTAL1, XTAL2, VDD(3V3), VDDA, VDD(1V8), VREF, VSSA, VSSIO, VSSCORE, VDD(DCDC)(3V3), D[31:0], VBUS, port1, port2, I2SRX\_CLK, I2STX\_CLK, I2SRX\_WS, I2STX\_WS, I2SRX\_SDA, I2STX\_SDA, SCK, SCK0, MOSI, MOSI0, MISO, MISO0, SSEL, SSEL1, SCK1, MOSI1, MISO1, SSEL1, MCICLK, MCIPWR, MCICMD, MCIDAT[3:0], TXD0, TXD2, TXD3, RXD0, RXD2, RXD3, TXD1, RXD1, DTR1, RTS1, DSR1, CTS1, DCD1, R1, RD1, RD2, TD1, TD2, SCL0, SCL1, SCL2, SDA0, SDA1, SDA2.



LPC2468  
User Manual

ARM Cortex-M series (M0, M1, M3, M4, M7)

e.g. Nordic Semiconductors nRF51822

- ARM Cortex-M0 microprocessor core
- 2.4GHz radio (for Bluetooth Low Energy or ANT)
- 32kB RAM, 256kB flash memory
- Various other devices
- Integrated in a 6mm x 6mm 48-pin package (“chip”)
- Applications?

