| **Name** | **Address** | **Type** | **Reset** | **Description** |
| --- | --- | --- | --- | --- |
| REG\_NAME | 0x0000 | RW | 0x0000\_0000 | Register description |
|  |  |  |  |  |

**Note:** The bit width of the register map is 32.

**REG\_NAME 0x0000**

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **31** | **30** | **29** | **28** | **27** | **26** | **25** | **24** | **23** | **22** | **21** | **20** | **19** | **18** | **17** | **16** |
| field\_3 | | | | | | | | | | | | | | | |
| **15** | **14** | **13** | **12** | **11** | **10** | **9** | **8** | **7** | **6** | **5** | **4** | **3** | **2** | **1** | **0** |
|  | | | field\_2 |  | | field\_1 | | field  \_0 | | | | | | | |

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Field** | **Name** | **Type** | **Reset** | **Description** |
| 31:16 | field\_3 | RO | 0x0000 | Register field 3. |
| 12:12 | field\_2 | WC | 0x0 | Register field 2. |
| 9:8 | field\_1 | RW | 0x0 | Register field 1. |
| 7:0 | field\_0 | RW | 0x00 | Register field 0. |