# LCD Project

## Overview

This doc is an information appendix to LCD project – LCD1602.

In this project we would like to build a state machine that will handle the LCD, and will be integrated in the LCD.vhdl.

## State machine diagram

## Avalon-MM

|  |  |
| --- | --- |
| 0x00 | Letter 0, ROW 0 |
| 0x01 |  |
| 0x02 |  |
| . | . |
| 0X0F | Letter 15, Row 0 |
| 0x10 | Letter 0, Row 1 |
| . | . |
| . | . |
| 0x1F | Letter 15, Row 1 |

,

## UART