

[Question 1]

The IR-Virtual instructions are translated into x86 assembly code. Each IR-Virtual instruction is mapped to one or more x86 assembly instructions. This step produces executable machine code that can be run on a computer. There were several pros and cons that our group discovered while implementing, testing, and running our code. We found the IR virtual to be much easier to read than assembly. It is difficult to understand programs for solving arithmetic questions in assembly, but it is very straightforward in IR virtual which was a positive. The con that we as a group struggled with is when we wrote our programs for the IR virtual and we were trying to test it, the test cases were not as straightforward as the other test cases we have dealt with previous classes and past projects in this class. Most of the test cases were in python which was easier to implement compared to when we were trying to implement test cases in IR virtual. Overall, we had a positive experience with IR virtual even though it was hard to implement the test cases as the programs themselves were easy to write and implement.

```
1 racket compiler.rkt -v test-programs/sum1.irv
2 two.irv: ((mov-lit r0 2) (print r0))
3 sum2.irv ((mov-lit r0 10)
4   (mov-lit r1 6)
5   (mov-lit r2 0)
6   (add r2 r1)
7   (add r2 r0)
8   (print r2))
9 sum3.irv ((mov-lit r0 5)
10  (mov-lit r1 3)
11  (mov-lit r2 0)
12  (add r2 r1)
13  (add r2 r0)
14  (print r2))
```

(Also pass in -m for Mac)

[Question 2]

For this task, you will write three new .ifa programs. Your programs must be correct, in the sense that they are valid. There are a set of starter programs in the test-programs directory now. Your job is to create three new .ifa programs and compile and run each of them. It is very much possible that the compiler will be broken: part of your exercise is identifying if you can find any possible bugs in the compiler.

For each of your exercises, write here what the input and output was from the compiler. Read through each of the phases, by passing in the -v flag to the compiler. For at least one of the programs, explain carefully the relevance of each of the intermediate representations.

For this question, please add your .ifa programs either (a) here or (b) to the repo and write where they are in this file.

```
1  Arith3.ifa: (* 10 10) Output: 100
2  Arith3.asm:
3  section .data
4      int_format db "%ld",10,0
5      global _main
6      extern _printf
7  section .text
8  _start: call _main
9          mov rax, 60
10         xor rdi, rdi
11         syscall
12  _main:  push rbp
13         mov rbp, rsp
14         sub rsp, 48
15         mov esi, 10
16         mov [rbp-24], esi
17         mov esi, 10
18         mov [rbp-16], esi
19         mov esi, [rbp-24]
20         mov [rbp-8], esi
21         mov edi, [rbp-16]
22         mov eax, [rbp-8]
23         imul eax, edi
24         mov [rbp-8], eax
25         mov rax, [rbp-8]
26         jmp finish_up
27  finish_up:  add rsp, 48
28             leave
29             ret
```

```

1 Cond2.ifa: (cond [(- 10 (* 2 3)) (print 4)])
2   [( * 10 (+ 2 2)) (print 40)]
3   [else (print 5)])
4 Cond2.asm:
5 section .data
6   int_format db "%ld",10,0
7   global _main
8   extern _printf
9 section .text
10 _start: call _main
11   mov rax, 60
12   xor rdi, rdi
13   syscall
14 _main: push rbp
15   mov rbp, rsp
16   sub rsp, 304
17   mov esi, 10
18   mov [rbp-48], esi
19   mov esi, 2
20   mov [rbp-96], esi
21   mov esi, 3
22   mov [rbp-88], esi
23   mov esi, [rbp-96]
24   mov [rbp-32], esi
25   mov edi, [rbp-88]
26   mov eax, [rbp-32]
27   imul eax, edi
28   mov [rbp-32], eax
29   mov esi, [rbp-48]
30   mov [rbp-80], esi
31   mov edi, [rbp-32]
32   mov eax, [rbp-80]
33   sub eax, edi
34   mov [rbp-80], eax
35   mov esi, 0
36   mov [rbp-40], esi
37   mov edi, [rbp-40]
38   mov eax, [rbp-80]
39   cmp eax, edi
40   mov [rbp-80], eax
41   jz lab1275
42   jmp lab1277
43 lab1275: mov esi, 4
44   mov [rbp-16], esi
45   mov esi, [rbp-16]
46   lea rdi, [rel int_format]
47   mov eax, 0
48   call _printf
49   mov rax, 0
50   jmp finish_up
51 lab1277: mov esi, 10
52   mov [rbp-72], esi
53   mov esi, 2
54   mov [rbp-152], esi
55   mov esi, 2
56   mov [rbp-144], esi
57   mov esi, [rbp-152]
58   mov [rbp-136], esi
59   mov edi, [rbp-144]
60   mov eax, [rbp-136]
61   add eax, edi
62   mov [rbp-136], eax
63   mov esi, [rbp-72]
64   mov [rbp-128], esi
65   mov edi, [rbp-136]
66   mov eax, [rbp-128]
67   imul eax, edi
68   mov [rbp-128], eax
69   mov esi, 0
70   mov [rbp-104], esi
71   mov edi, [rbp-104]
72   mov eax, [rbp-128]
73   cmp eax, edi
74   mov [rbp-128], eax
75   jz lab1283
76   jmp lab1285
77 lab1283: mov esi, 40
78   mov [rbp-64], esi
79   mov esi, [rbp-64]
80   lea rdi, [rel int_format]
81   mov eax, 0
82   call _printf
83   mov rax, 0
84   jmp finish_up
85 lab1285: mov esi, 5
86   mov [rbp-120], esi
87   mov esi, [rbp-120]
88   lea rdi, [rel int_format]
89   mov eax, 0
90   call _printf
91   mov rax, 0
92   jmp finish_up
93 finish_up: add rsp, 304
94   leave
95   ret

```

```

1  2587.ifa: 2587
2  2587.asm:
3  section .data
4      int_format db "%ld",10,0
5      global _main
6      extern _printf
7  section .text
8      _start:      call _main
9                  mov rax, 60
10                 xor rdi, rdi
11                 syscall
12  _main: push rbp
13         mov rbp, rsp
14         sub rsp, 16
15         mov esi, 2587
16         mov [rbp-8], esi
17         mov rax, [rbp-8]
18         jmp finish_up
19  finish_up: add rsp, 16
20         leave
21         ret

```

[Question 3]

Describe each of the passes of the compiler in a slight degree of detail, using specific examples to discuss what each pass does. The compiler is designed in series of layers, with each higher-level IR desugaring to a lower-level IR until ultimately arriving at x86-64 assembler. Do you think there are any redundant passes? Do you think there could be more?

In answering this question, you must use specific examples that you got from running the compiler and generating an output.

Arith to IfArith:

Explanation: This step simplifies the Arith-IfArith language by removing the let construct and replacing variable references with their corresponding values.

Example Call: Given an Arith-IfArith expression `(let ([x 3]) (* (+ x 2) (- x 1)))`.

Output: The equivalent IfArith expression `(* (+ 3 2) (- 3 1))`.

IfArith to IfArith-Tiny:

Explanation: IfArith-Tiny is a simplified version of IfArith where let expressions can only have literals as bindings. This step converts IfArith expressions to IfArith-Tiny by replacing all variable bindings with literals.

Example Call: Given an IfArith expression `(+ 2 (* 3 4))`.

Output: The IfArith-Tiny equivalent `(+ 2 (let ([temp1 (* 3 4)]) temp1))`.

IfArith-Tiny to ANF:

Explanation: Administrative Normal Form (ANF) is a form where all subexpressions are simple variable bindings or primitive operations. This step converts IfArith-Tiny expressions to ANF by ensuring that all expressions are in this normalized form.

Example Call: Starting with an IfArith-Tiny expression `(let ([temp1 (* 3 4)]) (+ 2 temp1))`.

Output: The ANF representation `(let ([temp1 (* 3 4)]) (let ([temp2 (+ 2 temp1)]) temp2))`.

ANF to IR-Virtual:

Explanation: IR-Virtual is an intermediate representation language that is closer to machine code. This step converts ANF expressions to IR-Virtual instructions, which are low-level operations that closely resemble assembly language instructions.

Example Call: Taking an ANF expression `(let ([temp1 (* 3 4)]) (let ([temp2 (+ 2 temp1)]) temp2))`.

Output: IR-Virtual instructions:

```
(mov-lit temp1 12)
(mov-reg temp2 temp1)
(add temp2 2)
```

IR-Virtual to x86:

Explanation: Finally, the IR-Virtual instructions are translated into x86 assembly code. Each IR-Virtual instruction is mapped to one or more x86 assembly instructions. This step produces executable machine code that can be run on a computer.

Example Call: Converting IR-Virtual instructions to x86 assembly.

Output: Corresponding x86 assembly code:

```
section .data
    int_format db "%ld",10,0

section .text

global _main
extern printf
```

```

_main:
    push rbp
    mov rbp, rsp
    sub rsp, 16
    mov esi, 12
    mov rax, 2
    add rax, rsi
    lea rdi, [rel int_format]
    mov eax, 0
    call printf
    add rsp, 16
    leave
    ret

```

[Question 4]

This is a larger project, compared to our previous projects. This project uses a large combination of idioms: tail recursion, folds, etc.. Discuss a few programming idioms that you can identify in the project that we discussed in class this semester. There is no specific definition of what an idiom is: think carefully about whether you see any pattern in this code that resonates with you from earlier in the semester.

In this project, the main similarities between it and projects 3 and 4 are that they are all compilers in Racket used to run specifically written code. An example of something we had to do in this project was rewriting built-in racket functions using other ones that exist. For instance, within ifarith-tiny, there are implementations of functions like let*, and list mutating functions that are all written out using if statements, the let function, and other conditionals. This reminded us of the lambda encoding project where we rewrote some racket functions and constants using lambdas. Additionally, the portion of the project that we had to code the most ourselves was like both projects 3 and 4, where we were writing the built-ins as a direct style application.

[Question 5]

In this question, you will play the role of bug finder. I would like you to be creative, adversarial, and exploratory. Spend an hour or two looking throughout the code and try to break it. Try to see if you can identify a buggy program: a program that should work, but does not. This could either be that the compiler crashes, or it could be that it produces code

which will not assemble. Last, even if the code assembles and links, its behavior could be incorrect.

To answer this question, I want you to summarize your discussion, experiences, and findings by adversarially breaking the compiler. If there is something you think should work (but does not), feel free to ask me.

Your team will receive a small bonus for being the first team to report a unique bug (unique determined by me).

While working through the code and running the different types of files on the terminal, it was relatively easy to run through everything and our group was able to utilize linux commands to assist us with creating and writing files directly on the terminal. However, we did encounter some issues when trying to convert .irv and .Ira files to assembly code. First of all, we would create these files through “nano” on the terminal and write them that way, and then turn them into assembly using the racket compiler. However, when we tried to run and link the newly created assembly files, we were having trouble using nasm and ld. Although we had homebrew and the nasm package installed for x86, the terminal was giving us an error saying that it did not exist when we tried running the new files right after they were created. To fix this, we had to restart the terminal and make sure to be in the right directory for it to work. Although this may not be a bug within the code itself, we still thought it was appropriate to share since given an outlet to do so here.

[High Level Reflection]

In roughly 100-500 words, write a summary of your findings in working on this project: what did you learn, what did you find interesting, what did you find challenging? As you progress in your career, it will be increasingly important to have technical conversations about the nuts and bolts of code, try to use this experience to think about how you would approach group code critique. What would you do differently next time, what did you learn?

The project helped me understand what the different compilers are and how they are different compared to each other. What was interesting is how easy it was to implement the programs and their physical codes in IR virtual compared to assembly but on the flip side how much harder it was to implement the test cases. As a group, we had discussions on how we could find the new compilers on a personal basis and then discussed if we as a group prefer one over the other. It was interesting to have those discussions because different people have different preferences, and this was helpful in knowing what each of us liked and whether we could adapt as a group based on our strengths and weaknesses.

None of us really had any prior knowledge on how to use the other compilers and how to implement the test cases for the specific compilers to test our programs, so it was a lot of researching and learning as a group to figure out how each compiler works and how the implementations of the test cases help. It helped that we had other examples already given to us which we could use as our springboard to come up with our own programs and test cases. What we would do differently the next time around is probably spend a bit more time playing around with the compilers and see if we can find ways to translate the way we implement stuff in one compiler to a different compiler. It probably is already possible but we as a group needed more time to play around with them to understand them better.