#### Dashboard / My courses / COSC3360SP2023-01 / EXAM 1 / Theory Part - Exam 1

| Started on   | Thursday, 16 February 2023, 2:31 PM      |
|--------------|--|
| State        | Finished                                 |
| Completed on | Thursday, 16 February 2023, 2:58 PM      |
| Time taken   | 26 mins 46 secs                          |
| Grade        | <b>38.00</b> out of 50.00 ( <b>76</b> %) |
| Information  |  |

The theory part of this exam uses sequential navigation when presenting the questions. Therefore, questions must be answered the moment they are presented, as you will not have the option of going back to a previous question.

After the theory part, two programming questions will be presented.

## True or False questions (2 points each question)

| Question <b>1</b>       |                              |                       |  |  |
|-------------------------|------------------------------|-----------------------|--|--|
| Correct                 |                              |                       |  |  |
| Mark 2.00 out of 2.00   |                              |                       |  |  |
|                         |                              |                       |  |  |
| A programmed I/O use    | s interrupts to know the sta | te of the I/O device. |  |  |
| Select one:             |                              |                       |  |  |
| ○ True                  |                              |                       |  |  |
| ■ False                 |                              |                       |  |  |
|                         |                              |                       |  |  |
| The correct answer is ' | False'                       |                       |  |  |

| Question <b>2</b>   |
|---|
| Correct   |
| Mark 2.00 out of 2.00   |
|   |
| The Program Counter (PC) size determines the number of positions of the memory.             |
| Select one:   |
| True   ✓  |
| ○ False   |
|   |
| The correct answer is 'True'.   |
| The correct answer is true.   |
|   |
| Question <b>3</b>   |
| Correct   |
| Mark 2.00 out of 2.00   |
|   |
| In Paging (memory management), processes are comprised of a number of variable-size blocks. |
| Select one:   |
| ○ True  |
| False   ✓   |
|   |
|   |
| The correct answer is 'False'.  |
|   |
| Question <b>4</b>   |
| Correct   |
| Mark 2.00 out of 2.00   |
|   |
| In a serial processing system, a memory protection mechanism is needed.                     |
| Select one:   |
| ○ True  |
| <ul><li>● False ✓</li></ul>   |
|   |
|   |
| The correct answer is 'False'.  |
|   |

| Question <b>5</b>   |
|---|
| Correct   |
| Mark 2.00 out of 2.00   |
|   |
| The program counter is a User-visible register.                                   |
| Select one:   |
| ○ True  |
| False   ✓   |
|   |
| The correct answer is 'False'.  |
| The correct answer is Taise.  |
|   |
| Question <b>6</b>   |
| Correct  Mark 2.00 out of 2.00  |
| Will K 2.50 Get 61 2.50   |
|   |
| The OS uses memory tables to keep track of both main and virtual memory.          |
| Select one:   |
| True   ✓  |
| ○ False   |
|   |
| The correct answer is 'True'.   |
|   |
| _   |
| Question <b>7</b> Correct   |
| Mark 2.00 out of 2.00   |
|   |
| A benefit of threads is the enhance efficiency in communication between programs. |
| A benefit of threads is the emilance emiliency in communication between programs. |
| Select one:   |
| True   ✓  |
| ○ False   |
|   |
| The correct answer is 'True'.   |
|   |

| 1/23, 9:11 PM                                      | Theory Part - Exam 1: Attempt review                          |
|--|---|
| Question <b>8</b>                                  |   |
| Incorrect  |   |
| Mark 0.00 out of 2.00                              |   |
|  |   |
| In a User-Level Thread implementation, a           | system call will block all of the threads within the process. |
| Select one:  |   |
| ○ True   |   |
| False   ★  |   |
|  |   |
| The correct answer is 'True'.                      |   |
| Information  |   |
|  |   |
|  |   |
| Question <b>9</b>                                  |   |
| Correct  |   |
| Mark 4.00 out of 4.00                              |   |
| Select the interrupt that is classified as a       | hardware interrupt:   |
| Select one:  |   |
| a. Accessing an invalid mem address                | SS .  |
| <ul> <li>b. Illegal machine instruction</li> </ul> |   |
| <ul><li>○ c. Memory parity error</li></ul>         |   |
| Od. Division by zero                               |   |
|  |   |
| The correct answer is: Memory parity error         | or  |
|  |   |

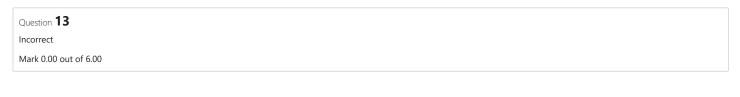
| Question 10             |  |  |  |
|-------------------------|--|--|--|
| Incorrect               |  |  |  |
| Mark 0.00 ou            | rt of 4.00   |  |  |
|                         |  |  |  |
| Select th               | e advantage of an SMP system that is related to fault tolerance: |  |  |
| Select o                | ne:  |  |  |
| О а.                    | Scaling  |  |  |
| O b.                    | Availability   |  |  |
| O c.                    | Incremental Growth   |  |  |
| O d.                    | None of the above  |  |  |
| e.                      | Performance ×  |  |  |
|                         |  |  |  |
| The corr                | oct answer is: Availability                                      |  |  |
| me com                  | ect answer is: Availability                                      |  |  |
|                         |  |  |  |
| Question 11             |  |  |  |
| Correct<br>Mark 4.00 ou | rt of 4.00   |  |  |
| Walk 4.00 00            | 11.01.4.00   |  |  |
|                         |  |  |  |
| Select th               | e element that is not part of the process control block:         |  |  |
| Select o                | ne:  |  |  |
| О а.                    | Priority   |  |  |
| b.                      | Program code♥  |  |  |
| O c.                    | State  |  |  |
| O d.                    | PID  |  |  |
| О е.                    | None of the above  |  |  |
|                         |  |  |  |
| The corr                | ect answer is: Program code                                      |  |  |

| Question 12          | 2   |
|----------------------|---|
| Correct              |   |
| Mark 4.00 o          | ut of 4.00  |
|                      |   |
| Select t             | he element that is not part of the thread in the multithreaded process model: |
| Select o             | ne:   |
| <ul><li>a.</li></ul> | User Address space ❤  |
| O b.                 | User Stack  |
| О с.                 | Thread Control Block  |
| O d.                 | None of the above   |
| О е.                 | Kernel Stack  |
|                      |   |
| The cor              | rect answer is: User Address space  |
|                      |   |
| Information          |   |
|                      |   |

# Calculate the following parameters of a hypothetical computer system with these features:

- a) HEX notation
- b) IR = OPCode + Mem Addr;
- c) # of OPCodes = 256
- d) PC = 4 HEX digits; and
- e) Mem word size = Data (unsigned integer) = IR

Note: do not enter the unit when writing your answer.



Number of HEX digits used to represent an OPCode:



The correct answer is: 2

Information

## Given the following two-level memory system:

- Level 1 memory access time = TL1
- Level 2 memory access time = TL2
- Average time to access a word from mem = 275 ms
- TL1 = TL2/100
- Miss Ratio = 0.1 (10 %)
- Time to find a word in any level of the memory (0 ms).
- Do not enter the unit when writing your answer.

### Calculate:

| Question 14   |
|---|
| Correct   |
| Mark 6.00 out of 6.00   |
|   |
| Hit Ratio in percentage (do not include the percentage sign): |

The correct answer is: 90

Answer:

```
Question 15
Correct
Mark 6.00 out of 6.00
```

Given the following code:

```
void * func(void * pointer)
{
    int *int_ptr = (int *) pointer;
    for(int i = 0; i < 6; i++)
        if (i % 2 != 0)
            *int_ptr = *int_ptr * 2;
    return NULL;
}
int main()
{
    static int x = 1;
    pthread_t tid;
    pthread_create(&tid, NULL, func, (void *) &x);
    pthread_join (tid, NULL);
    printf ("X = % d\n", x);
    return 0;
}</pre>
```

How many child threads are created by the main thread?

The correct answer is: 1

#### → Attendance

Jump to...

Programming Question 1 ►