|  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Transition | LD\_D | CL\_D | LD\_IR | CL\_IR | LD\_AC | CL\_AC | Add/Sub | R/W | Mem\_En | D/PC | LD\_PC | PC\_CNT | CL\_PC |
| Any -> S0 |  |  |  |  |  |  |  |  |  | 1 |  |  |  |
| S0 -> S1 |  |  | 1 |  |  |  |  |  |  | 1 |  | 1 |  |
| S1 -> S2 |  |  |  |  |  |  |  |  |  |  |  |  |  |
| S2 -> S5 |  |  |  |  |  |  |  |  |  | 1 |  |  |  |
| S2 -> S3 |  |  |  |  |  | 1 |  |  |  | 1 |  |  |  |
| S3 -> S4 | 1 |  |  |  |  |  |  |  |  | 1 |  |  |  |
| S4 -> S5 Add |  |  |  |  | 1 |  | 1 |  |  | 1 |  | 1 |  |
| S4 -> S5 Sub |  |  |  |  |  |  |  |  |  |  |  |  |  |
| S4 -> S5 Store |  |  |  |  |  |  |  |  |  |  |  |  |  |
| S4 -> S5 BNZ (Z = 1) |  |  |  |  |  |  |  |  |  |  |  |  |  |
| S4 -> S5 BNZ (Z = 0) |  |  |  |  |  |  |  |  |  |  |  |  |  |
| S5 -> I |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |