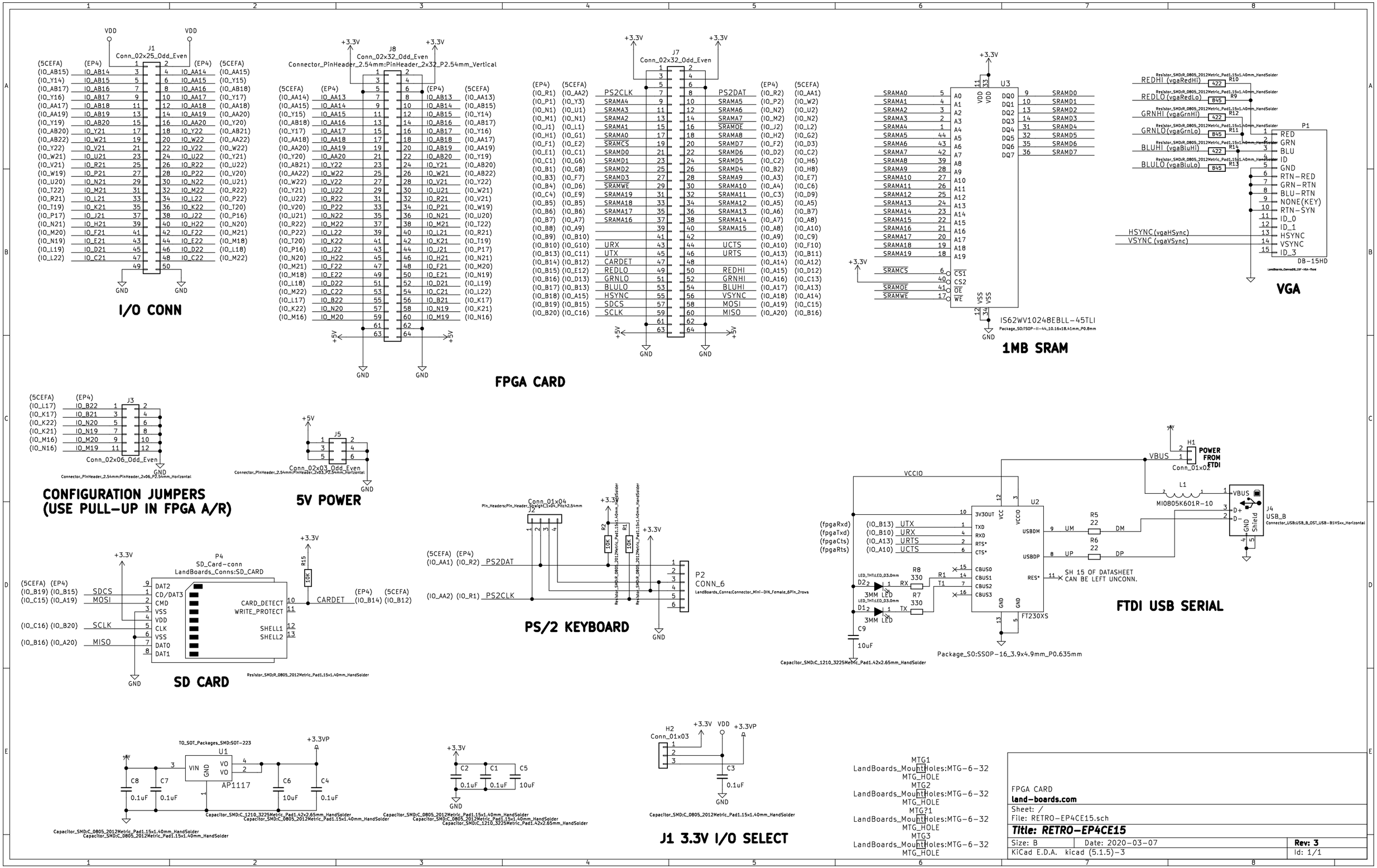
I/O CONN
J1
Conn_02x25_Odd_Even
FPGA CARD
J8
Conn_02x32_Odd_Even
Connector_PinHeader_2.54mm:PinHeader_2x32_P2.54mm_Vertical
J7
Conn_02x32_Odd_Even
1MB SRAM
U3
IS62WV10248EBLL-45TLI
Package_SO:TSOP-II-44_10.16x18.41mm_P0.8mm
VGA
P1
DB-15HD
CONFIGURATION JUMPERS
(USE PULL-UP IN FPGA A/R)
J3
Conn_02x06_Odd_Even
5V POWER
J5
Conn_02x03_Odd_Even
SD CARD
P4
SD_Card-conn
LandBoards_Conns:SD_CARD
PS/2 KEYBOARD
Conn_01x04
J2
P2
CONN_6
FTDI USB SERIAL
U2
FT230XS
Package_SO:SSOP-16_3.9x4.9mm_P0.635mm
POWER FROM FTDI
H1
Conn_01x02
J4
USB_B
MIOB05K601R-10
SH 15 OF DATASHEET
CAN BE LEFT UNCONN.
J1 3.3V I/O SELECT
H2
Conn_01x03
AP1117
U1
MTG1
LandBoards_MountHoles:MTG-6-32
MTG_HOLE
MTG2
MTG?1
MTG3
FPGA CARD
land-boards.com
Sheet: /
File: RETRO-EP4CE15.sch
Title: RETRO-EP4CE15
Size: B
Date: 2020-03-07
Rev: 3
KiCad E.D.A. kicad (5.1.5)-3
Id: 1/1