**R32V2020 Programmer’s Reference Card**

|  |  |  |  |
| --- | --- | --- | --- |
| **Category** | **Name** | **Format** | **Syntax** |
| System | No Operation | NO\_ARGS | nop |
| Halt and Catch Fire | hcf |
| Reset | res |
| Arithmetic | Add | BIN\_DEST | ads rd,rs2,rs1 |
| Multiply | BIN\_DEST | ads rd,rs2,rs1 |
| Logical | Shift left by 1 | UN\_DEST | ls1 rd,rs1 |
| Shift left by 8 | UN\_DEST | ls8 rd,rs8 |
| Shift right by 1 | UN\_DEST | rs1 rd,rs1 |
| Shift right by 8 | UN\_DEST | rs8 rd,rs8 |
| Rotate left by 1 | UN\_DEST | lr1 rd,rs1 |
| Rotate right by 1 | UN\_DEST | rr1 rd,rs1 |
| Arithmetic Shift right by 1 | UN\_DEST | ra1 rd,rs1 |
| Compare | Compare | BIN\_CMP | cmp rs2,rs1 |
| Swap | Swap Endian | UN\_DEST | ens rd,rs1 |
| Immediate | Load immediate lower | IMM\_DEST | lil rd,imm |
| Load immediate upper | IMM\_DEST | liu rd,imm |
| Load immediate extended | IMM\_DEST | lix rd,imm |
| Load/Stores Data | Load Data Byte | R6\_DEST | ldb rd |
| Load Data Short | R6\_DEST | lds rd |
| Load Data Long | R6\_DEST | ldl rd |
| Store Data Byte | UN\_R6\_DEST | sdb rs1 |
| Store Data Short | UN\_R6\_DEST | sds rs1 |
| Store Data Long | UN\_R6\_DEST | sdl rs1 |
| Load/Stores Peripheral | Load Peripheral Byte | R5\_DEST | lpb rd |
| Load Peripheral Short | R5\_DEST | lps rd |
| Load Peripheral Long | R5\_DEST | lpl rd |
| Store Peripheral Byte | UN\_R5\_DEST | spb rs1 |
| Store Peripheral Short | UN\_R5\_DEST | sps rs1 |
| Store Peripheral Long | UN\_R5\_DEST | spl rs1 |
| Stack | Push to stack | UN\_R4\_DEST | pss rs1 |
| Pull from stack | R5\_DEST | pus rd |
| Store to stack | UN\_R4\_DEST | sss rs1 |
| Load from stack | R5\_DEST | lss rd |
| Branches | Branch Always | ADDR | bra addr |
| Branch if equal to zero | ADDR | bez addr |
| Branch if equal to one | ADDR | be1 addr |
| Branch if not zero | ADDR | bnz addr |
| Branch if carry clear | ADDR | bcc addr |
| Branch if carry set | ADDR | bcs addr |
| Branch if less than | ADDR | blt addr |
| Branch if greater than | ADDR | bgt addr |
| Branch if equal | ADDR | beq addr |
| Branch if not equal | ADDR | bne addr |
| Branch to subroutine | ADDR | bsr addr |
| Return from subroutine | R7\_DEST | rts |

**Instruction Format**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Format** | **D31..D24** | **D23..D20** | **D19..D16** | **D15..D12** | **D11..D00** |
| ADDR | OPCODE | offset | | | |
| BIN\_CMP | OPCODE | X | rs1 | rs1 | X |
| BIN\_DEST | OPCODE |  |  |  |  |
| IMM\_DEST | OPCODE | rd | Immed (20-bits) | | |
| NO\_ARGS | OPCODE | X | X | X | X |
| R4\_DEST | OPCODE | rd | X | (r4) | X |
| R5\_DEST | OPCODE | rd | X | (r5) | X |
| R6\_DEST | OPCODE | rd | X | (r6) | X |
| R7\_DEST | OPCODE | rd | X | (r7) | X |
| UN\_DEST | OPCODE | rd | X | rs1 | X |
| UN\_R4\_DEST | OPCODE | (r4) | X | rs1 | X |
| UN\_R5\_DEST | OPCODE | (r5) | X | rs1 | X |
| UN\_R6\_DEST | OPCODE | (r6) | X | rs1 | X |