# SKB501 User Manual

### **General Description**

### Ready for Bluetooth 5 and high grade IoT security

The SKB501 is an advanced, highly flexible single chip solution for today's increasingly demanding ULP wireless applications for connected devices on our person, connected living environments and the IoT at large. It is designed ready for the major feature advancements of Bluetooth® 5 and takes advantage of Bluetooth 5's increased performance capabilities which include long range and high throughput modes. Inherent industry-grade security is essential in today's applications.

### Bluetooth 5 – Bluetooth low energy further and faster

The nRF52840 is ready to take advantage of the considerable performance improvements for Bluetooth low energy with the arrival of the Bluetooth 5 specification. Of greatest importance is the support for longer range (up to x4 compared to Bluetooth 4.x) and doubling of on-air data-rate, up to 2Mbs from 1Mbs in Bluetooth 4.x

### Wide protocol support with addition of 802.15.4

The 802.15.4 PHY and MAC layers are supported natively on the nRF52840. This allows nRF52840 to be used in a wide range of home and industrial sensor network applications as it supports two of the most popular wireless sensor standards in use today, Bluetooth low energy and 802.15.4 derivatives. This adds to the already existing radio support for Bluetooth low energy.

### **Applications**

Computer peripherals and I/O devices

Mouse

Keyboard

Multi-touch trackpad

◆ Interactive entertainment devices



Remote control

3D Glasses

Gaming controller

Advanced wearables

Connected watches

Advanced personal fitness devices

Wearables with wireless payment

Connected Health

Virtual/Augmented Reality applications

◆ IoT

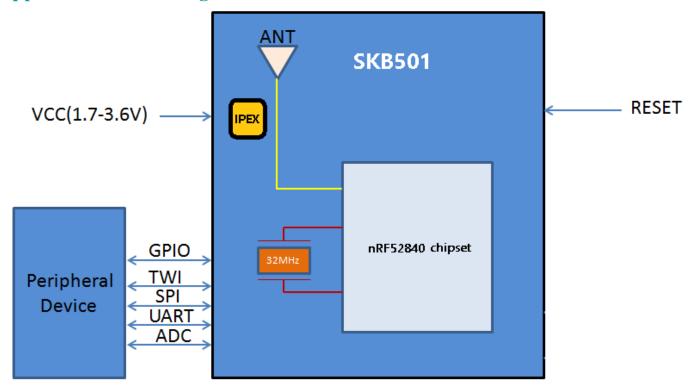
Smart Home sensors and controllers Industrial IoT sensors and controllers

#### **Features**

- ◆ Bluetooth 5 ready multi-protocol radio
- ◆ Bluetooth 5 datarate support: 2Mbs, 1Mbs, 500Kbs, 125Kbs
- ◆ 32-bit ARM Cortex-M4F @ 64MHz
- ◆ Up to 111 dB link budget for Bluetooth long range mode
- ◆ Programmable output power from +8dBm to -20dBm
- -96dBm Sensitivity for Bluetooth low energy
- RSSI
- ◆ Wide supply voltage range +5.5v to 1.7v
- ◆ Full selection of interfaces SPI/UART/PWM
- ◆ Programmable Peripheral Interface PPI
- ◆ High speed SPI interface 32MHz
- EasyDMA for all digital interfaces
- ◆ 12bit/200K SPS ADC
- ◆ 128 bit AES/ECB/CCM/AAR co-processor
- ◆ 20 General Purpose I/O pins
- ♦ SPI Master/Slave
- ◆ Two-wire Master (I2C compatible)
- ◆ UART (CTS/RTS)
- ◆ RoHS compliance (Lead-free)
- ◆ FCC,CE compliance



### **Application Block Diagram**



SKB501 Block Diagram

### **Interfaces**

### **Power Supply**

Regulated power for the SKB501 is required. The input voltage Vcc range should be 1.7V to 3.6V. Suitable decoupling must be provided by external decoupling circuitry (10uF and 0.1uF). It can reduce the noise from power supply and increase power stability.

### **System Function Interfaces**

#### **GPIOs**

The general purpose I/O is organized as one port with up to 20 I/Os enabling access and control of up to 19 pins through one port. Each GPIO can be accessed individually with the following user configurable features:

- 1. Input/output direction
- 2. Output drive strength
- 3. Internal pull-up and pull-down resistors
- 4. Wake-up from high or low level triggers on all pins
- 5. Trigger interrupt on all pins
- 6. All pins can be used by the PPI task/event system; the maximum number of pins that



can be interfaced through the PPI at the same time is limited by the number of GPIOTE channels

- 7. All pins can be individually configured to carry serial interface or quadrature demodulator signals
- 8. All pins can be configured as PWM signal.
- 9. There are 6 ADC/LPCOMP input in the 20 I/Os.

### Two-wire Interface (I2C Compatible)

The two-wire interface can communicate with a bi-directional wired-AND bus with two lines (SCL, SDA). The protocol makes it possible to interconnect up to 127 individually addressable devices. The interface is capable of clock stretching, supporting data rates of 100 kbps ,250kbps and 400 kbps. The module has 2 TWI ports and they properties like following table.

Instance	Master/Slave
TWI0	Master
TWI1	Master

**TWI Pin Share Scheme** 

**Note:** I2C:Inter—Integrated Circuit

### Flash Program I/Os

The module has two programmer pins, respectively SWDCLK pin and SWDIO pin. The two pin Serial Wire Debug (SWD) interface provided as a part of the Debug Access Port (DAP) offers a flexible and powerful mechanism for non-intrusive debugging of program code. Breakpoints and single stepping are part of this support.

### Serial Peripheral Interface

The SPI interfaces enable full duplex synchronous communication between devices. They support a three-wire (SCK, MISO, MOSI) bi-directional bus with fast data transfers. The SPI Master can communicate with multiple slaves using individual chip select signals for each of the slave devices attached to a bus. Control of chip select signals is left to the application through use of GPIO signals. SPI Master has double buffered I/O data. The SPI Slave includes EasyDMA for data transfer directly to and from RAM allowing Slave data transfers to occur while the CPU is IDLE. The GPIOs are used for each SPI interface line can be chosen from any GPIOs on the device and configed independently. This enables great flexibility in device pinout and efficient use of printed circuit board space and signal routing.

The SPI peripheral support SPI mode 0,1,2,and 3. The module have 3 SPI ports and theirs they properties are as below:

Instance	Master/Slave
SPI0	Master
SPI1	Master
SPIS1	Slave

**SPI Properties** 

### **UARTs**

The Universal Asynchronous Receiver/Transmitter offers fast, full-duplex, asynchronous serial communication with built-in flow control (CTS, RTS), support in hardware up to 1 Mbps baud. Parity checking is supported.

Support the following baudrate in bps unit:

1200/2400/4800/9600/14400/19200/28800/38400/57600/76800/115200.

**Note:** The GPIOs are used for each SPI/TWI/UART interface line can be chosen from any GPIOs on the device and configed independently.

### Analog to Digital Converter (ADC)

The 12 bit incremental Analog to Digital Converter (ADC) enables sampling of up to 8 external signals through a front-end multiplexer. The ADC has configurable input and reference prescaling, and sample resolution (8,10, and 12 bit).

**Note:** The ADC module uses the same analog inputs as the LPCOMP module. Only one of the modules can be enabled at the same time.

SKB501 Pin Number	Pin Number	Description
6	P0.28	Digital I/O; Analog
7	P0.29	Digital I/O; Analog
8	P0.30	Digital I/O; Analog
9	P0.31	Digital I/O; Analog
11	P0.02	Digital I/O; Analog
12	P0.03	Digital I/O; Analog

**ADC Pins** 

### Low Power Comparator (LPCOMP)

In System ON, the block can generate separate events on rising and falling edges of a signal, or sample the current state of the pin as being above or below the threshold. The block can be configured to use any of the

analog inputs on the device. Additionally, the low power comparator can be used as an analog wakeup source from System OFF or System ON. The comparator threshold can be programmed to a range of fractions of the supply voltage.

### Reset

The reset pin of the SKB501 module is in the internal pull-high state, when the reset pin of the module is input to a low level, the module will be automatically reset. After the reset pin is used, the param eters of the current setting will not be ANT.

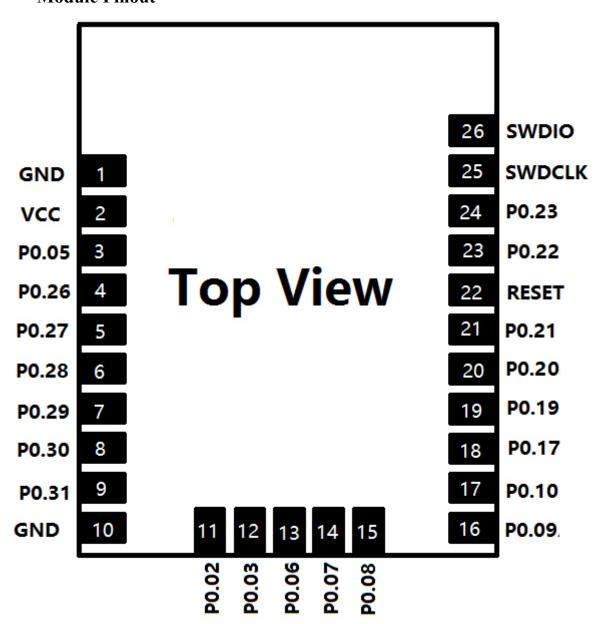
### **Module Specifications**

Hardware Features		
Model	SKB501, SKB501-CSPI, SKB501-XXPI, SKB501-CSEI, SKB501-XXEI	
ANTenna Type	PCB Antenna	
<b>Chipset Solution</b>	nRF52840	
Voltage	1.7V~3.6V	
Dimension(L×W×H)	17.4×13.7×1.9 mm	
Wireless Features		
Wireless Standards	Bluetooth 5	
Frequency Range	2400MHz2483.5MHz	
Data Rates	1Mbps(Bluetooth 5)	
Modulation Technique	GFSK Modulation(Bluetooth 5)	
Wireless Security	AES HW Encryption	
Transmit Power	Tx Power -20 to +8 dBm in 4 dB Steps(Bluetooth ® 4.2)	
Work Mode	Central/Peripheral(Bluetooth ® 4.2)	
Others		
Certification	RoHS	
Environment	Operating Temperature: -40 °C ~85 °C	
	Storage Temperature: -40 °C~125 °C	
	Operating Humidity: 10%~90% Non-condensing	
	Storage Humidity: 5%~90% Non-condensing	



# **Module Pinout and Pin Description**

### **Module Pinout**



**SKB501 Module Pinout** 

### 7.2 Pin Description

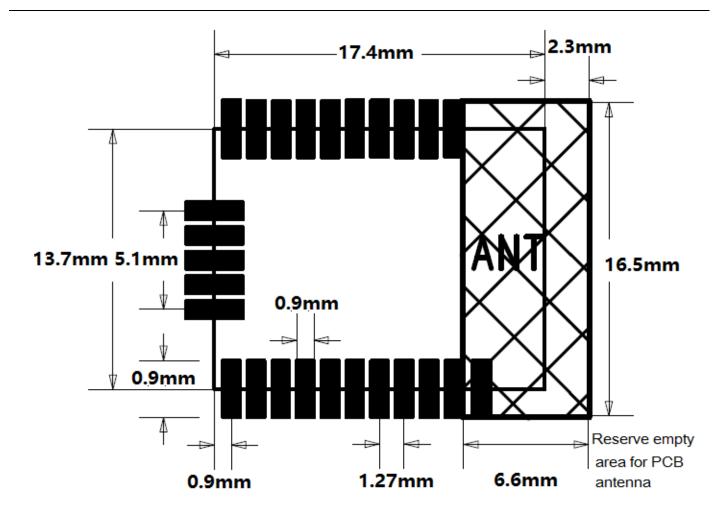
Pin No.	Pin Name	Description	Remark
1	GND	Ground	
2	VCC	Main Power Supply	1.7V to 3.6V
3	P0.05	Digital I/O; UART RTS	Digital I/O
4	P0.26	Digital I/O; I2C_SDA	Digital I/O



5	P0.27	Digital I/O; I2C_SCL	Digital I/O
	T		
6	P0.28	Digital I/O; Analog input	ADC/LPCOMP input 4
7	P0.29	Digital I/O; Analog input	ADC/LPCOMP input 5
8	P0.30	Digital I/O; Analog input	ADC/LPCOMP input 6
9	P0.31	Digital I/O; Analog input	ADC/LPCOMP input 7
10	GND	Ground	
11	P0.02	Digital I/O; Analog input	ADC/LPCOMP input 0
12	P0.03	Digital I/O; Analog input	ADC/LPCOMP input 1
13	P0.06	Digital I/O; UART TXD	Digital I/O
14	P0.07	Digital I/O; UART CTS	Digital I/O
15	P0.08	Digital I/O; UART RXD	Digital I/O
16	P0.10	Digital I/O;	Digital I/O;
17	P0.09	Digital I/O;	Digital I/O;
18	P0.17	Digital I/O; SPI_CS	Digital I/O
19	P0.19	Digital I/O; SPI_CLK	Digital I/O
20	P0.20	Digital I/O; SPI_D0	Digital I/O
21	P0.21	Digital I/O; SPI_D1	Digital I/O
22	RESET	System Reset (Active low)	Reset
23	P0.22	Digital I/O; SPI_D2	Digital I/O
24	P0.23	Digital I/O; SPI_D3	Digital I/O
25	SWDCLK	Hardware debug and Flash program I/O	Digital input
26	SWDIO	Hardware Debug and Flash Program I/O	Digital I/O

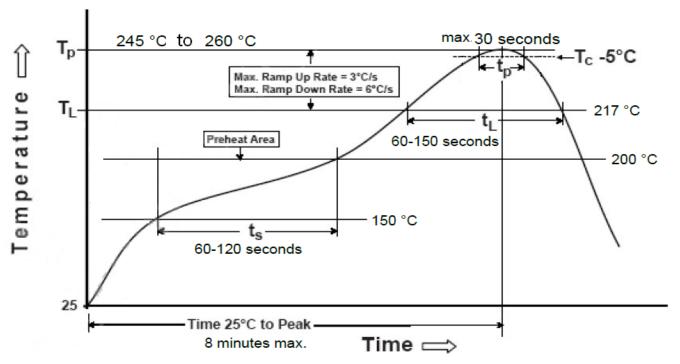
# **PCB Footprint and Dimensions**





**SKB501 Recommended PCB Footprint** 

# **Manufacturing Process Recommendations**



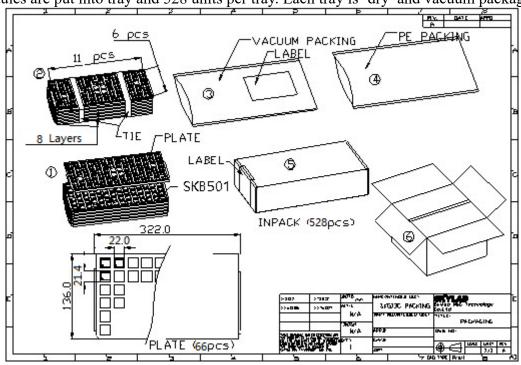


### **SKB501 Typical Lead-free Soldering Profile**

**Note:** The final re-flow soldering temperature map chosen at the factory depends on additional external factors, for example, choice of soldering paste, size, thickness and properties of the module's baseboard etc. Exceeding the maximum soldering temperature in the recommended soldering profile may permanently damage the module.

### **Packaging Specification**

SKB501 modules are put into tray and 528 units per tray. Each tray is 'dry' and vacuum packaging.



SKB501 Packaging



#### CE NB

Herby, Skylab M&C Technology Co., Ltd. declares that this Bluetooth module, SKB501 is in compliance with the essential requirements and other relevant provisions of Directive 2014/53/EU. In accordance with Article 10(2) and Article 10(10), this product allowed to be used in all EU member states.

Use the Bluetooth module in the environment with the temperature between -20 °C and 85 °C,

Operation Frequency: 2402MHz~2480MHz

Max output power: 0.0009W

Manufacturer: Skylab M&C Technology Co., Ltd.

Address: 6/F, Building 9, Lijincheng park, Gongye East Rd, Longhua St, Longhua District, Shenzhen 518109, China

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Fax: 0755-83458560

E-mail: anna.lvshan@skylab.com.cn

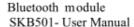
#### FCC Statement

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- -- Reorient or relocate the receiving antenna.
- -- Increase the separation between the equipment and receiver.
- -- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- -- Consult the dealer or an experienced radio/TV technician for help

This device complies with part 15 of the FCC rules. Operation is subject to the following two conditions (1)this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.





The module itself not have its own shielding, in order to ensure it complies all requirements, the host in which the module will be installed must provide the shielding

#### FCC RF Radiation Exposure Statement:

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment.

#### Notice to OEM integrator

The end user manual shall include all required regulatory information/warning as show in this manual. The OEM integrator is responsible for testing their end-product for any additional compliance requirements required with this module installed.

If the FCC ID is not visible when the module is installed inside another device, then the outside of the device into which the module is installed must also display a label referring to the enclosed module. The end product shall haves the words "Contains Transmitter Module FCC ID: 2ACOE-SKB501".

### The device must be professionally installed

The intended use is generally not for the general public. It is generally for industry/commercial use. The connector is within the transmitter enclosure and can only be accessed by disassembly of the transmitter that is not nomally required. the user has no access to the connector. Installation must be controlled. Installation requires special training

#### Declaration of Conformity

#### I hereby declare that the product

#### Product:

Product Name: Bluetooth module

Model: SKB501, SKB501-CSPI, SKB501-XXPI, SKB501-CSEI, SKB501-XXEI

Brand Name: N/A Hardware Version: V1.0 Software Version: B0171V101

satisfies all the technical regulations applicable to the product within the scope of Council

Directives 2014/35/EU, 2014/30/EU and 2014/53/EU:

Draft ETSI EN 300 328 V2.2.0 (2017-11)
Draft ETSI EN 301 489-17 V3.2.0 (2017-03)

Draft ETSI EN 301 489-1 V2.2.0 (2017-03)

EN 62479:2010

EN 60950-1:2006+A11:2009+A1:2010+A12:2011+A2:2013

(Title(s) of regulations, standards, etc.)

All essential radio test suites have been carried out.

#### NOTIFIED BODY: PHOENIX TEST-LAB GmbH

Address:

Köningswinkel 10
D-32825 Blomberg
Germany
Identification Number: 0700

#### MANUFACTURER or AUTHORISED REPRESENTATIVE:

Address:

Skylab M&C Technology Co., Ltd.

6/F, Building 9, Lijincheng park, Gongye East Rd, Longhua St, Longhua District, Shenzhen 518109, China

This declaration is issued under the sole responsibility of the manufacturer and, if applicable, his authorized representative.

Signature:

Sanchen.

Mar. 28, 2019

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Position	manager
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