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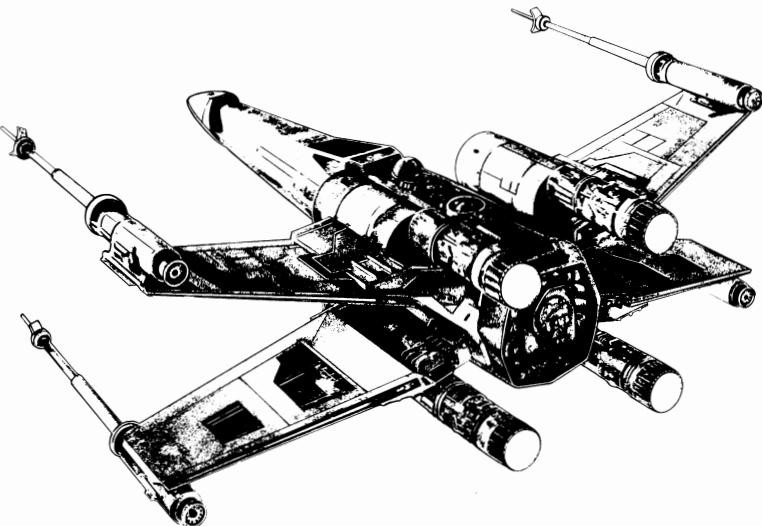
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NOTE

A STAR WARS Troubleshooting Guide is included as part of this Schematic Package Supplement. The STAR WARS Troubleshooting Guide contains Diagnostic Hardware tests and Atari CAT Box troubleshooting procedures.



NOTE
This staple temporarily holds the schematic package together. Remove the staple before using these schematics.

Schematic Package Supplement to

**STAR^{*}
WARS**

Operators Manual

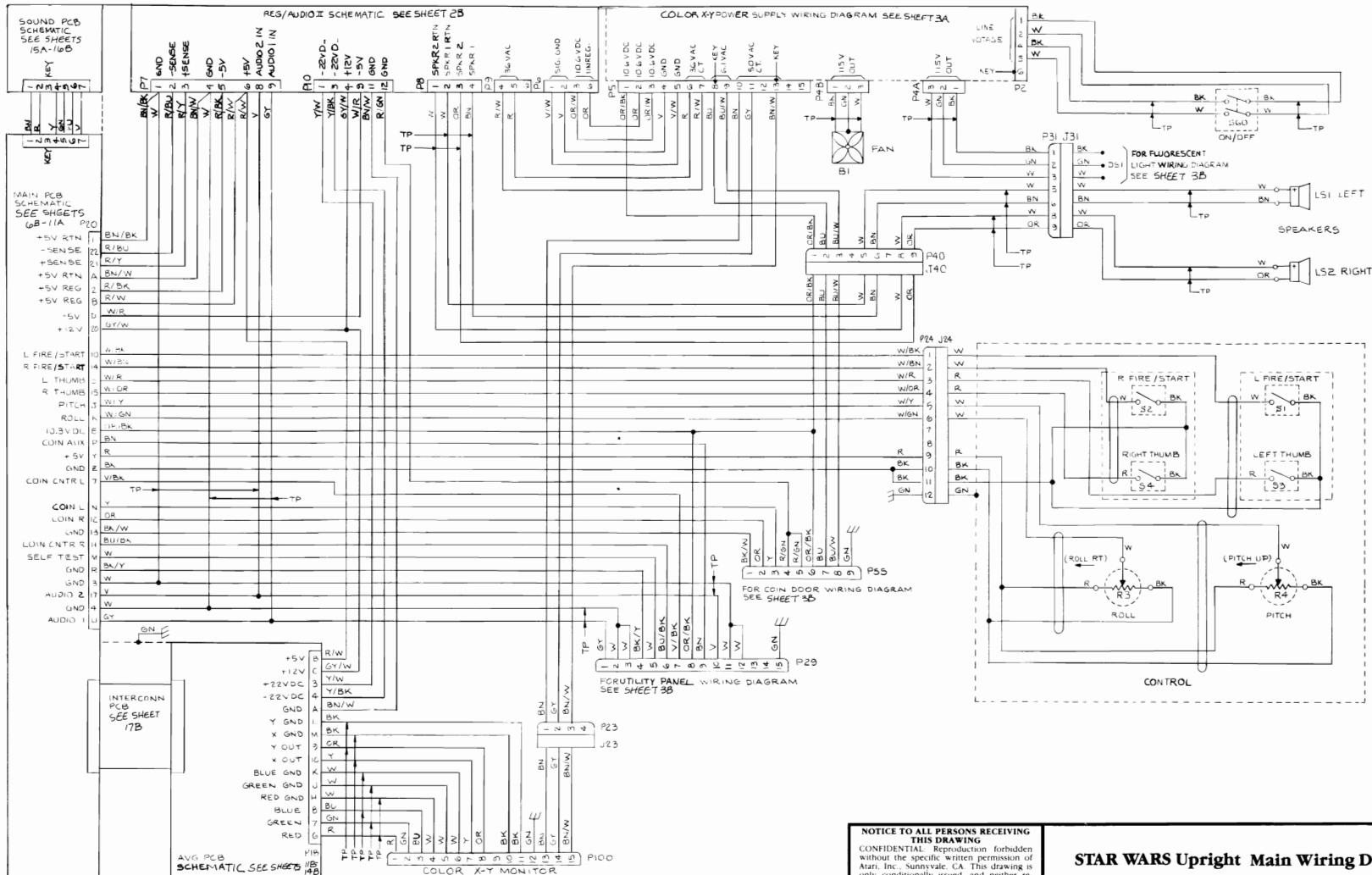
Includes Troubleshooting Guide

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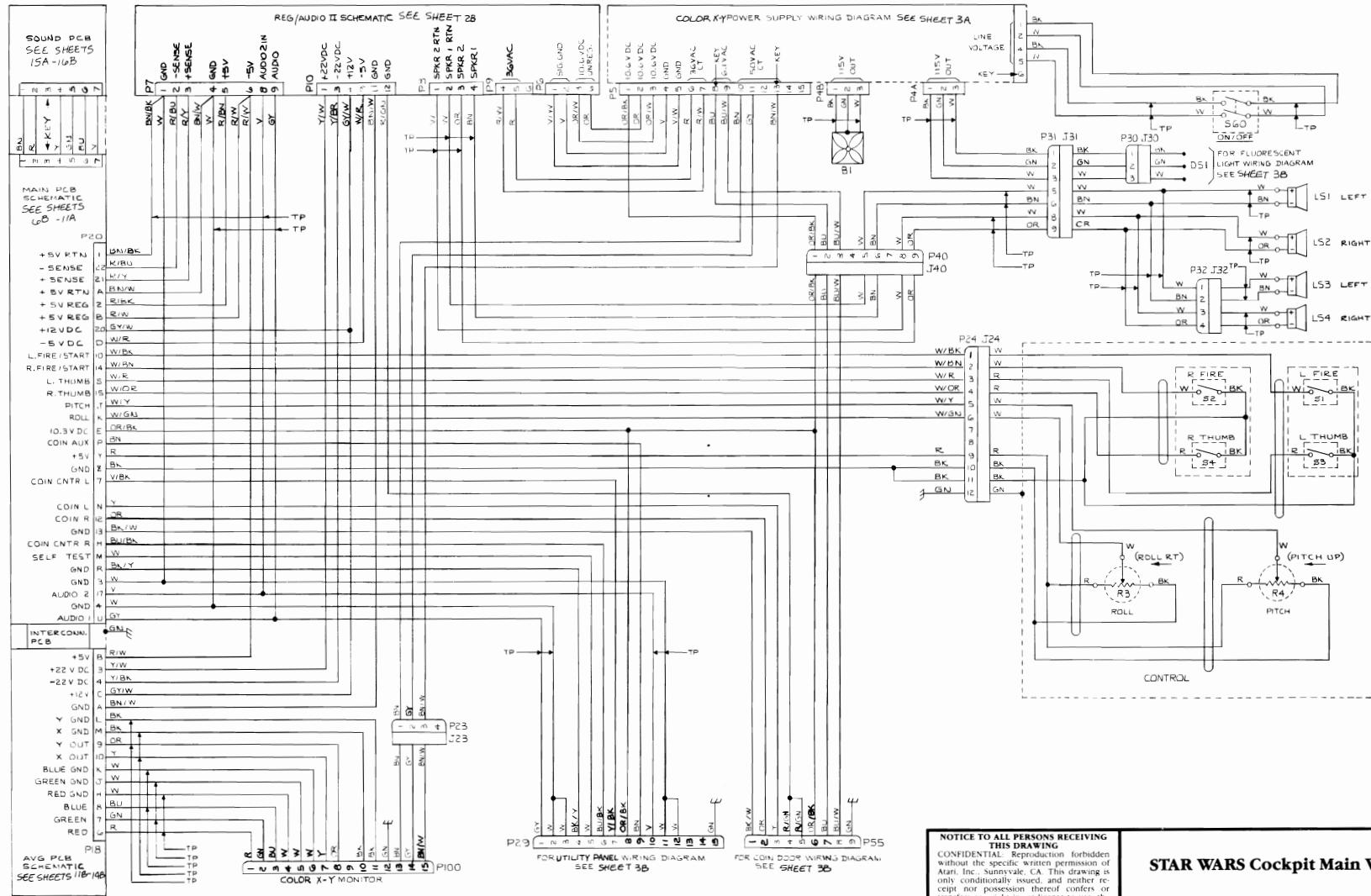
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STAR WARS Upright Main Wiring Diagram



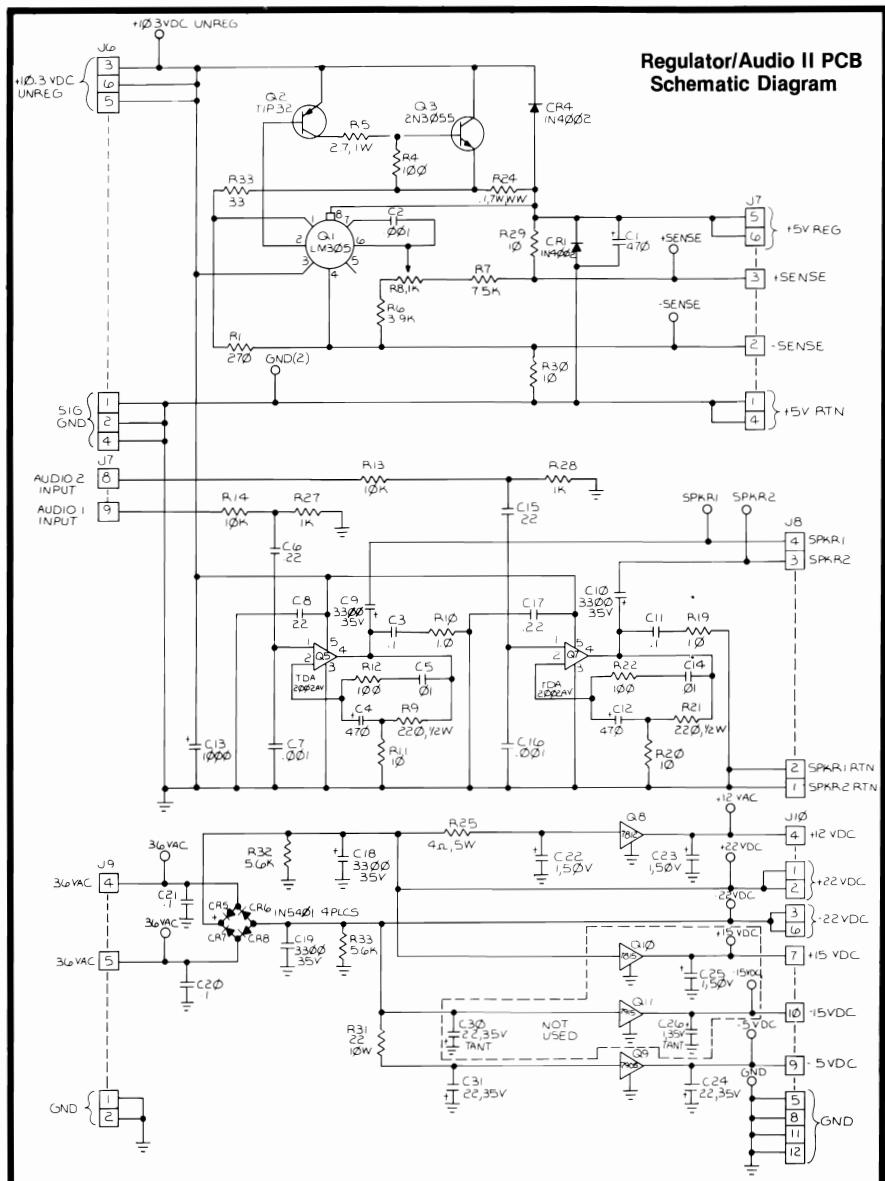
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STAR WARS Cockpit Main Wiring Diagram



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STAR WARS Regulator/Audio II PCB

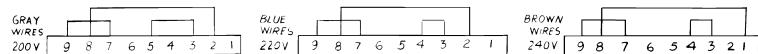


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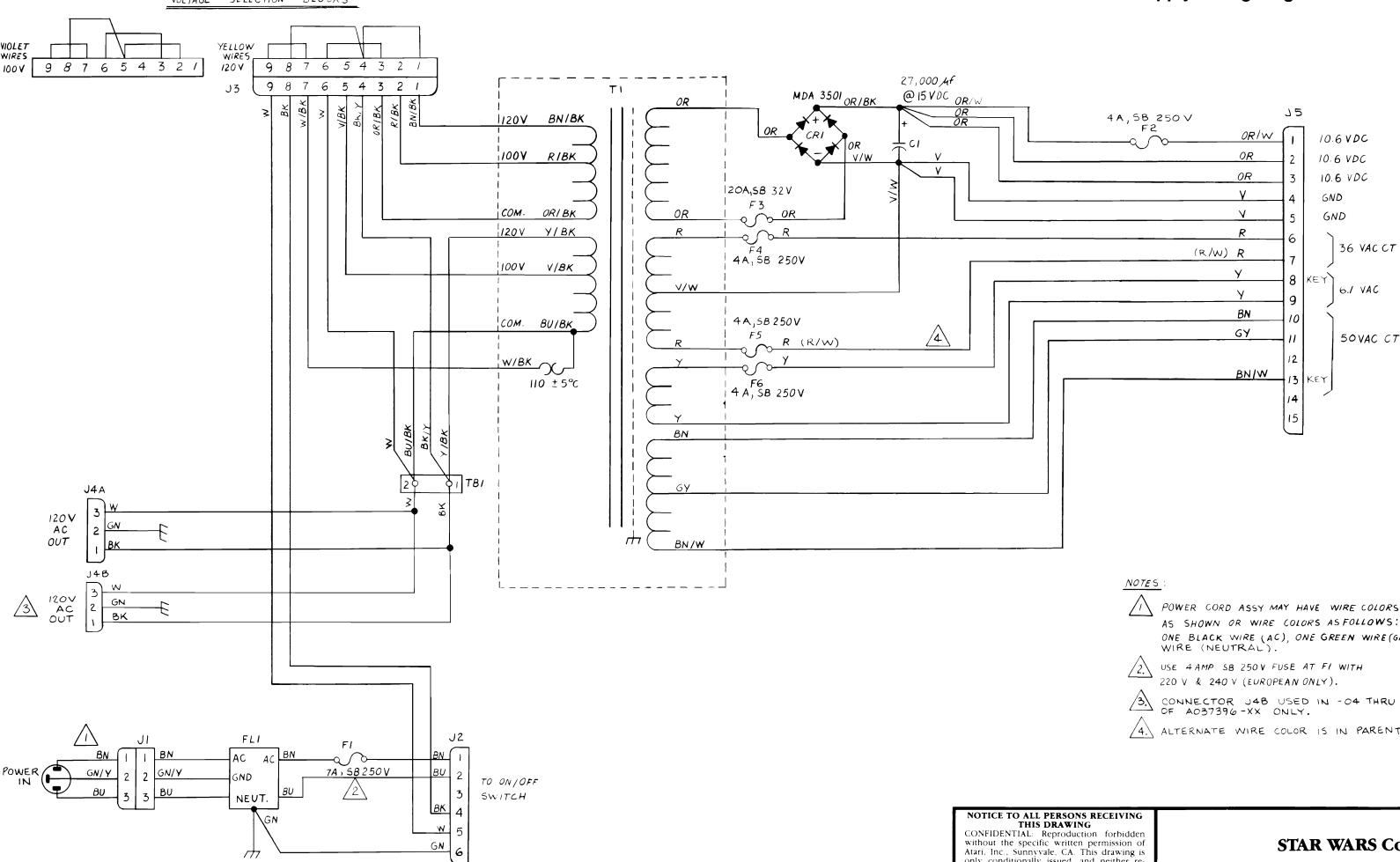
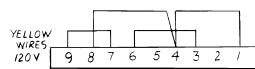
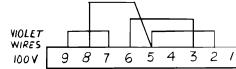
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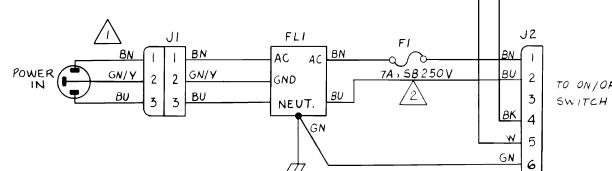


VOLTAGE SELECTION BLOCKS



NOTES:

- POWER CORD ASSY MAY HAVE WIRE COLORS AS SHOWN OR WIRE COLORS AS FOLLOWS:
ONE BLACK WIRE (AC), ONE GREEN WIRE(GND), & ONE WHITE WIRE (NEUTRAL).
- USE 4 AMP SB 250V FUSE AT F1 WITH 220 V & 240 V (EUROPEAN ONLY).
- CONNECTOR J4B USED IN -04 THRU -06 VERSIONS OF A037396-XX ONLY.
- ALTERNATE WIRE COLOR IS IN PARENTHESES.



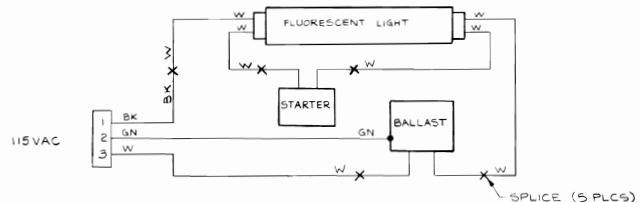
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STAR WARS Color X-Y
Power Supply Wiring Diagram

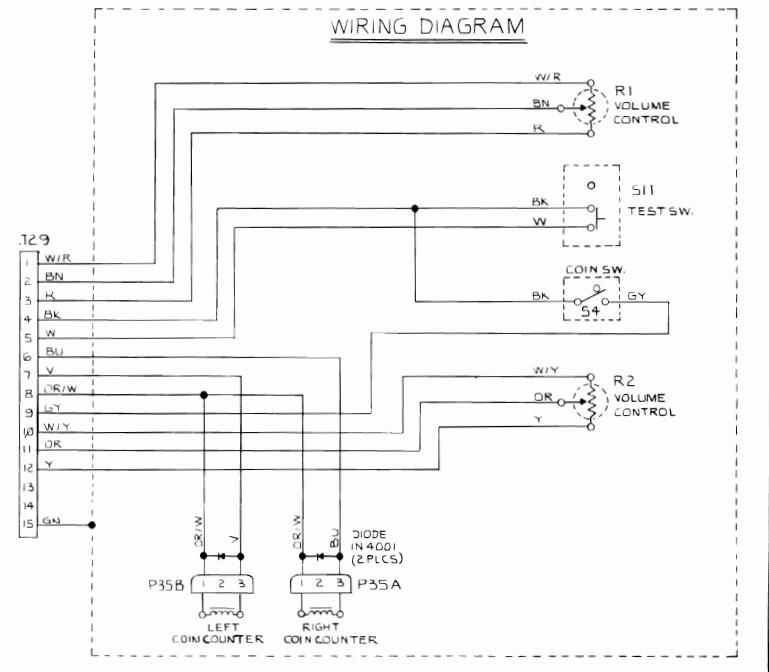


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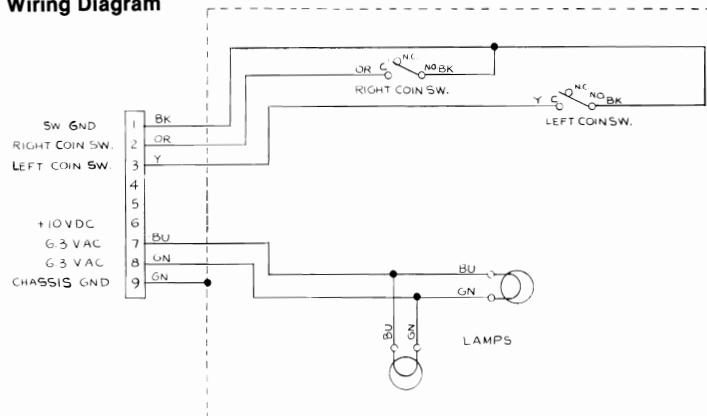
Fluorescent Light Wiring Diagram



Utility Panel Wiring Diagram



**Coin Door
Wiring Diagram**



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STAR WARS Game Interfaces

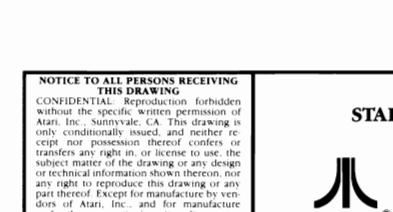


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Main PCB

HEXA-DECIMAL ADDRESS	ADDRESS BUS SIGNAL LINES										READ/WRITE	DATA BUS SIGNAL LINES							FUNCTION						
	A15	A14	A13	A12	A11	A10	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0	D7	D6	D5	D4	D3	D2	D1	D0	
0000-07FF	0	0	0	0	0	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VRAM0
0800-0FFF	0	0	0	0	1	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VRAM1
1000-17FF	0	0	0	1	0	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VRAM2
1800-1FFF	0	0	0	1	1	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VRAM3
2000-27FF	0	0	0	1	0	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VRAM4
2800-2FFF	0	0	0	1	0	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	VRAM5
3000-3FFF	0	0	1	1	A	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D	D	D	VROM
4300	0	1	0	0	0	0	1	1	0	0	0					R	D								LEFT F/S
	0	1	0	0	0	0	1	1	0	0	0					R	D								RIGHT F/S
	0	1	0	0	0	0	1	1	0	0	0					R	D								SPARE 1
	0	1	0	0	0	0	1	1	0	0	0					R	D								SELF-TEST
	0	1	0	0	0	0	1	1	0	0	0					R	D								SLAM
	0	1	0	0	0	0	1	1	0	0	0					R	D								COIN AUX
	0	1	0	0	0	0	1	1	0	0	0					R	D								COIN L
	0	1	0	0	0	0	1	1	0	0	0					R	D								COIN R
4320	0	1	0	0	0	0	1	1	0	0	1					R	D								MATH RUN
	0	1	0	0	0	0	1	1	0	0	1					R	D								VGHALT
	0	1	0	0	0	0	1	1	0	0	1					R	D								LEFT THUMB
	0	1	0	0	0	0	1	1	0	0	1					R	D								RIGHT THUMB
	0	1	0	0	0	0	1	1	0	0	1					R	D								SPARE 2
	0	1	0	0	0	0	1	1	0	0	1					R	D								DIAGN
	0	1	0	0	0	0	1	1	0	0	1					R	D								Unused
	0	1	0	0	0	0	1	1	0	0	1					R	D								Unused
4340	0	1	0	0	0	0	1	1	0	1	0					R	D	D	D	D	D	D	D	D	OPT0
4360	0	1	0	0	0	0	1	1	0	1	1					R	D	D	D	D	D	D	D	D	OPT1
4380	0	1	0	0	0	0	1	1	1	0	0					R	D	D	D	D	D	D	D	D	ADC
4400	0	1	0	0	0	1	0	0				0	R	D	D	D	D	D	D	D	D	D	D	MAINREAD	
	0	1	0	0	0	1	0	0				0	W	D	D	D	D	D	D	D	D	D	D	MAINWR	
4401	0	1	0	0	0	1	0	0				1	R	D											MAIN READY FLAG
	0	1	0	0	0	1	0	0				1	R	D											SOUND READY FLAG
4500-4FFF	0	1	0	0	0	1	0	1	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	NOVRAM

HEXA-DECIMAL ADDRESS	ADDRESS BUS SIGNAL LINES										READ/WRITE	DATA BUS SIGNAL LINES							FUNCTION								
	A15	A14	A13	A12	A11	A10	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0	D7	D6	D5	D4	D3	D2	D1	D0			
4600	0	1	0	0	0	1	1	0	0	0	0					R/W									EVGGO		
4620	0	1	0	0	0	1	1	0	0	0	0	1				R/W									EVGRES		
4640	0	1	0	0	0	1	1	0	0	1	0					R/W									WDCLR		
4660	0	1	0	0	0	1	1	0	0	1	1					R/W									IRQCLR		
4680	0	1	0	0	0	1	1	0	1	0	0					W	D								COIN CNTR 2		
4681	0	1	0	0	0	1	1	0	1	0	0					W	D								COIN CNTR 1		
4682	0	1	0	0	0	1	1	0	1	0	0					W	D								LED3		
4683	0	1	0	0	0	1	1	0	1	0	0					W	D								LED2		
4684	0	1	0	0	0	1	1	0	1	0	0					W	D								MPAGE		
4685	0	1	0	0	0	1	1	0	1	0	0					W	D								PRNGCR		
4686	0	1	0	0	0	1	1	0	1	0	0					W	D								LEDT		
4687	0	1	0	0	0	1	1	0	1	0	0					W	D								RECALL		
46A0	0	1	0	0	0	1	1	0	1	0	1					R/W									NSTORE		
46C0	0	1	0	0	0	1	1	0	1	1	0					W									ADCSTART0		
46C1	0	1	0	0	0	1	1	0	1	1	0					W									ADCSTART1		
46C2	0	1	0	0	0	1	1	0	1	1	0					W									ADCSTART2		
46E0	0	1	0	0	0	1	1	0	1	1	1					R/W									SOUNDRST		
4700	0	1	0	0	0	1	1	1	1	1	1					0	0	0	R	D	D	D	D	D	D	D	REH
4701	0	1	0	0	0	1	1	1	1	1	1					0	0	0	W	D	D	D	D	D	D	D	MWO
4702	0	1	0	0	0	1	1	1	1	1	1					0	0	0	W	D	D	D	D	D	D	D	REL
4703	0	1	0	0	0	1	1	1	1	1	1					0	1	1	R	D	D	D	D	D	D	D	MWT
4704	0	1	0	0	0	1	1	1	1	1	1					1	0	0	W	D	D	D	D	D	D	D	MW2
4705	0	1	0	0	0	1	1	1	1	1	1					1	0	1	W	D	D	D	D	D	D	D	PRNG
4706	0	1	0	0	0	1	1	1	1	1	1					1	1	0	W	D	D	D	D	D	D	D	DVSRL
4707	0	1	0	0	0	1	1	1	1	1	1					1	1	1	W	D	D	D	D	D	D	D	DVDDH
4800-4FFF	0	1	0	0	1	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	RAM		
5000-5FFF	0	1	0	1	A	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	MBRAM		
6000-7FFF	0	1	1	A	A	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D	D	D	ROM0		
8000-9FFF	1	0	0	A	A	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D	D	D	ROM1		
A000-BFFF	1	0	1	A	A	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D	D	D	ROM2		
C000-DFFF	1	1	0	A	A	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D	D	D	ROM3		
E000-FFFF	1	1	1	A	A	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D	D	D	ROM4		



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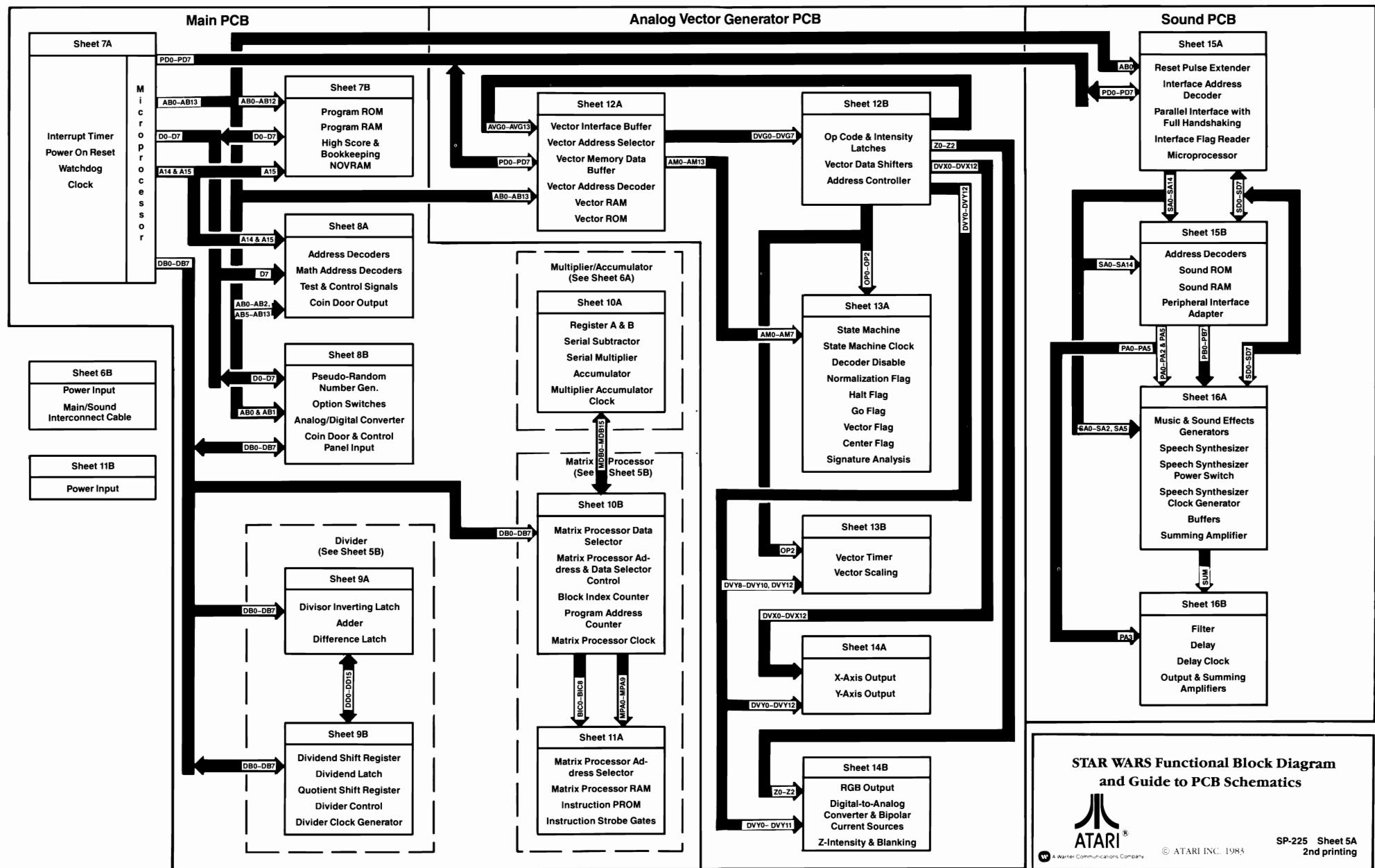
STAR WARS Memory Map -part 1

Sound PCB

STAR WARS Memory Map -part 2



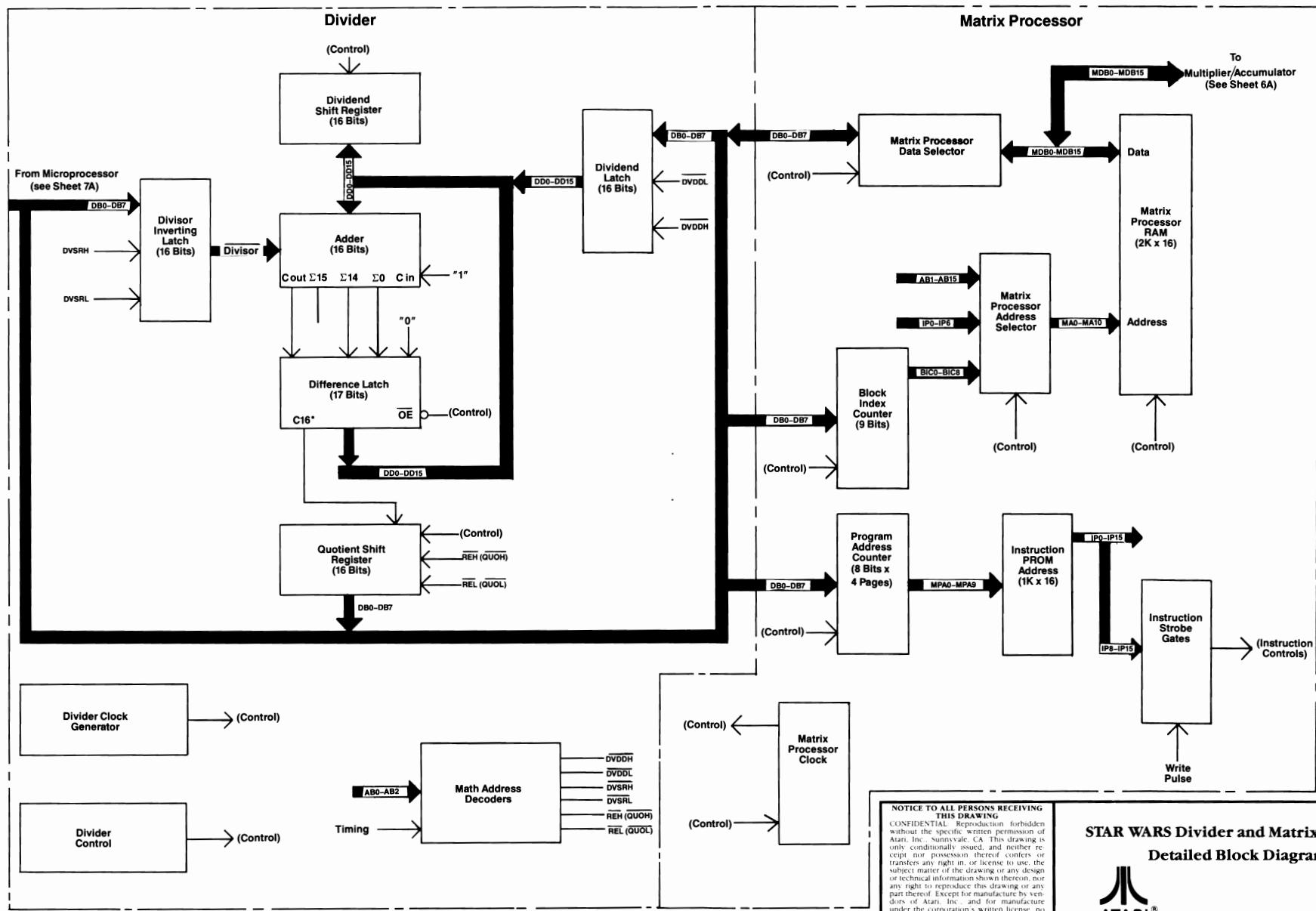
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STAR WARS Functional Block Diagram and Guide to PCB Schematics



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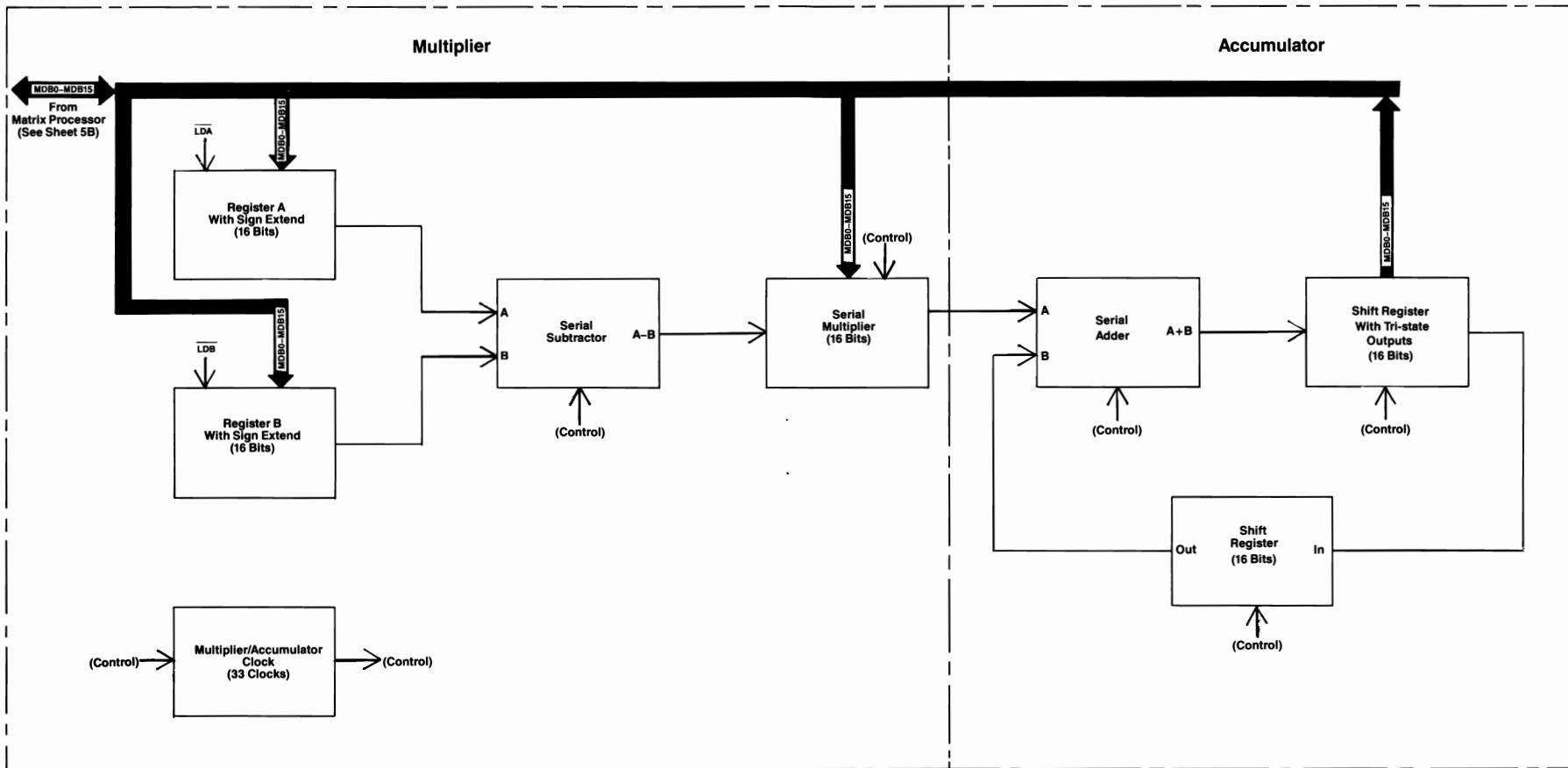
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STAR WARS Divider and Matrix Processor Detailed Block Diagrams



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Multiplier/Accumulator
 $ACC = ACC + (A - B) \times C$



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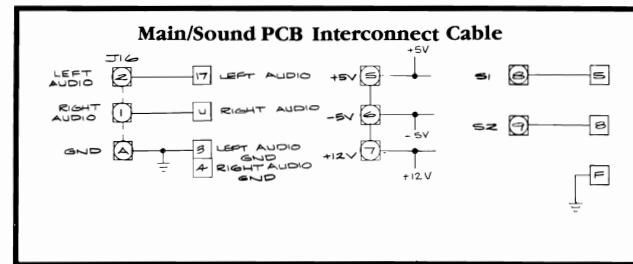
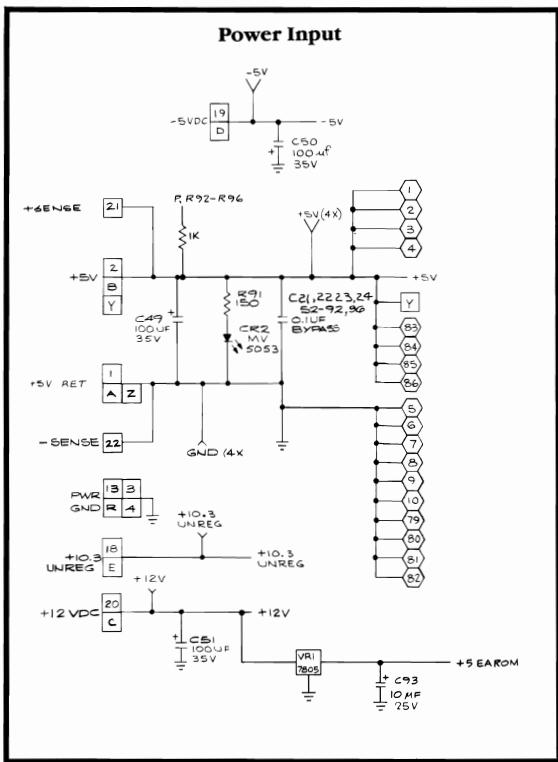
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STAR WARS Multiplier/Accumulator Detailed Block Diagram



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NOTE

- = J20 CONNECTOR
- = TEST CONNECTOR
- = J17 CONNECTOR
- = J16 CONNECTOR

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STAR WARS Main PCB

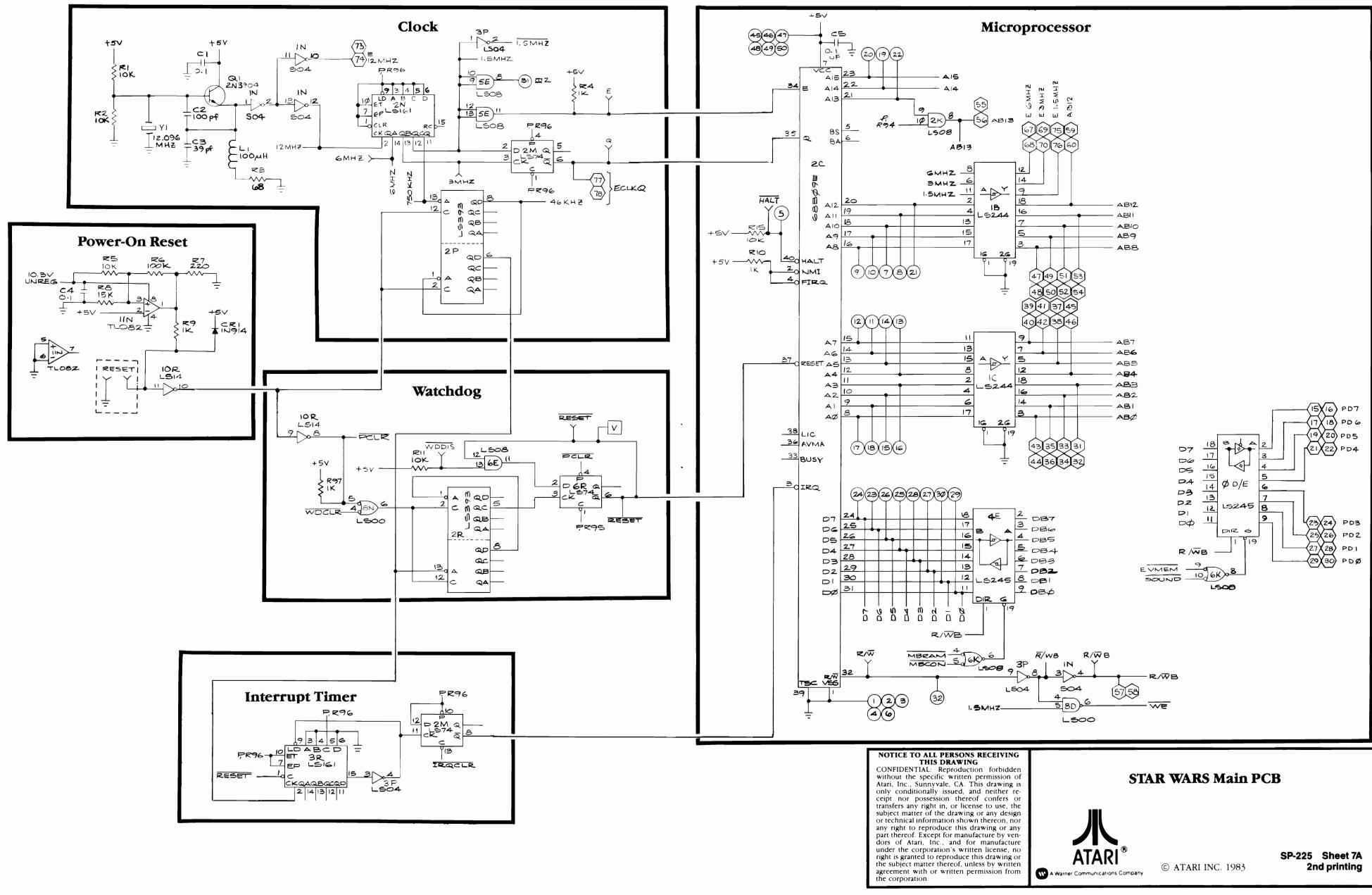


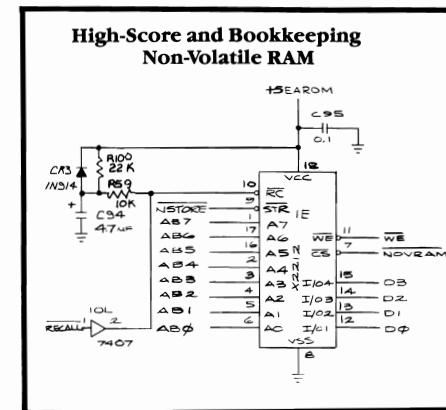
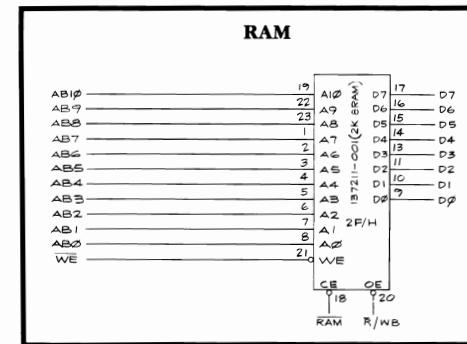
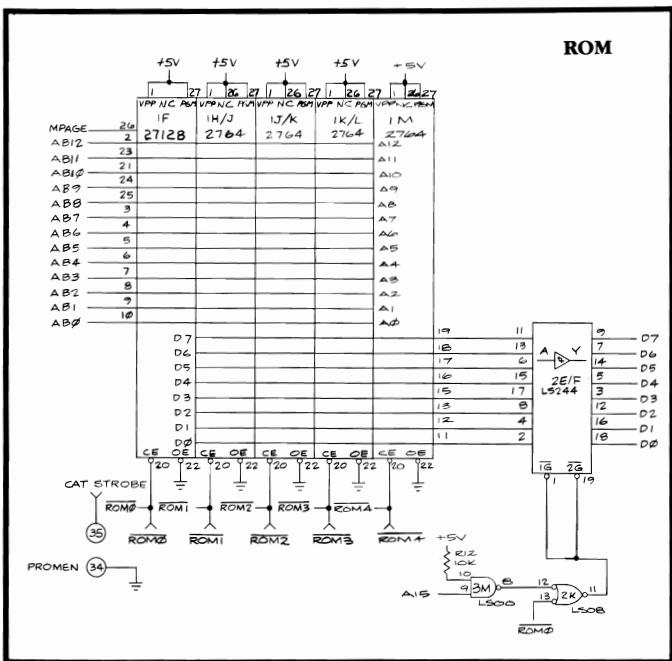
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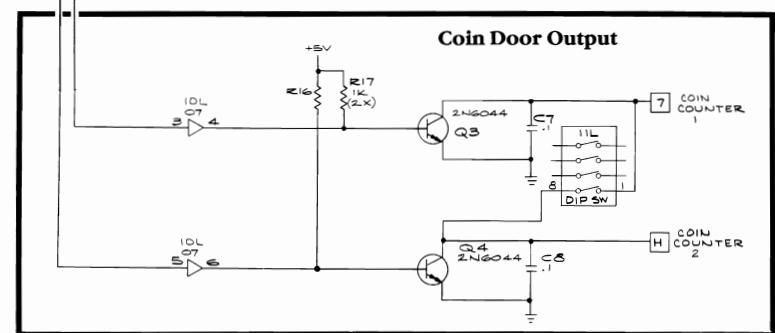
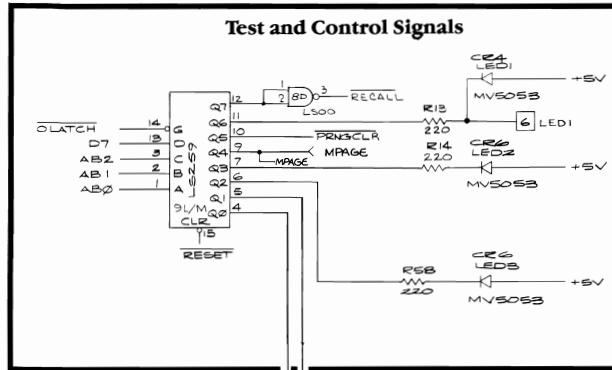
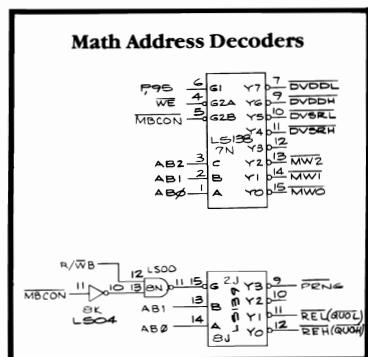
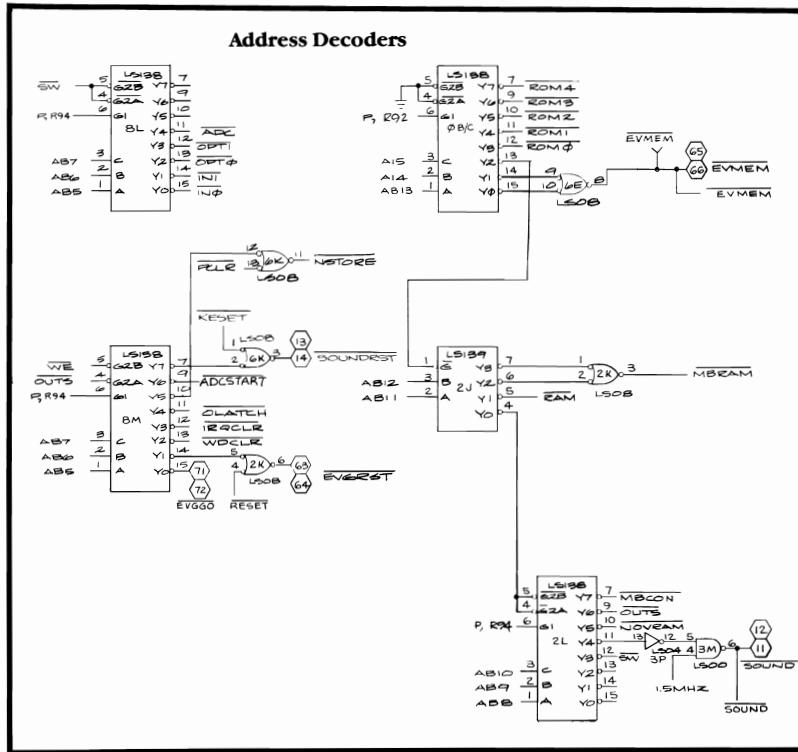


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STAR WARS Main PCB



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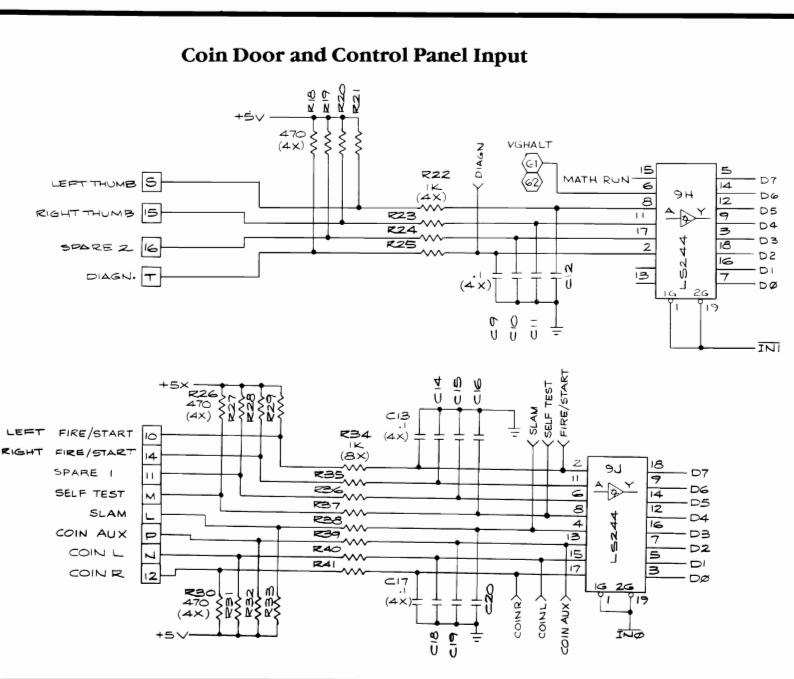
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STAR WARS Main PCB

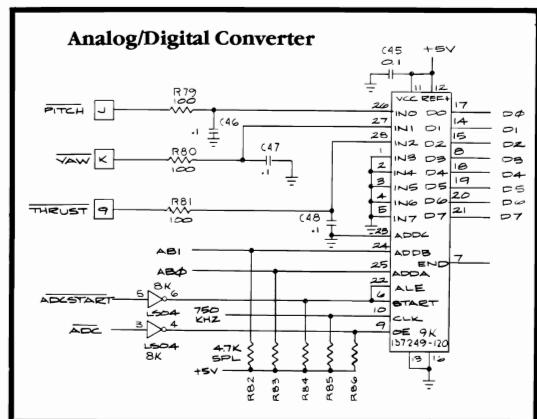


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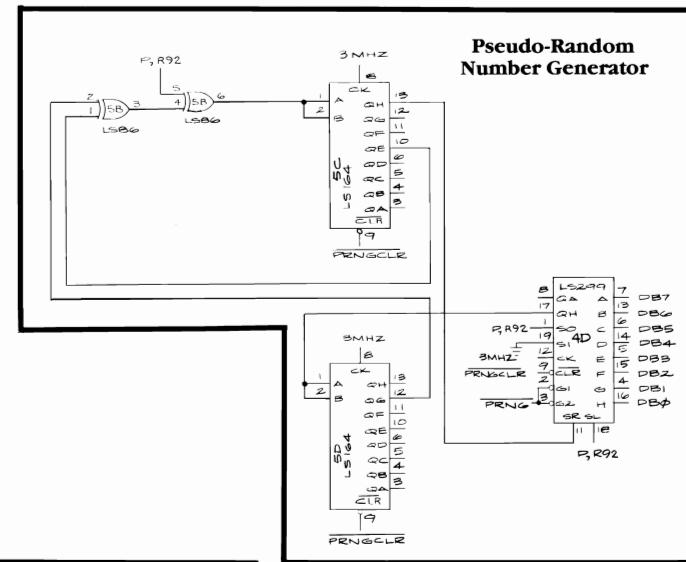
Coin Door and Control Panel Input



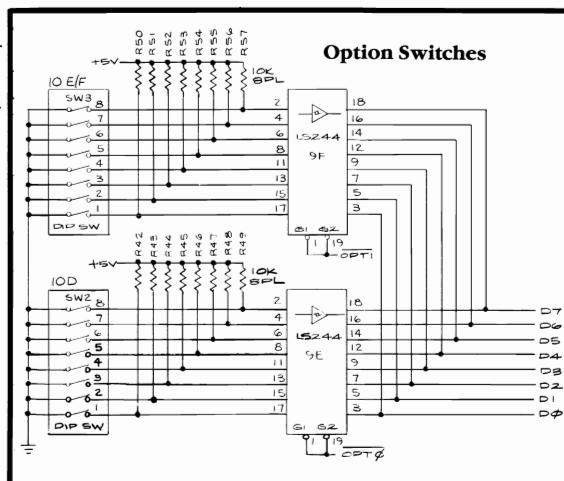
Analog/Digital Converter



Pseudo-Random Number Generator



Option Switches

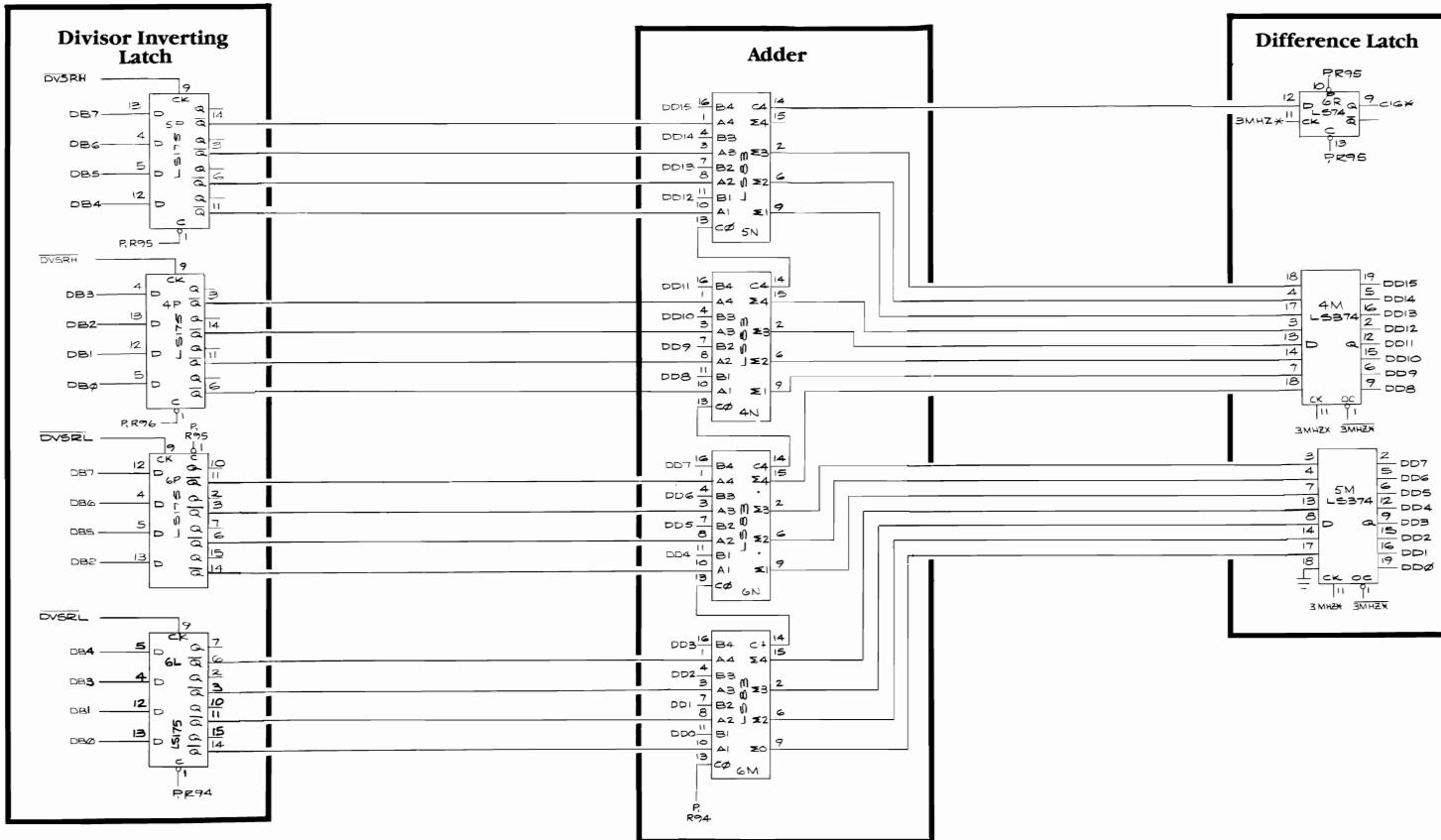


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STAR WARS Main PCB





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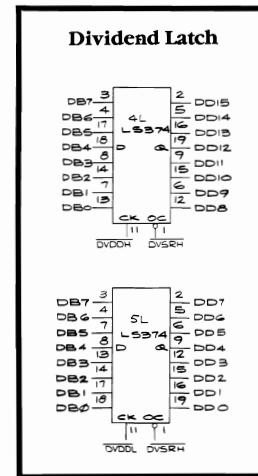
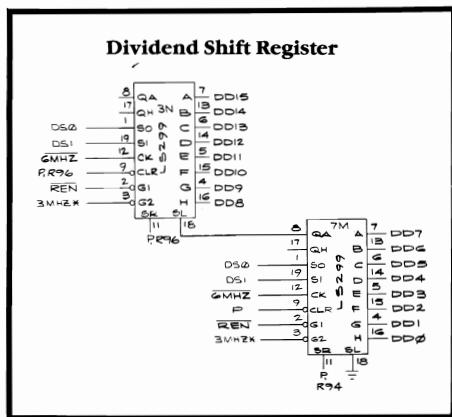
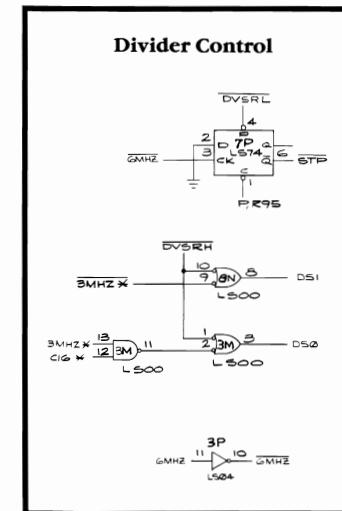
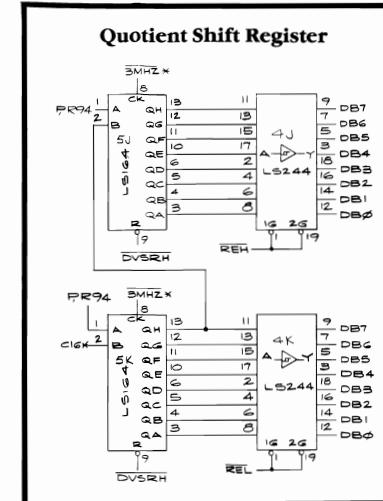
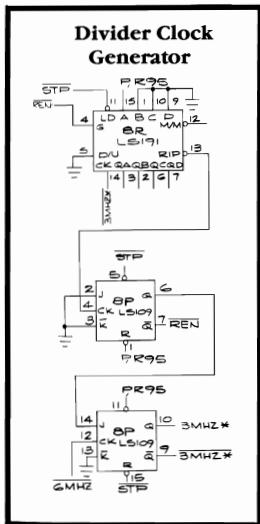
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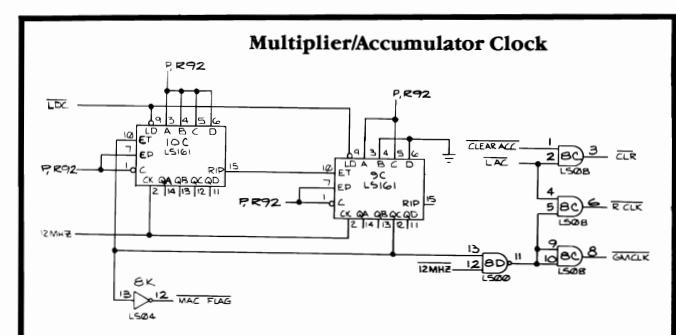
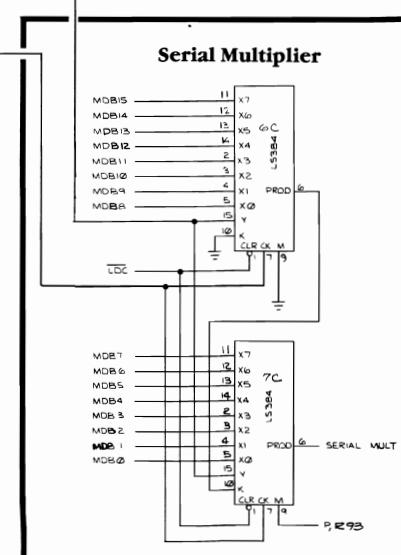
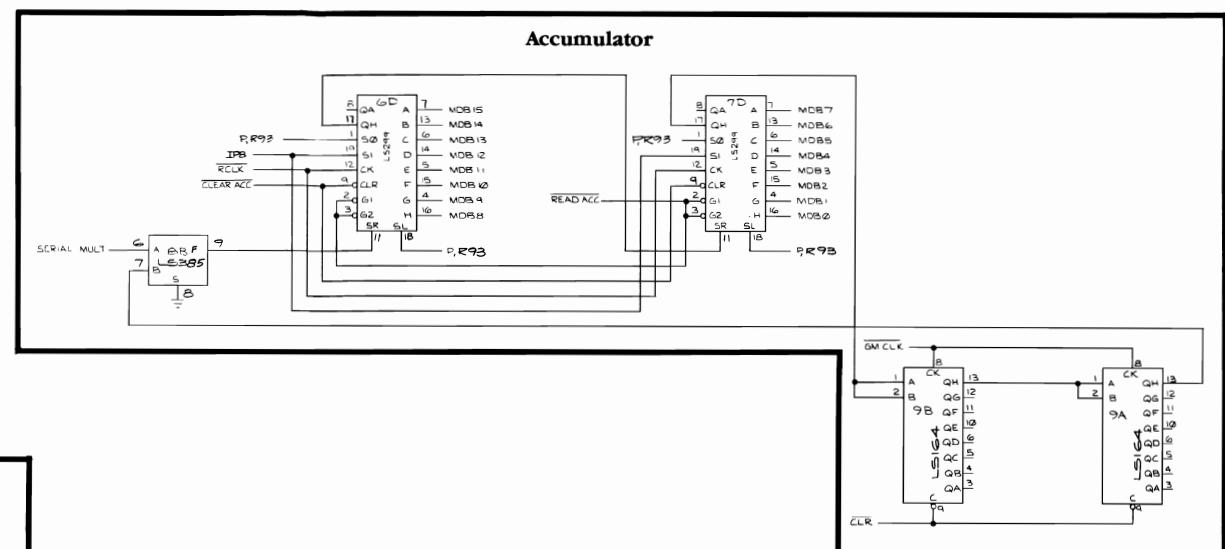
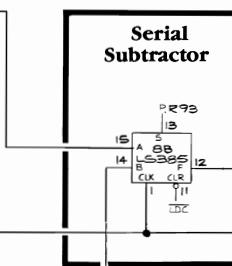
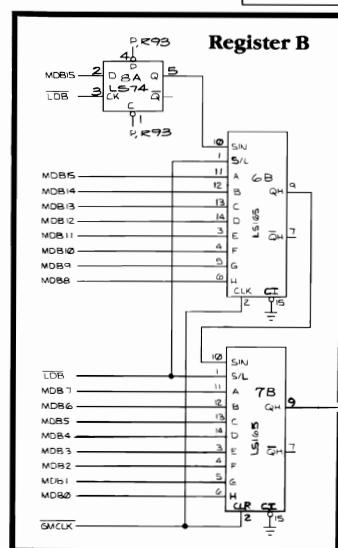
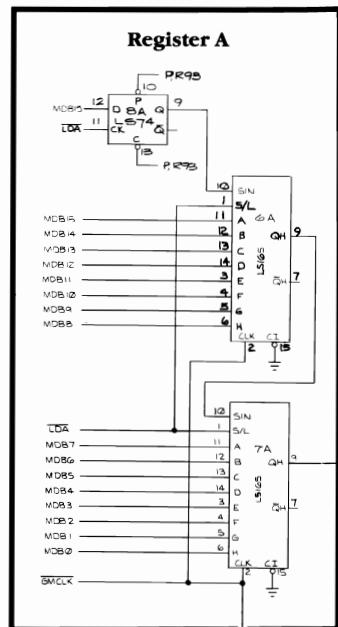


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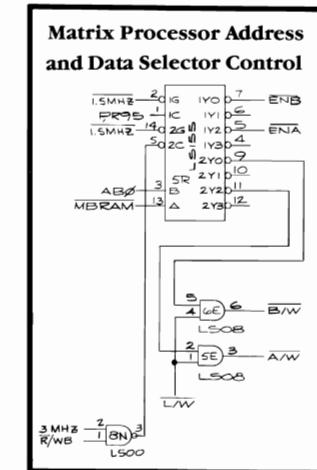
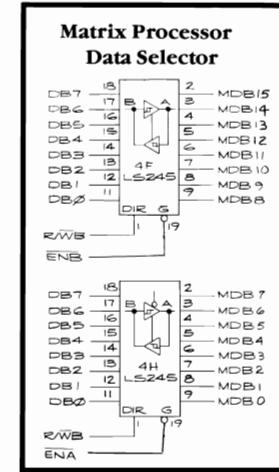
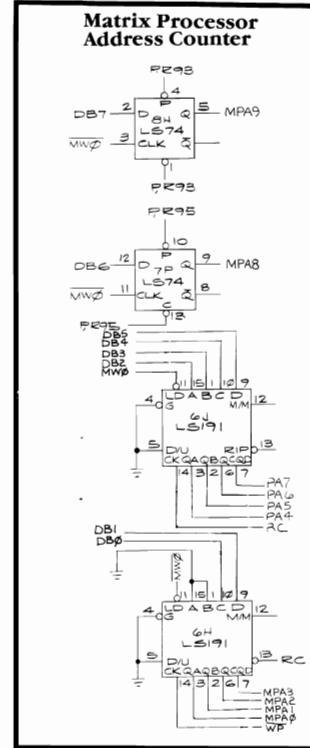
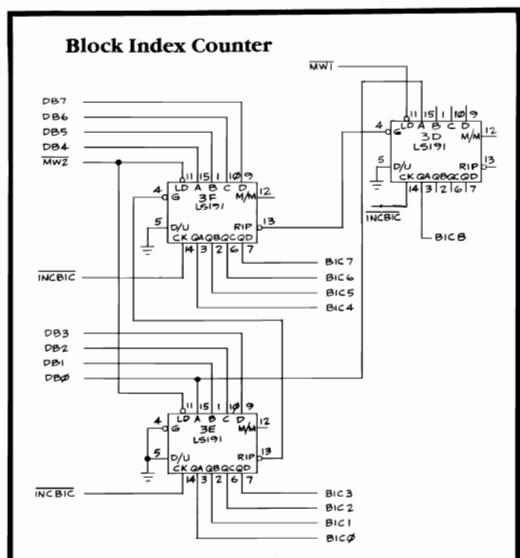
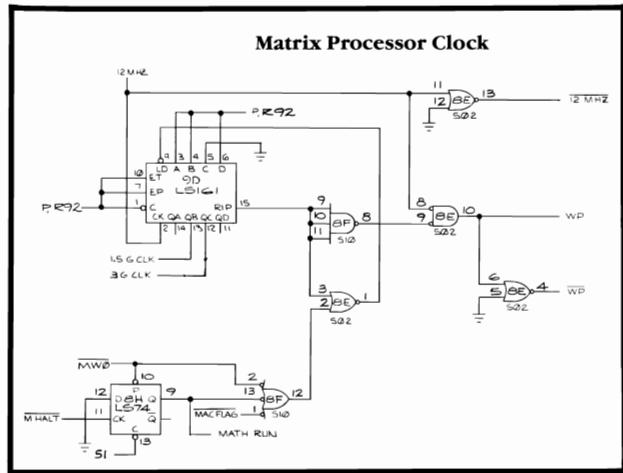


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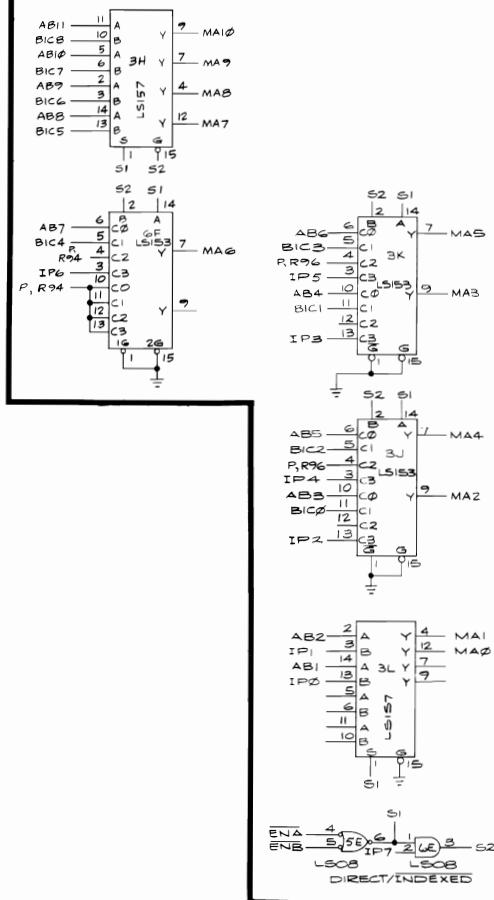
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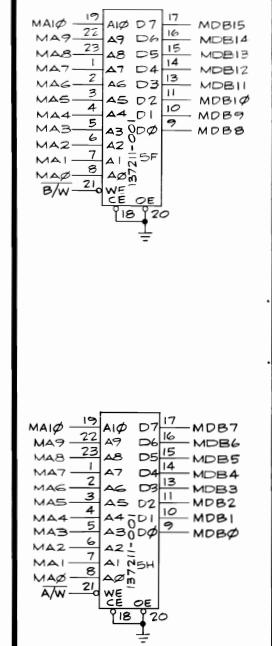
STAR WARS Main PCB



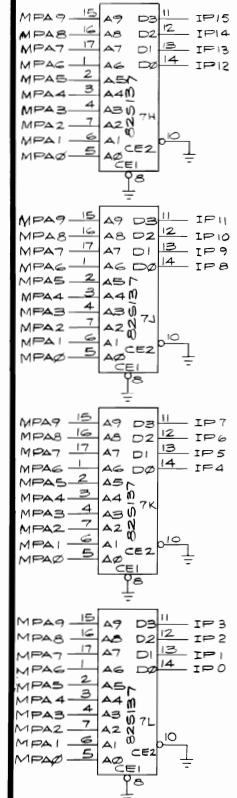
Matrix Processor Address Selector



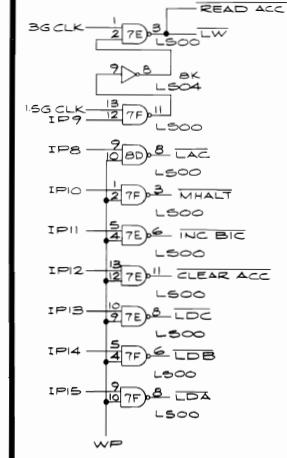
Matrix Processor RAM



Instruction PROM



Instruction Strobe Gates



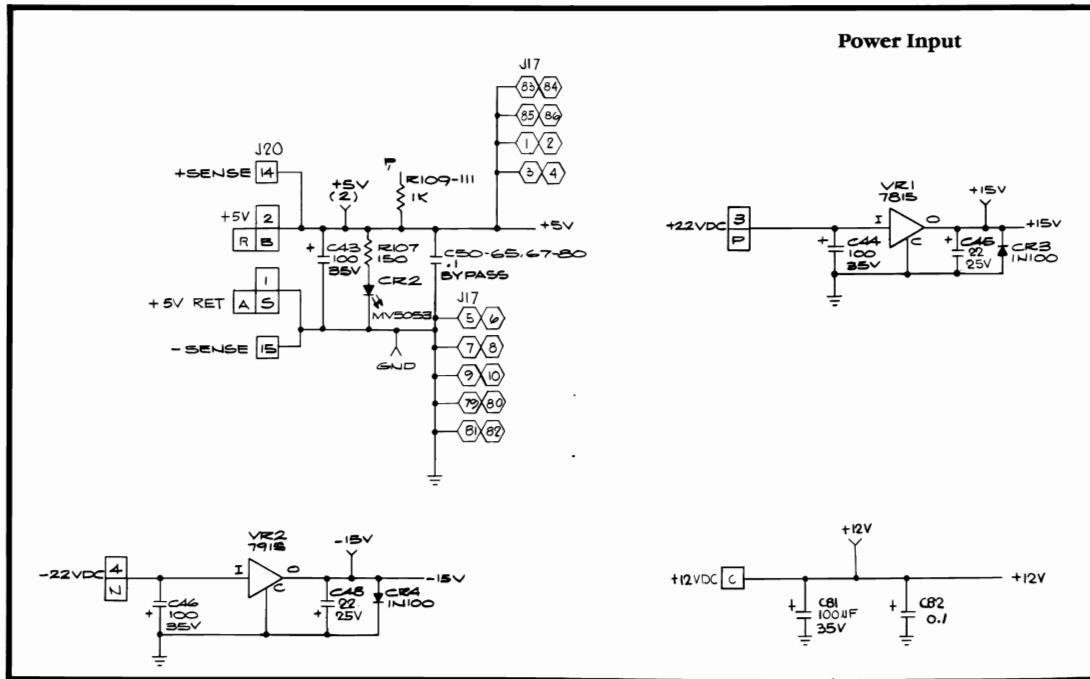
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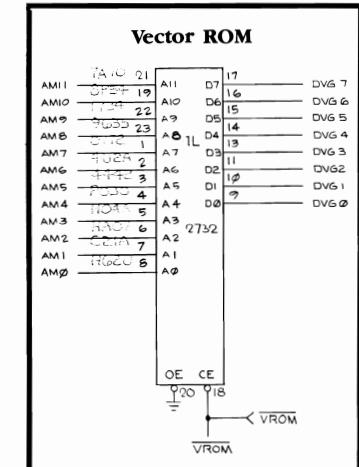
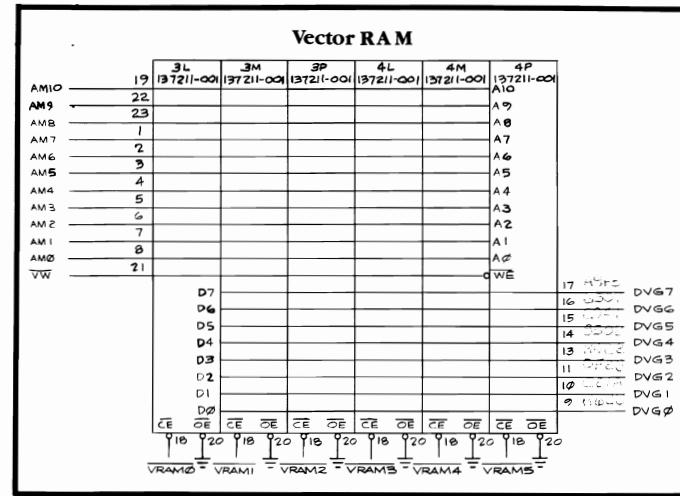
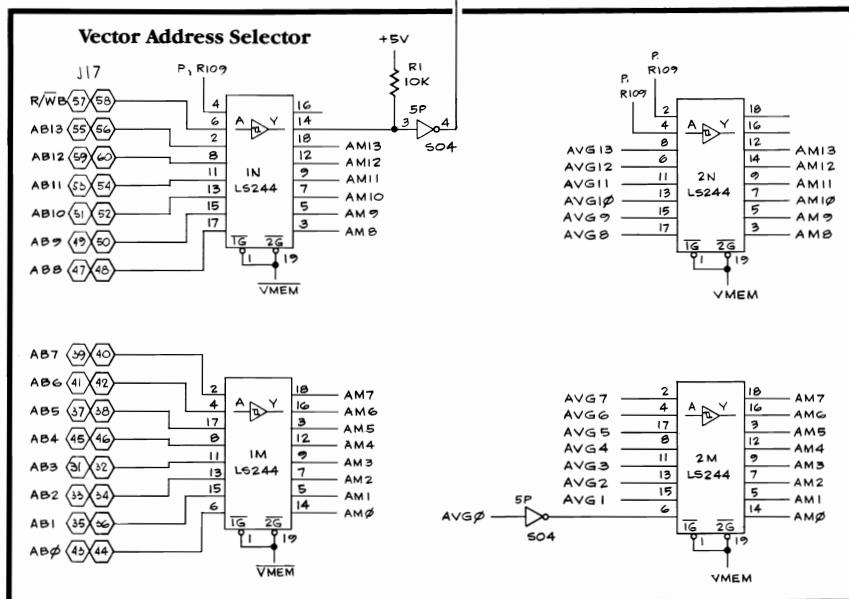
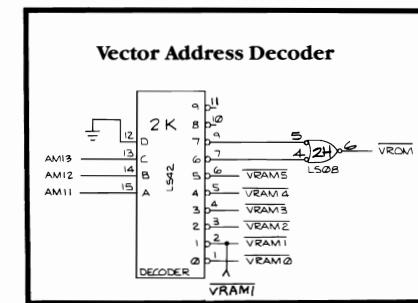
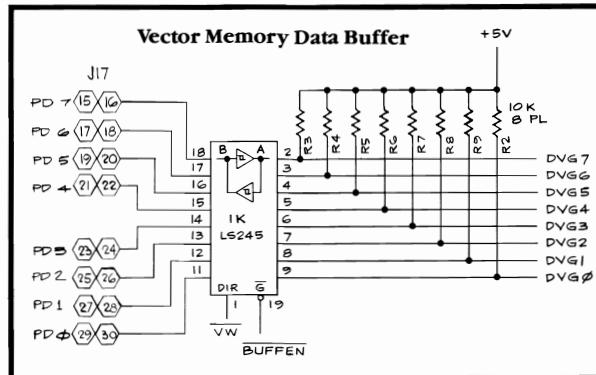
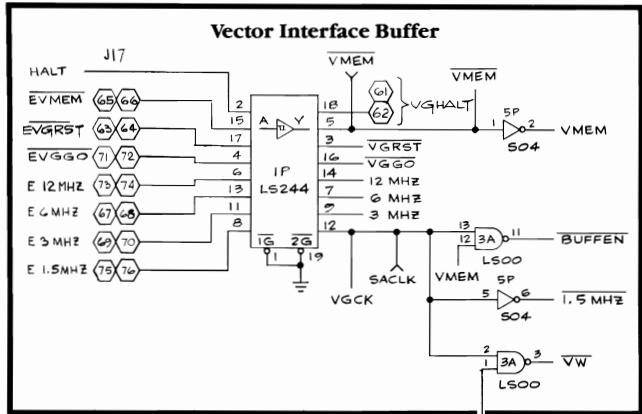
STAR WARS Analog Vector-Generator PCB



W

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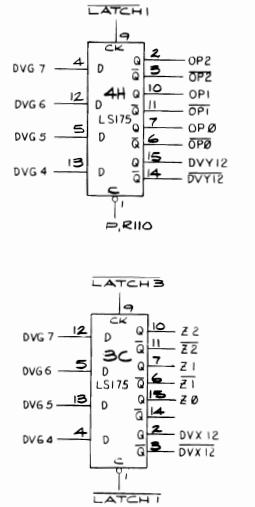
STAR WARS Analog Vector-Generator PCB



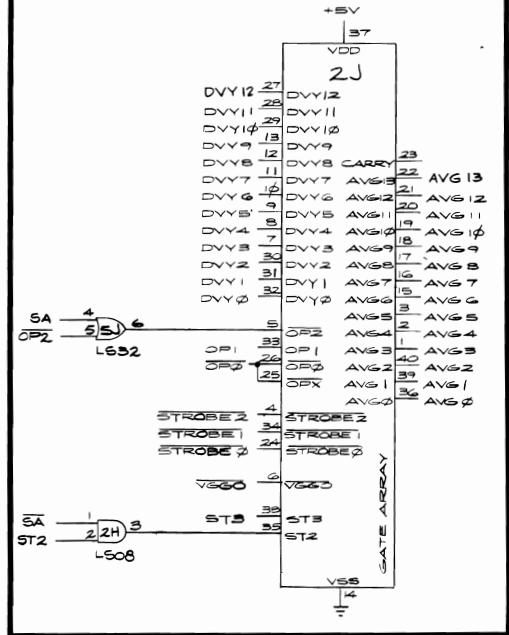
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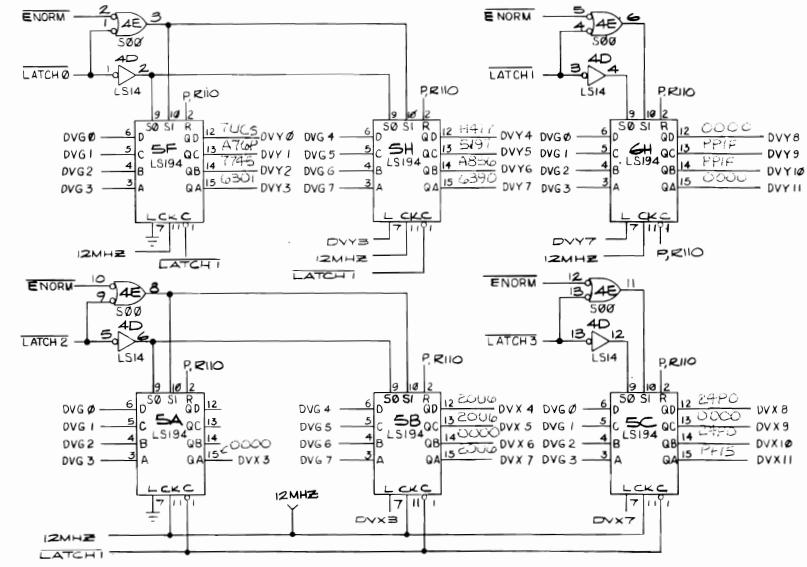
Op Code and Intensity Latches



Address Controller



Vector Data Shifters



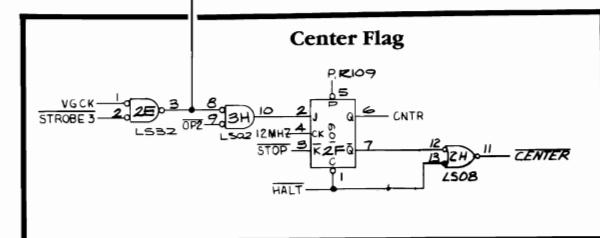
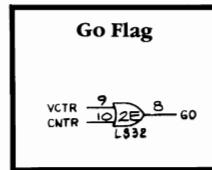
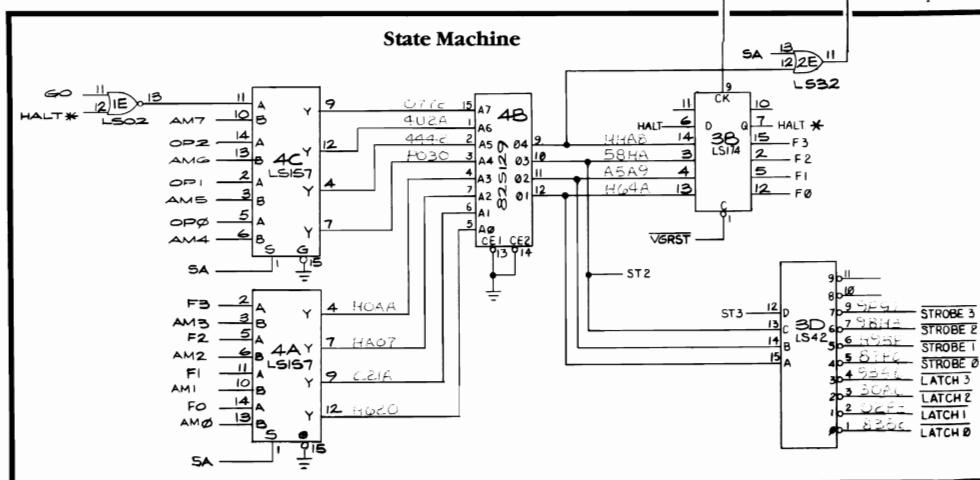
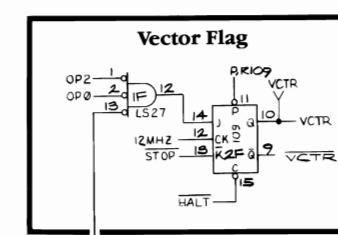
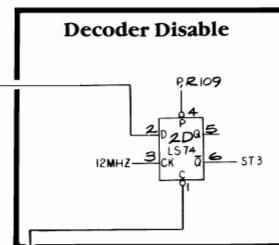
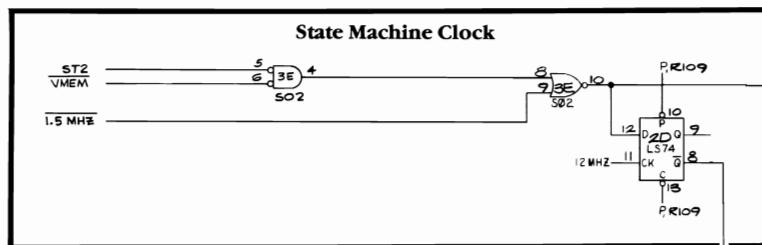
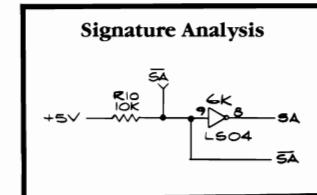
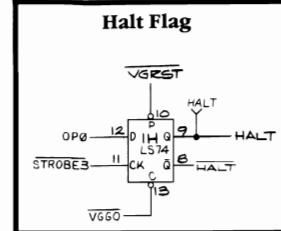
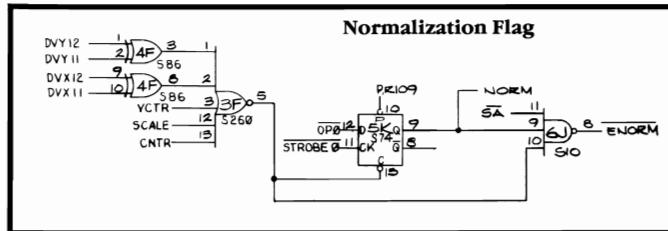
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STAR WARS Analog Vector-Generator PCB



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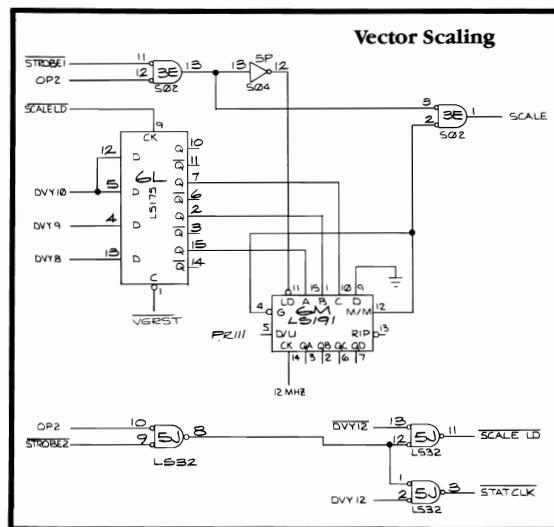
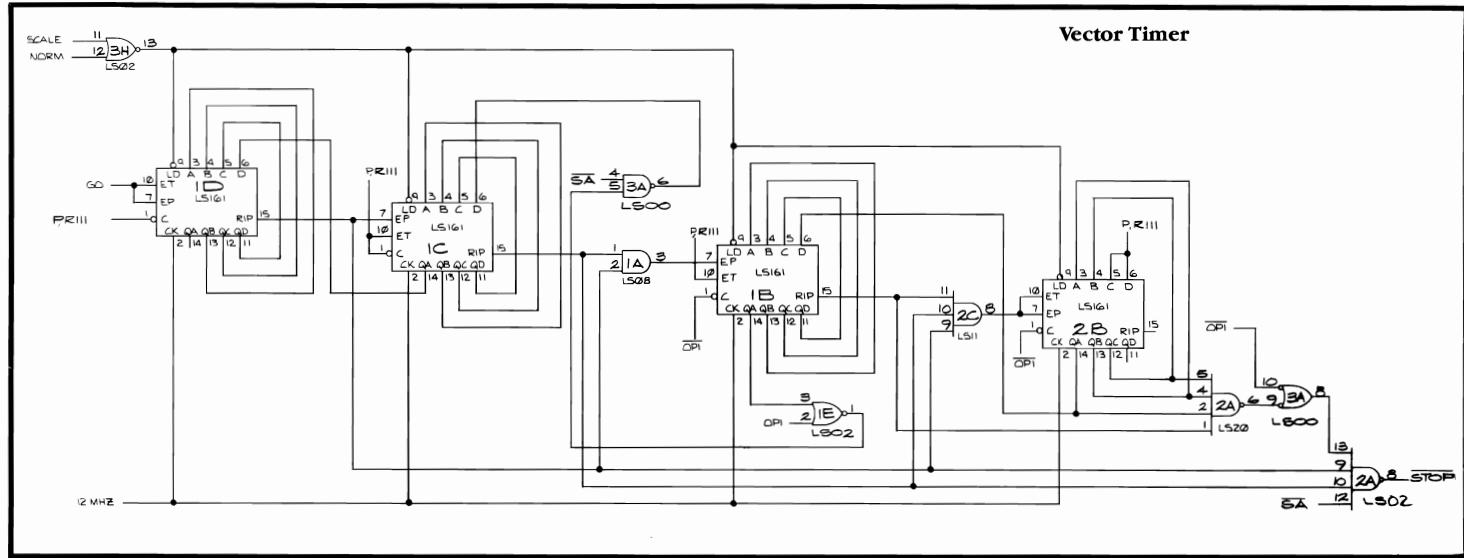


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Vector Timer

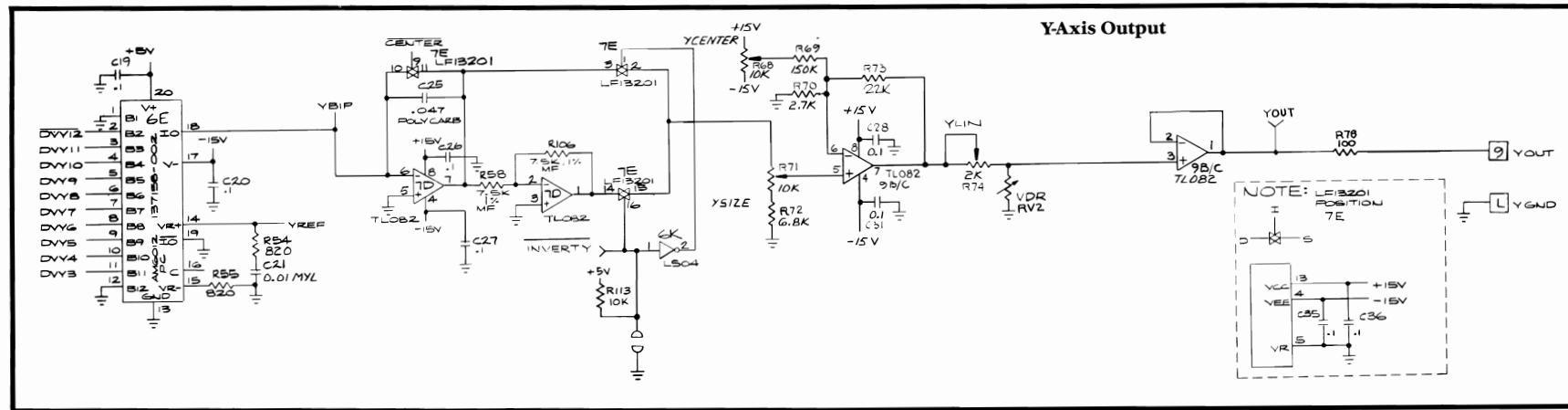
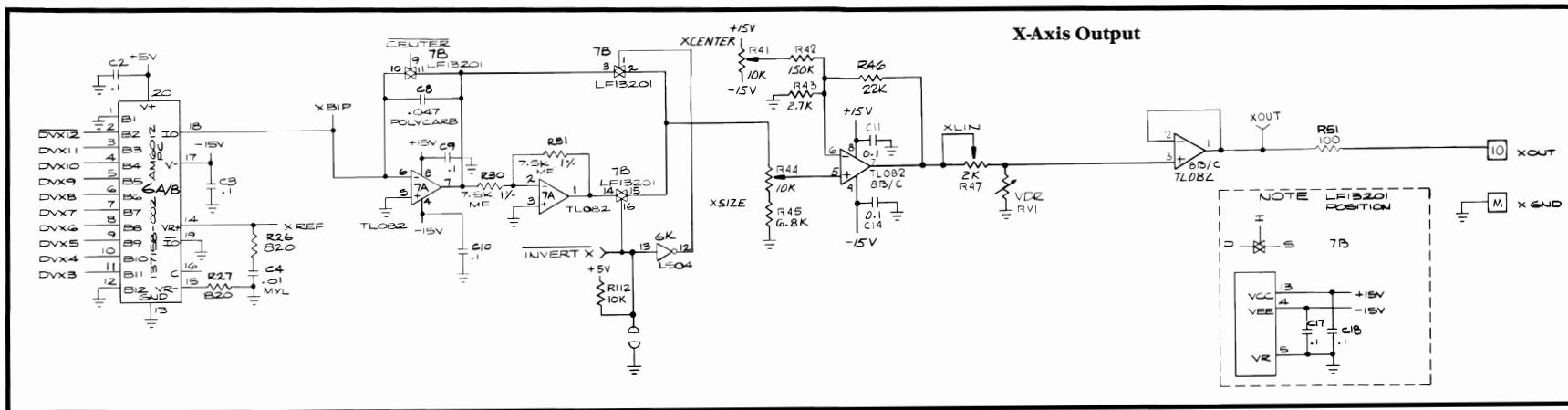
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X and Y Video Adjustments

Perform the following procedure to adjust the display size, centering, and linearity:

1. Set the game to the SelfTest Mode and select the Cross-hatch Pattern Display as described in Chapter 2 of the game manual.
2. Adjust XCENTER (R41) and YCENTER (R68) so that the crosshatch pattern is centered on the screen.
3. Adjust XSIZE (R44) and YSIZE (R71) so that the crosshatch pattern covers the entire visible screen area.
4. Adjust XLIN (R47) and YLIN (R74) so that the diagonal lines are straight and the diamond-shaped squares are symmetrical. Since the linearity potentiometers change the size of the display, it may be necessary to repeat step 3 to obtain the correct display.

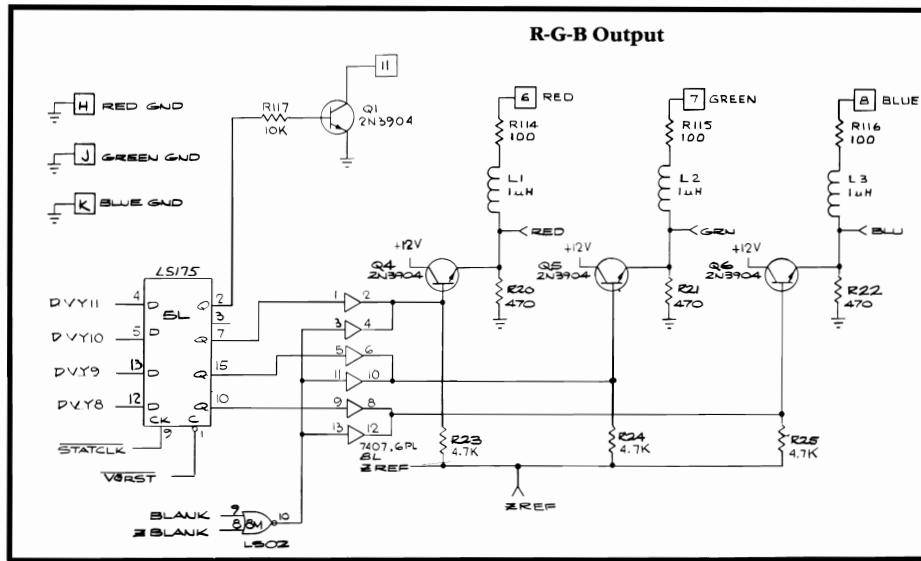
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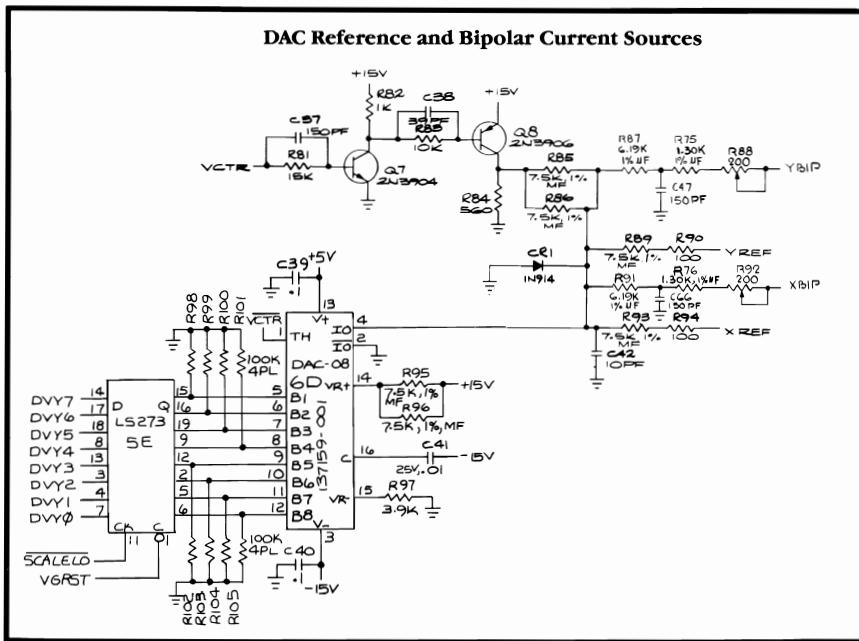
STAR WARS Analog Vector-Generator PCB



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R-G-B Output

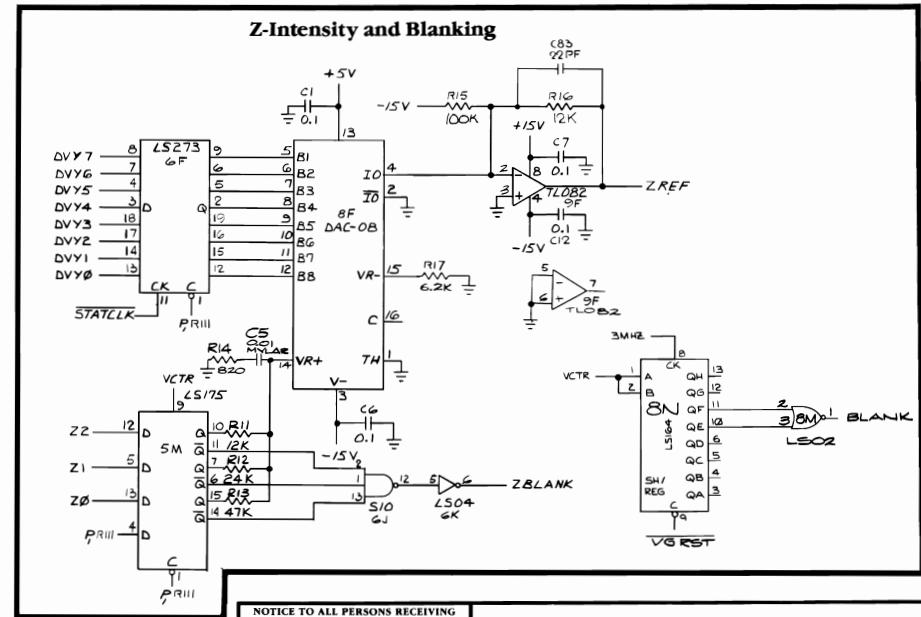


DAC Reference and Bipolar Current Sources

Bipolar Offset Adjustments

Perform the following procedure to adjust the bipolar offset current:

1. Set the game to the Self-Test Mode and select the Bipolar Offset Test Display as described in Chapter 2 of the game manual.
 2. Adjust XBIP (R92) and YBIP (R88) so that the displayed-box line width is $\frac{1}{8}$ -inch or less.



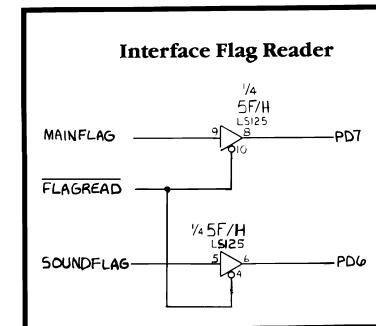
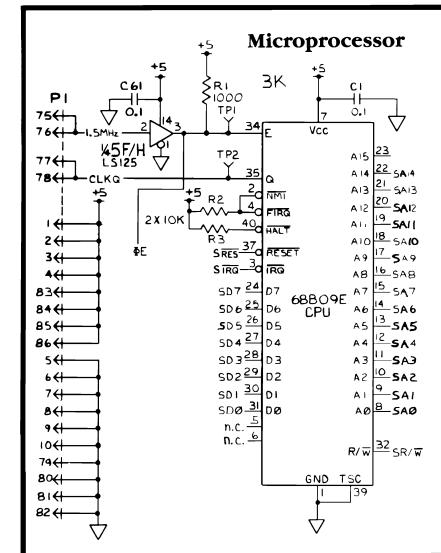
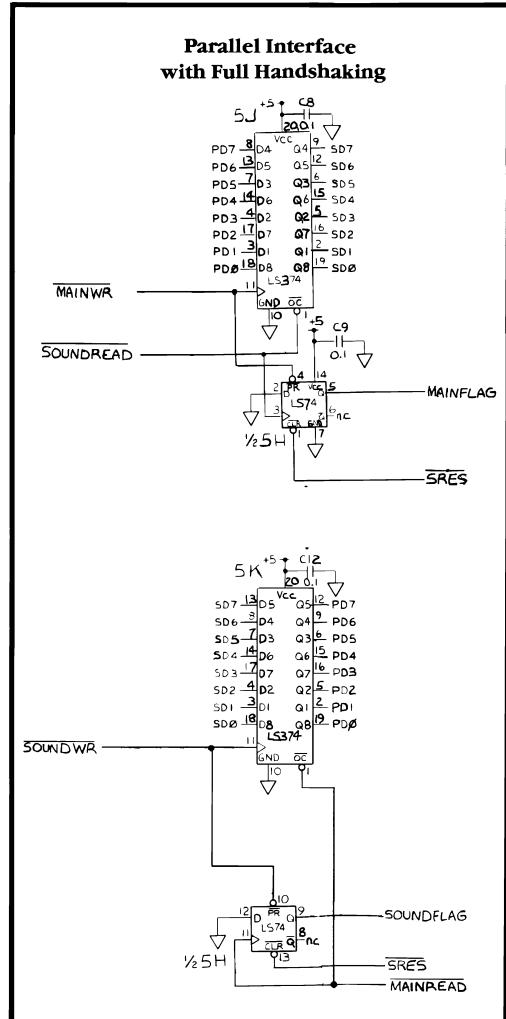
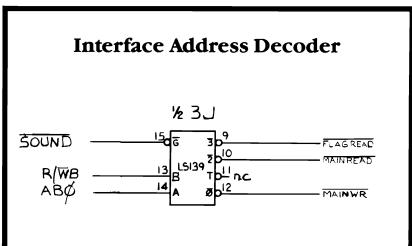
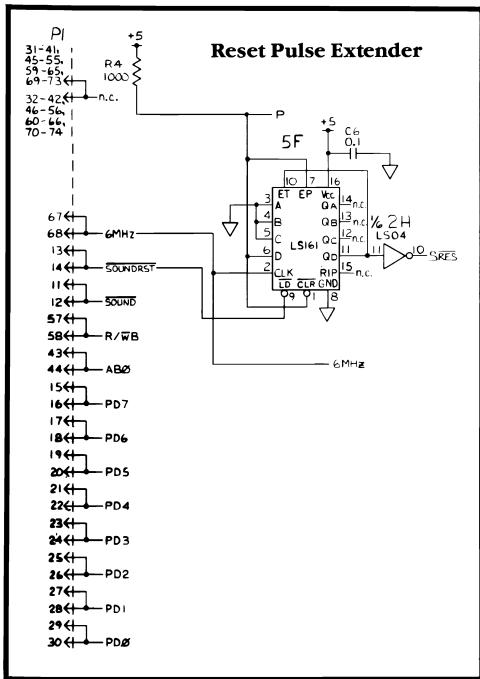
Z-Intensity and Blanking

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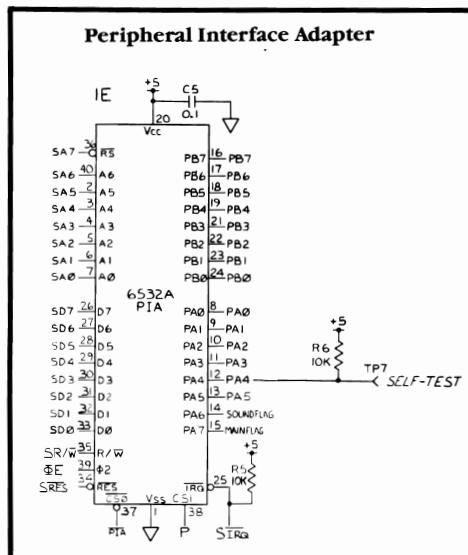
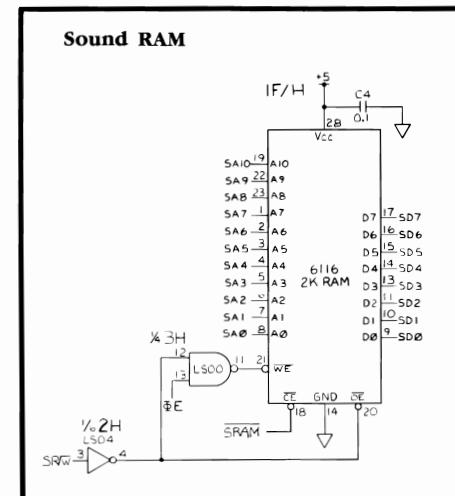
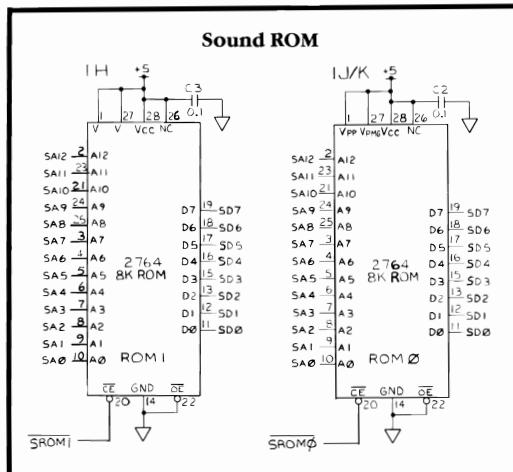
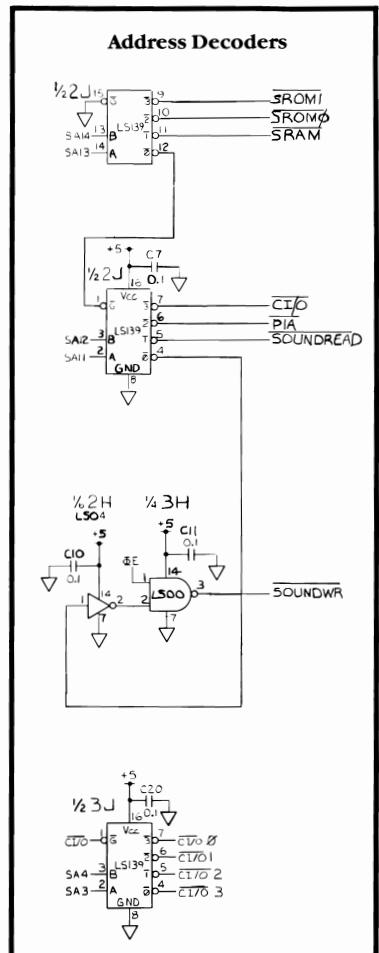


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STAR WARS Sound PCB





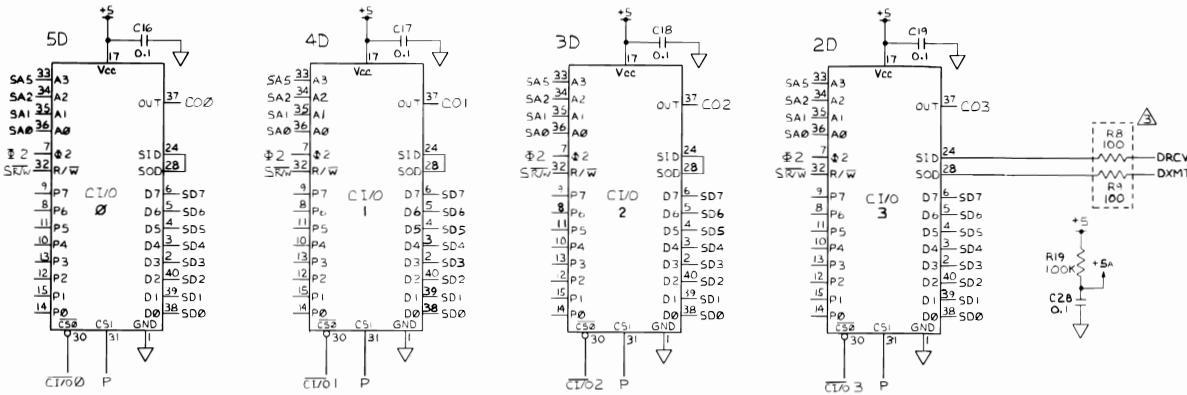
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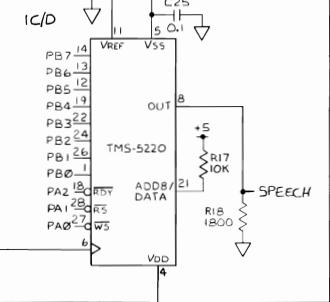


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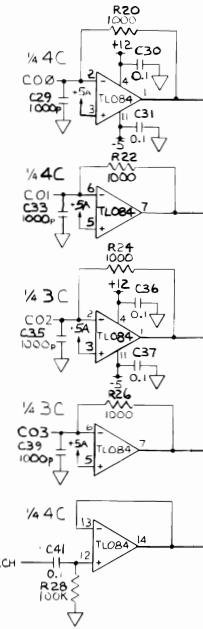
Music and Sound Effects Generators



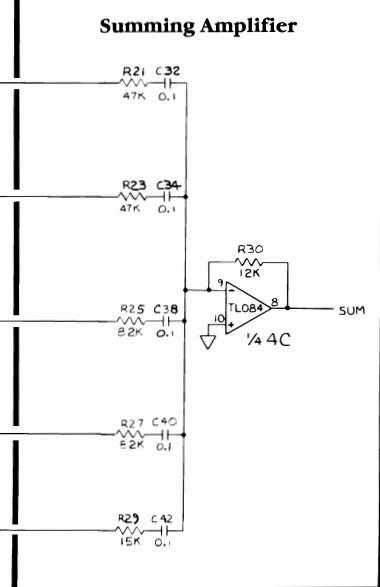
Speech Synthesizer



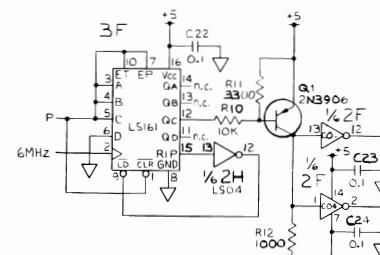
Buffers



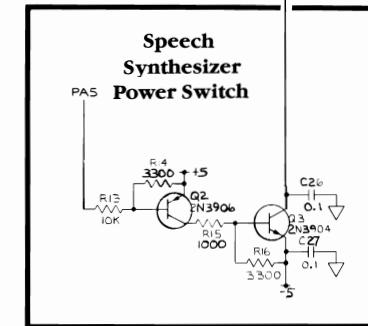
Summing Amplifier



Speech Synthesizer Clock Generator



Speech Synthesizer Power Switch

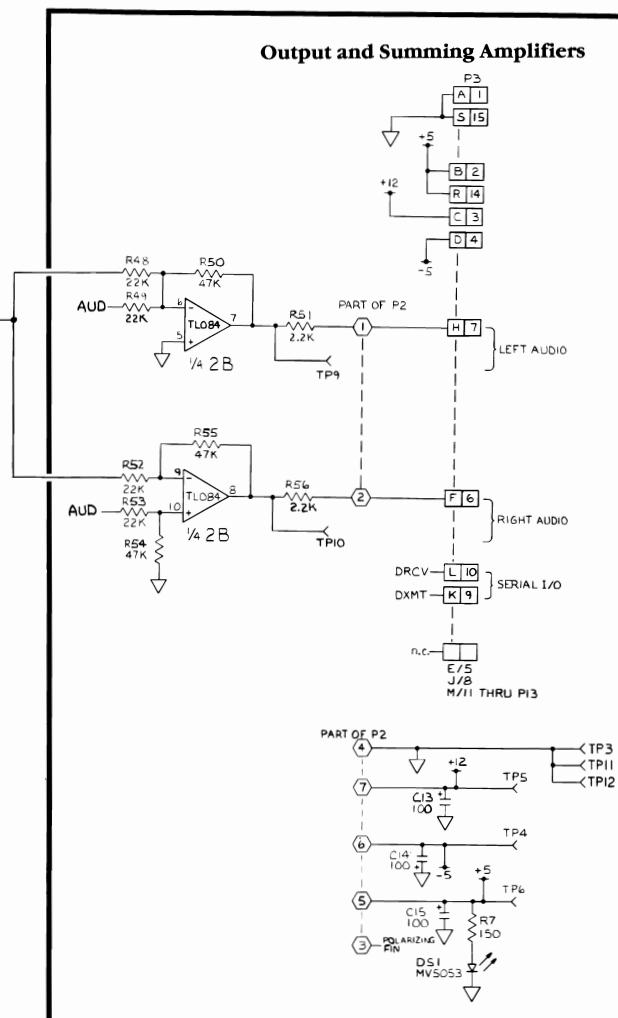
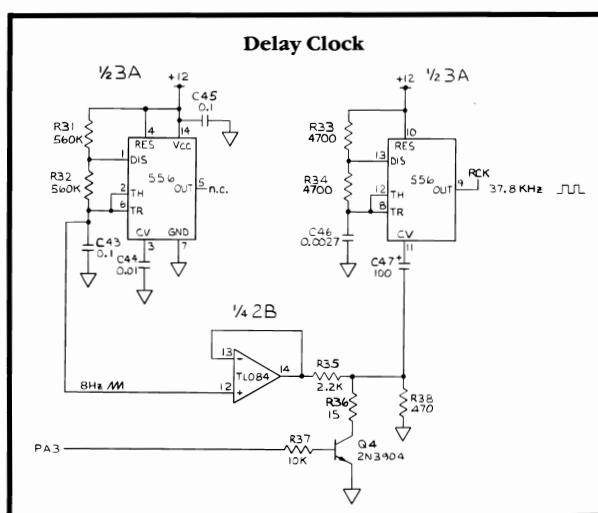
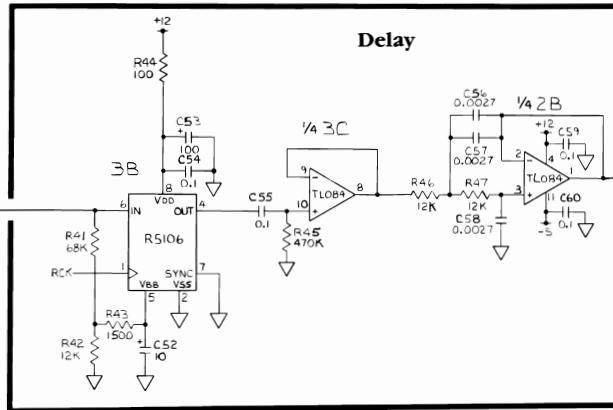
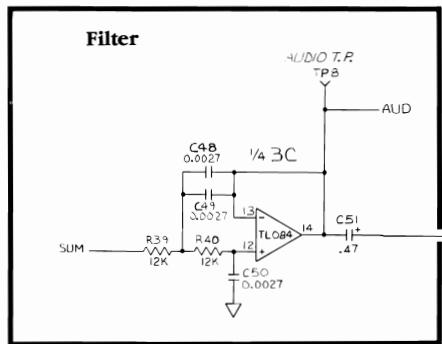


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STAR WARS Sound PCB





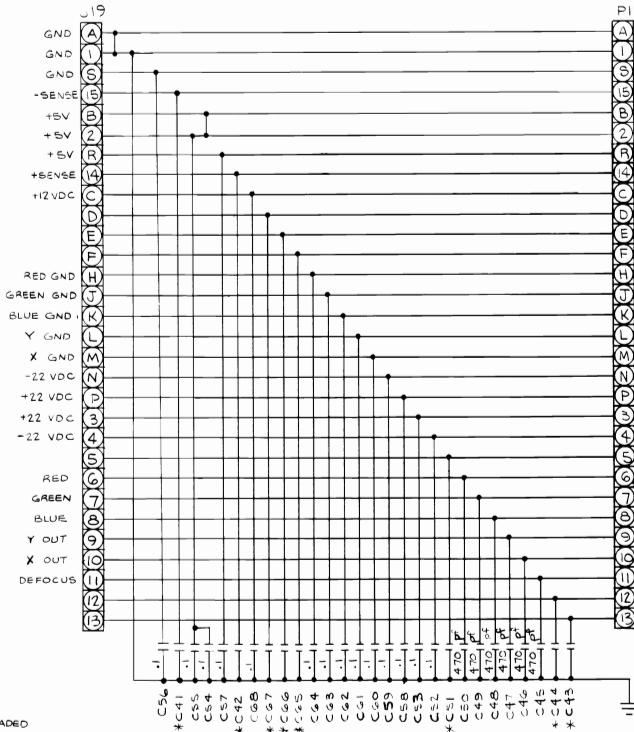
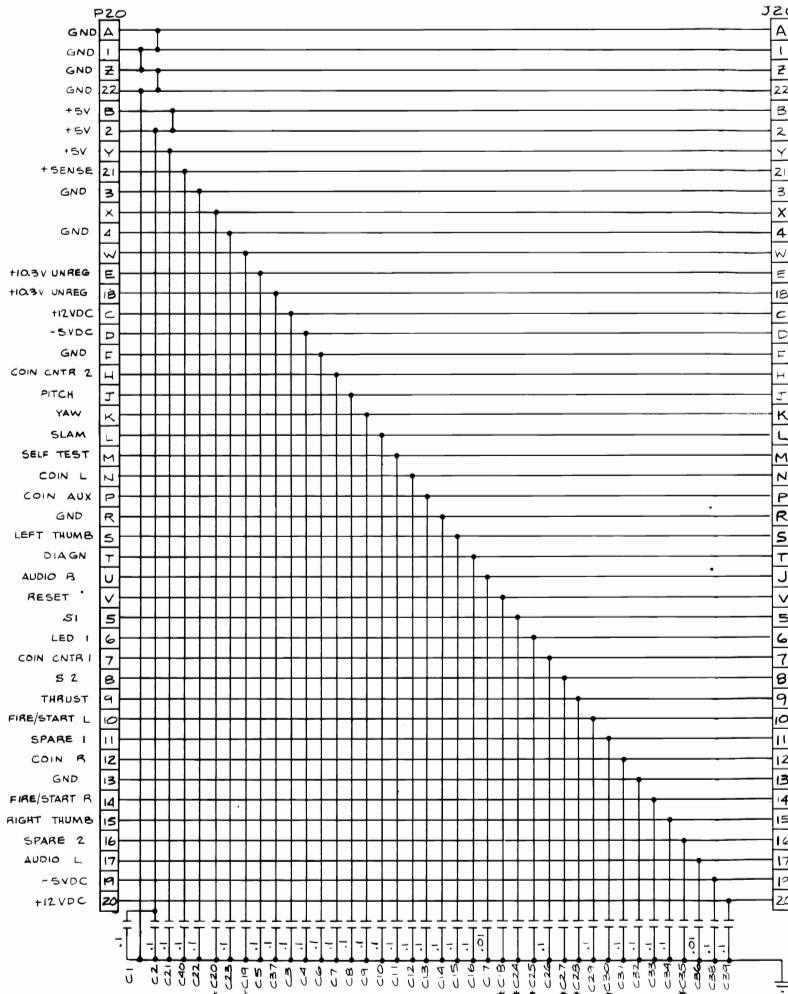
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STAR WARS Sound PCB



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SP-225 Sheet 16B
2nd printing



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STAR WARS EMI Shield PCB



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STAR WARS Interconnect PCB



W

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