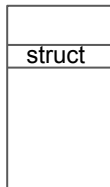


STM Operation

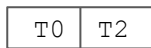
ReadTx
WriteTx

Address

Hash Table



Last 2 threads
accesses



Communication
matrix

3				
2	+1			
1				
0			+1	
	0	1	2	3