tsne

	1	2
gt4_500	−14	−33
cut_r1k	−8	−3
cut_r250	−9	2
cut40_r1k	−3	5
cut40_r250	−13	8
be30	26	15
tri30	−11	9
Nss30	7	2
Ess30	13	8
twi30	11	−7
tpi250	−18	9
tpi1k	−5	12
l_rumple	20	−6
nbr_stdDev_r100	−6	22
ndvi_p5_r100	1	−25
ndvi_p5_r500	−2	−25
ndvi_p50_r100	18	−17
ndvi_p50_r500	12	−16
ndmi_p95_r100	15	1
LC08_045029_20180726_B1	−33	8
LC08_045029_20180726_B5	−1	10
lg_DistStream	0	−1
lg_DistRoad	−1	−12
lg_cover2m_max	10	−17
lg_cover2m_4m	15	29
lg_cover4m_16m	13	25