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| Title *  |                  |             |  |
| Size: A4   | Number:*         | Revision:*  |   |
| Date: 12/20/2014   | Time: 6:15:48 PM | Sheet* of * |   |
| File: C:\Users\Dalius\Documents\Old\Darbas\I-V converter\HF Resistors.SchDoc |                  |             |   |

A

B

C

D

1

2

3

4

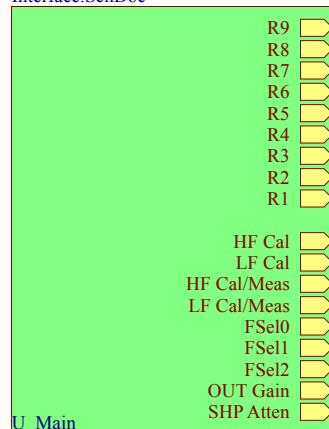
A

B

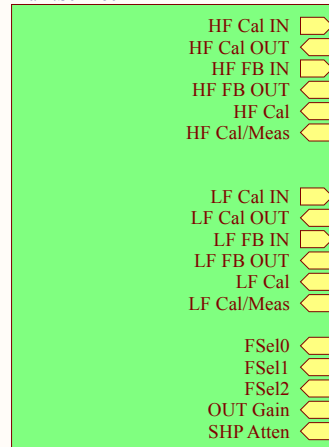
C

D

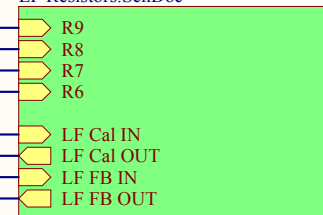
U\_Interface  
Interface.SchDoc



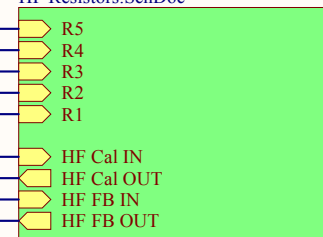
U\_Main  
Main.SchDoc



U\_LF Resistors  
LF Resistors.SchDoc



U\_HF Resistors  
HF Resistors.SchDoc



U\_PSU  
PSU.SchDoc



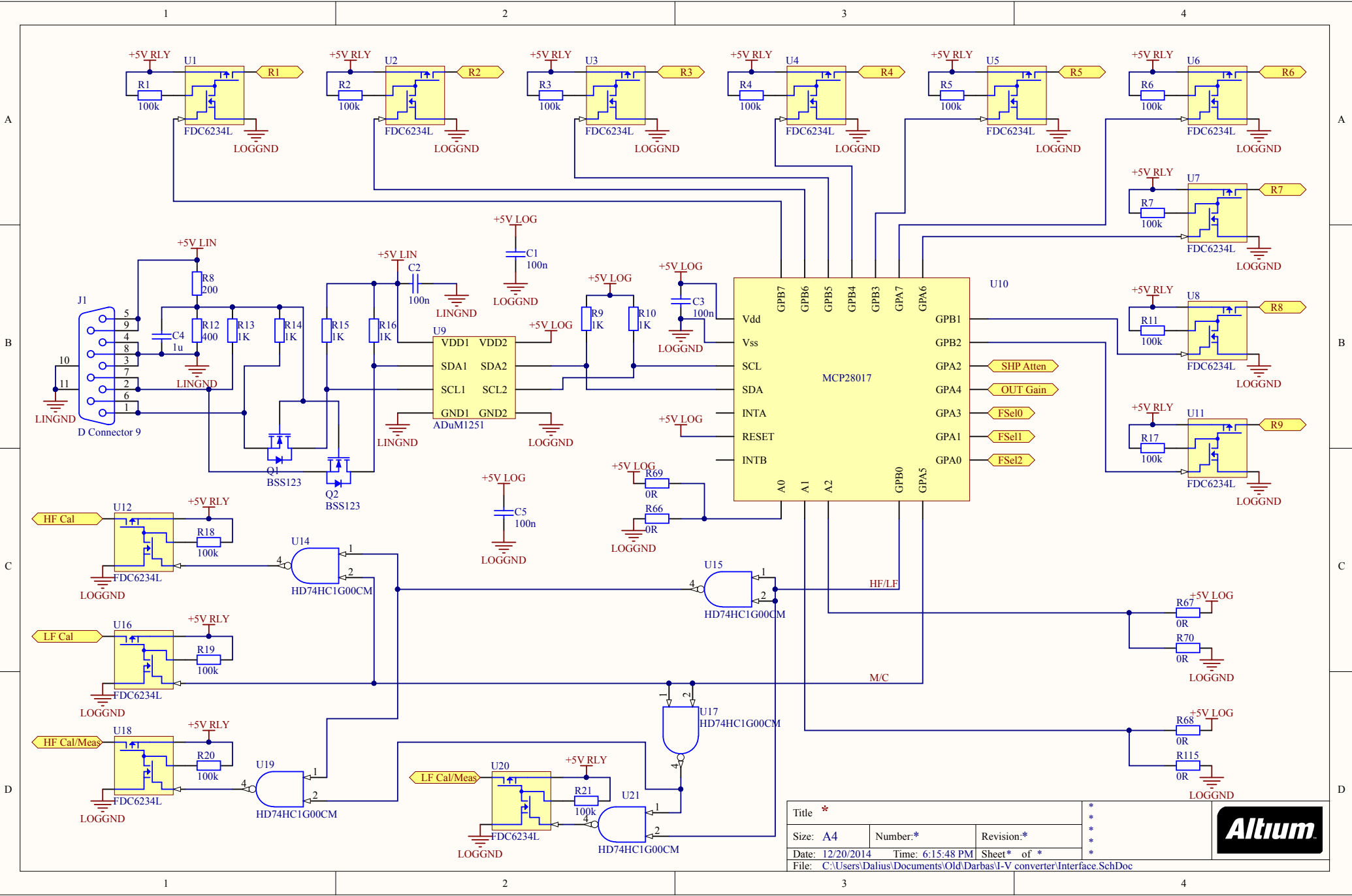
| Title |                               |           |
|-------|-------------------------------|-----------|
| Size  | Number                        | Revision  |
| A     |                               |           |
| Date: | 12/20/2014                    | Sheet of  |
| File: | C:\Users\...\Hierarchy.SchDoc | Drawn By: |

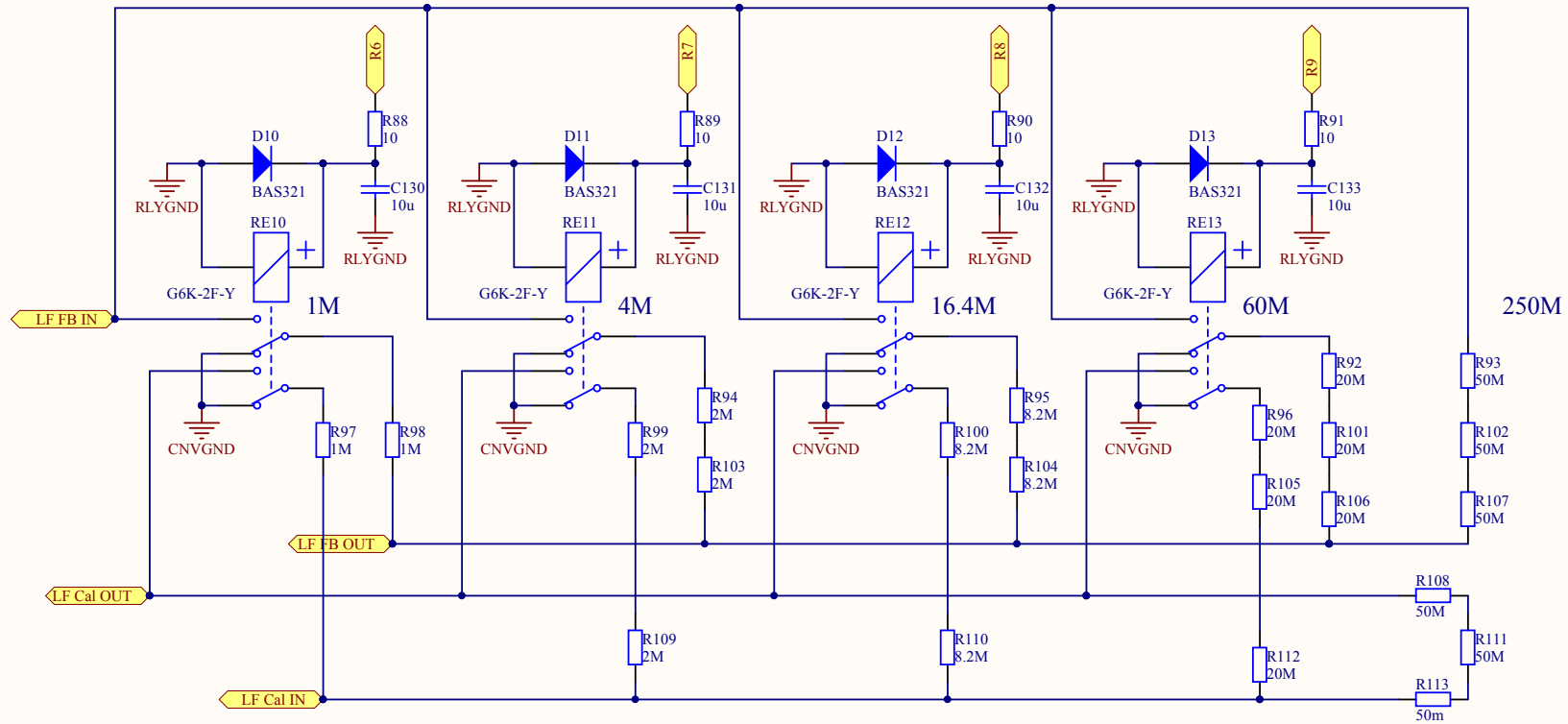
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| Title *  |                  |             | *<br>*<br>*<br>* |
| Size: A4   | Number:*         | Revision:*  |                  |
| Date: 12/20/2014   | Time: 6:15:49 PM | Sheet* of * |                  |
| File: C:\Users\Dalius\Documents\Old\Darbas\I-V converter\LF Resistors.SchDoc |                  |             |                  |



