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*Example 1:*

1. each segments has 8 entries page descriptor table -> 8 pages

**Maximum size = 8 \* page size = 8 \* 2Kb = 16Kb**

2. Task has 4 segments

**Maximum logic address space = 4 \* 16Kb = 64Kb**

3. Virtual address space is 16 bits wide

Page size is 2Kb -> offset is 11 bits

Page table for each segments has 8 entries -> 3 bits for page number

-> 16 - 11 - 3 = 2 bits for segment number

**the format is: (2 bits segment number | 3 bits page number | 11 bits offset)**

00021ABC is reprensented in binary as :

0000 0000 0000 0010 0001 1010 1011 1100

**-> Maxium physical address space = 2^32 = 4GB**

*Example 2:*

1. **CAFE0000**

segment number = 0 -> page table A

page number = 0 -> CAFE

offset = 0000

-> CAFE0000

2. **bad virtual address (segment number invalid)**

segment number = 2 -> invalid

3. **D8BF5555**

segment number = 1 -> page table B

page number = 1 -> D8BF

offset = 5555

-> D8BF5555

*Quiz* 1: **D**

Process Block

P1 B6

P2 B2

P3 B5

P4 B4

P5 B5

*Quiz* 2: **D**

Jobs Block

J1 B1

J2 B3

J3 B3

J4 Not Allocated

*Quiz* 3: **B**

40 < 500 -> hợp lệ

Physical address = base + offset = 1400 + 40 = 1440