



## AN ABSTRACT OF THE DISSERTATION OF

Jeffrey M. Young for the degree of Doctor of Philosophy in Computer Science presented on September 23, 2011.

Title: Variational Satisfiability Solving

Abstract approved: \_\_\_\_\_

Eric Walkingshaw

Over the last two decades, satisfiability and satisfiability-modulo theory (SAT/SMT) solvers have grown powerful enough to be general purpose reasoning engines employed in various areas of software engineering and computer science. However, most practical use cases of SAT/SMT solvers require not just solving a single SAT/SMT problem, but solving sets of related SAT/SMT problems. This discrepancy was directly addressed by the SAT/SMT community with the invention of incremental SAT/SMT solving. However, incremental SAT/SMT solvers require end-users to hand write a program which dictates terms in a set that are shared between problems and terms which are unique. By placing the onus on end-users to write a program, incremental solvers couple the end-users' solution to the end-users' exact sequence of SAT/SMT problems—making the solution overly specific—and require the end-user to write extra infrastructure to coordinate or handle the results. In this thesis, I apply results from research on *variational* programming languages to the domain of SAT/SMT solvers to automate this interaction, creating the first variational SAT/SMT solver. I demonstrate numerous benefits to this approach: End-users need only identify the set of SAT/SMT problems to solve rather than identify the set *and* provide a program. Otherwise difficult optimizations can now be automatically detected and applied. Through use of variational constructs, the variational SAT/SMT can be made asynchronous and both single threaded and multi-threaded versions of variational SAT/SMT solvers are more performant in their expected use case.

©Copyright by Jeffrey M. Young  
September 23, 2011  
All Rights Reserved

# Variational Satisfiability Solving

by

Jeffrey M. Young

A DISSERTATION

submitted to

Oregon State University

in partial fulfillment of  
the requirements for the  
degree of

Doctor of Philosophy

Presented September 23, 2011  
Commencement June 2012

Doctor of Philosophy dissertation of Jeffrey M. Young presented on September 23, 2011.

APPROVED:

---

Major Professor, representing Computer Science

---

Director of the School of Electrical Engineering and Computer Science

---

Dean of the Graduate School

I understand that my dissertation will become part of the permanent collection of Oregon State University libraries. My signature below authorizes release of my dissertation to any reader upon request.

---

Jeffrey M. Young, Author

## ACKNOWLEDGEMENTS

I would like to acknowledge the Starting State and the Transition Function.

## TABLE OF CONTENTS

	<u>Page</u>
1 Introduction	1
1.1 Motivation and Impact . . . . .	1
1.2 Contributions and Outline of this Thesis . . . . .	1
2 Background	2
2.1 Satisfiability Solving . . . . .	2
3 Variational Propositional Logic	3
4 Variational Satisfiability Solving	4
5 Extensions to Variational Satisfiability Solving	5
6 Related Work	6
7 Conclusion	7
7.1 Fin . . . . .	7
Bibliography	7
Appendices	8
A Redundancy . . . . .	9

## Chapter 1: Introduction

### 1.1 Motivation and Impact

### 1.2 Contributions and Outline of this Thesis



## Chapter 2: Background

### 2.1 Satisfiability Solving

## Chapter 3: Variational Propositional Logic

## Chapter 4: Variational Satisfiability Solving

## Chapter 5: Extensions to Variational Satisfiability Solving

## Chapter 6: Related Work

## Chapter 7: Conclusion

Wow, that really was excellent.

### 7.1 Fin

This is the end, my only friend, the end.

## APPENDICES

## Appendix A: Redundancy

This appendix is inoperable.



