

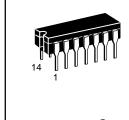
QUAD 2-INPUT NAND GATE

• ESD > 3500 Volts

VCC 14 13 12 11 10 9 8 1 2 3 4 5 6 7 GND

SN54/74LS00

QUAD 2-INPUT NAND GATE LOW POWER SCHOTTKY



J SUFFIX CERAMIC CASE 632-08



N SUFFIX PLASTIC CASE 646-06



D SUFFIX SOIC CASE 751A-02

ORDERING INFORMATION

SN54LSXXJ SN74LSXXN SN74LSXXD Ceramic Plastic SOIC

GUARANTEED OPERATING RANGES

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
T _A	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
ЮН	Output Current — High	54, 74			-0.4	mA
lOL	Output Current — Low	54 74			4.0 8.0	mA

SN54/74LS00

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits					
Symbol	Parameter		Min	Тур	Max	Unit	Test C	onditions
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
V	Input LOW Voltage	54			0.7	V	Guaranteed Input LOW Voltage for All Inputs	
VIL		74			0.8	V		
VIK	Input Clamp Diode Voltage			-0.65	-1.5	V	$V_{CC} = MIN, I_{IN} = -18 \text{ mA}$	
V	Output HIGH Voltage	54	2.5	3.5		V	V _{CC} = MIN, I _O	H = MAX, VIN = VIH
VOH		74	2.7	3.5		V	or V _{IL} per Truth	Table
Voi	Output LOW Voltage	54, 74		0.25	0.4	V	I _{OL} = 4.0 mA	V _{CC} = V _{CC} MIN, V _{IN} = V _{IL} or V _{IH}
VOL		74		0.35	0.5	V	I _{OL} = 8.0 mA	per Truth Table
1	Input HIGH Current				20	μΑ	$V_{CC} = MAX$, $V_{IN} = 2.7 V$	
ΊΗ					0.1	mA	$V_{CC} = MAX$, $V_{IN} = 7.0 V$	
Ι _{ΙL}	Input LOW Current				-0.4	mA	$V_{CC} = MAX$, $V_{IN} = 0.4 V$	
los	Short Circuit Current (Note 1))	-20		-100	mA	V _{CC} = MAX	
Icc	Power Supply Current Total, Output HIGH Total, Output LOW				1.6	mA	VCC = MAX	
					4.4			

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

AC CHARACTERISTICS $(T_A = 25^{\circ}C)$

		Limits				
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
^t PLH	Turn-Off Delay, Input to Output		9.0	15	ns	V _{CC} = 5.0 V
tPHL	Turn-On Delay, Input to Output		10	15	ns	C _L = 15 pF

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Datasheets for electronics components.