

A letter of motivation to the Robot Learning research group

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My background

I have been a Linux Kernel programmer for 20 years, porting Linux Kernel on several new CPUs and implementing low level/embedded drivers (e.g. IOMMU) in the upstream kernel. Those work are all about System Software architecture in traditional Computer Science (i.e. Operating System), using Assembler, C language and JTAG. Recently I got interested in a new concept of Machine Learning compiler (ML compiler) when I worked at Ericsson Research. Here's our published paper, [A TinyMLaaS Ecosystem for Machine Learning in IoT](#)

Historically ML development was about building up models of new algorithms in Python. These models are running on powerful Cloud GPU in Python execution environment (e.g. Pytorch). The size of ML models has been getting bigger and bigger while ML use cases are expanding from central Cloud computing to Edge computing where computing resources are smaller and diversified. There's apparently needs to run ML inference efficiently on various hardware environments,

including the very end of Edge computing. ML hardware accelerator at Edge computing needs its ML compiler to generate optimized executable binary for them. This trend is changing the traditional legacy Computer Science concept (e.g. Compiler) into a new era. For example, there are more needs for domain specific compilers. There are 2 aspects of “ML concept” at compilation. They are supplemental.

- Compiling ML models to convert a computational graph at training into the optimized efficient format. [MLIR](#) and [Apache TVM](#)
- Using ML methods to optimize compile operation itself (e.g. Reinforcement Learning to find the best outcome). [Apache TVM](#) is doing [this](#).

Right now I’m not employed after Ericsson Research and I’m under Helsinki university incubation program, [NEXUS](#), considering to start own business based on our [TinyML](#) solution.

My research interests

I’d like to research further ML compiler area, applying ML algorithms (e.g. RL) to ML compilers, esp. ML HW accelerators. This activity could be naturally extend to generation of HW design too. I was really entertained by “CS-E4890 - Deep Learning D, Lecture” and “ELEC-E8125 - Reinforcement learning D, Lecture”.

Benefit for the Robot Learning research group

Right now I have no idea about the Robot Learning research group I’d like to learn what kind of synergy could be.

Question

I’m not sure that this posted PhD position, [PhD Student Position in Reinforcement Learning and Planning for Hardware Accelerator Design](#), is suitable for my current purpose or not. It would be really nice if we talk about any possible options to research ML compiler area.

Reference

- [Alexander Ilin](#)

Appendix

Courses

I have recently taken the following courses at Aalto university.

- CS-C3240 - Machine Learning D, Lecture
- CS-E407513 - Special Course in Machine Learning, Data Science and Artificial Intelligence D
- CS-E4890 - Deep Learning D, Lecture
- CS-EJ3211 - Machine Learning with Python D
- CS-EJ3311 - Deep Learning with Python D, Lectures
- ELEC-E8125 - Reinforcement learning D, Lecture