TDAQmodule

fEXP	fDW	fActualHardID
fPCtrlMod	fData	fIndex
fCtrlMod	fMapSize	fType
fCtrl	fMaxReg	fBaseIndex
fEventSendMod	fNreg	fNChannel
fMemMap	fNInitReg	fNScalerChan
fBaseAddr	fNTestReg	fNBits
fVirtOffset	fNIRQReg	fInitLevel
fReg	fErrorCode	fIsError
fInitReg	fBus	fgIsA
fTestReg	fID	
fIRQReg	fHardID	

@~TDAQmodule	DecodeIRQ	GetNreg
TDAQmodule	WaitIRQ	GetNInitReg
SetConfig	ResetIRQ	GetNIRQReg
PostInit	EnableIRQ	GetNTestReg
InitReg	DisableIRQ	GetErrorCode
MapReg	RunTrigCtrl	GetBus
SetCtrl	EndTrigCtrl	GetID
Read	ResetTrigCtrl	GetHardID
Write	EnableTrigCtrl	CheckHardID
Read	DisableTrigCtrl	GetIndex
Write	Reset	SetID
MultiRead	ResetData	SetIndex
MultiWrite	GetRegType	GetType
Read	GetModType	IsType
Write	GetEventID	GetBaseIndex
Read	SendEventID	SetBaseIndex
Write	SetDAQexperiment	GetNChannel
Read	GetPCtrlMod	GetNScChannel
Write	GetCtrlMod	SetNChannel
ADCStore	GetCtrl	GetNBits
ScalerStore	GetMemMap	GetInitLevel
ScalerStore	GetBaseAddr	SetInitLevel
ErrorStore	GetVirtOffset	SetEventSendMod
InitWrite	GetVirtAddr	IsError
Test	GetReg	MapSlave
ReadHeader	GetInitReg	Class
ReadHeader	GetIRQReg	Class_Name
AddrS	GetTestReg	IsA
AddrL	GetDW	ShowMembers
ReadIRQ	GetData	Streamer
ReadIRQScaler	GetMapSize	
ReadIRQSlow	GetMaxReg	

TVMEmodule

fAM
fIsWrt
fHardIDReg
fgIsA

@~TVMEmodule
TVMEmodule
SetConfig
PostInit
InitReg
InitReg
InitReg
Read
Write
Read
Write
Write
Write
Read
Write
CheckHardID
GetAM
GetIsWrt
Class
Class_Name
IsA
ShowMembers
Streamer

TVME_CATCH

fCWrt
fSpyData
fMaxSpy
fIWrt
fCatchID
fTCSEventID
fFPGAfile
fTCSRecFile
fF1File
fgIsA

@~TVME_CATCH
TVME_CATCH
SetConfig
PostInit
InitCatchWrt
WrtCatch
WrtCatch
ProgFPGA
Pause
Din
InitFlag
Done
Busy
Din32
Fileout
SpyReset
SpyRead
GetEventID
Class
Class_Name
IsA
ShowMembers
Streamer

TVME_CATCH_TDC

fF1data
fgIsA

@~TVME_CATCH_TDC
TVME_CATCH_TDC
SetConfig
PostInit
ReadIRQ
F1ReadFile
F1Setup
Class
Class_Name
IsA
ShowMembers
Streamer