ΓA2System	TDAQmodule		
dList[5]	fEXP	fDW	fActualHardID
utFile	fPCtrlMod	fData	fIndex
File	fCtrlMod	fMapSize	fType
Stream	fCtrl	fMaxReg	fBaseIndex
it	fEventSendMod	fNreg	fNChannel
rror	fMemMap	fNInitReg	fNScalerChan
onfigPass	fBaseAddr	fNTestReg	fNBits
A	fVirtOffset	fNIRQReg	fInitLevel
	fReg	fErrorCode	flsError
TA OCCUPATIONS	fInitReg	fBus	fglsA
TA2System	fTestReg	fID	
elnit	fIRQReg	fHardID	
tError	@ TDAO a dada	December 1900	O-tN
tMessage	@~TDAQmodule	DecodelRQ	GetNreg
2Key	TDAQmodule	WaitIRQ	GetNInitReg
2Key	SetConfig	ResetIRQ	GetNIRQReg
2String	PostInit	EnableIRQ	GetNTestReg
MapKey	InitReg	DisableIRQ	GetErrorCode
MapString	MapReg	RunTrigCtrl	GetBus
CmdList	SetCtrl Read	EndTrigCtrl	GetID
Config		ResetTrigCtrl	GetHardID
nmandConfig	Write	EnableTrigCtrl	CheckHardID
eMisc	Read	DisableTrigCtrl	GetIndex
Config	Write	Reset	SetID
tlnit	MultiRead	ResetData	SetIndex
dName	MultiWrite	GetRegType	GetType
ckName	Read	GetModType	IsType
nputFile	Write	GetEventID	GetBaseIndex
LogFile	Read	SendEventID	SetBaseIndex
_ogStream	Write	SetDAQexperime	
.ogStream	Read	GetPCtrlMod	GetNScChannel
LogFile	Write	GetCtrlMod	SetNChannel
t	ADCStore	GetCtrl	GetNBits
ror	ScalerStore	GetMemMap	GetInitLevel
S Name	ScalerStore	GetBaseAddr	SetInitLevel
s_Name	ErrorStore	GetVirtOffset	SetEventSend
wMembers	InitWrite	GetVirtAddr	IsError
owwembers	Test	GetReg	MapSlave
	ReadHeader	GetInitReg	Class Name
	ReadHeader	GetIRQReg GetTestReg	Class_Name
	AddrS	•	IsA ShowMombars
	AddrL	GetDW	ShowMembers
	ReadIRQ	GetData	Streamer
	ReadIRQScaler	GetMapSize	
	ReadIRQSlow	GetMaxReg	
	-		

TVMEmodule fAM flsWrt **fHardIDReg** fglsA @~TVMEmodule **TVMEmodule** SetConfig PostInit InitReg InitReg InitReg Read Write Read Write Write Write Read Write CheckHardID **GetAM** GetIsWrt Class

Class Name

ShowMembers

Streamer

IsA

TVME_VUPROM **fTCSresetDelay** fDebugOut[16] **fPatternChan fTAPSEnable fEnableCPU** fMThresh[16] **fFCDelay** fTrigParm[64] fIntDelay[8] fCommandReply[1 **fRAMid** fIsIRQEnabled fInputMask flsPattRead fInputPrescale[16] flsScalerRead fL1Prescale[16] flsGUlpermit **flsTCSStarted** fL2Prescale[16] fL1Width fglsA fL1Delay fL2Delay @~TVME_VUPROMStartTCS TVME_VUPROM **StopTCS** SetConfig **GetCommandRepl PostInit** IsIRQEnabled **WaitIRQ IsPattRead**

IsScalerRead

GetFCDelay

GetIntDelay

GetInputMask

GetL1Prescale

GetL2Prescale

GetL1Width

GetL1Delay

GetL2Delay

GetMThresh

Class_Name

ShowMembers

Streamer

Class

IsA

GetInputPrescale

GetRAMid

GetEnableCPU

ResetIRQ

EnableIRQ

DisableIRQ

RunTrigCtrl

EndTrigCtrl

ResetTrigCtrl

EnableTrigCtrl

DisableTrigCtrl

ReadIRQScaler

CheckHardID

RAMDownload

SetDebugOut

SetPrescale

SetIntDelay

CmdExe

GetParms

SendEventID

GetEventID SetMThresh

ReadIRQ