TA2System	TDAQmodule		
st[5]	fEXP	fDW	fActualHardID
	fPCtrlMod	fData	fIndex
	fCtrlMod	fMapSize	fType
	fCtrl	fMaxReg	fBaseIndex
	fEventSendMod	fNreg	fNChannel
	fMemMap	fNInitReg	fNScalerChan
SS	fBaseAddr	fNTestReg	fNBits
	fVirtOffset	fNIRQReg	fInitLevel
	fReg	fErrorCode	flsError
1	fInitReg	fBus	fglsA
	fTestReg	fID	
	fIRQReg	fHardID	
	@~TDAQmodule	DecodelRQ	GetNreg
	TDAQmodule	WaitIRQ	GetNInitReg
	SetConfig	ResetIRQ	GetNIRQReg
	PostInit	EnableIRQ	GetNTestReg
	InitReg	DisableIRQ	GetErrorCode
	MapReg	RunTrigCtrl	GetBus
	SetCtrl	EndTrigCtrl	GetID
	Read	ResetTrigCtrl	GetHardID
ig	Write	EnableTrigCtrl	CheckHardID
	Read	DisableTrigCtrl	GetIndex
	Write	Reset	SetID
	MultiRead	ResetData	SetIndex
	MultiWrite	GetRegType	GetType
	Read	GetModType	IsType
	Write	GetEventID	GetBaseIndex
	Read	SendEventID	SetBaseIndex
	Write	SetDAQexperiment	GetNChannel
n	Read	GetPCtrlMod	GetNScChannel
	Write	GetCtrlMod	SetNChannel
	ADCStore	GetCtrl	GetNBits
	ScalerStore	GetMemMap	GetInitLevel
	ScalerStore	GetBaseAddr	SetInitLevel
	ErrorStore	GetVirtOffset	SetEventSendMod
	InitWrite	GetVirtAddr	IsError
	Test	GetReg	MapSlave
	ReadHeader	GetInitReg	Class
	ReadHeader	GetIRQReg	Class_Name
	AddrS	GetTestReg	IsA
	AddrL	GetDW	ShowMembers
	ReadIRQ	GetData	Streamer
	ReadIRQScaler	GetMapSize	
	ReadIRQSlow	GetMaxReg	
	Readingsiow	GetwaxNeg	

TCAMACmodule fC fΝ fglsA @~TCAMACmodule **TCAMAC**module SetConfig Postlnit Init InitReg Read Write Read Write ReadIRQ

CNAF

Class

IsA

CNAFL

Streamer

ReadIRQScaler Class_Name ShowMembers

fTCSresetDelay fglsA @~TCAMAC_4413SPI TCAMAC_4413SPILL SetConfig

TCAMAC_4413SPILL

PostInit RunTrigCtrl **EndTrigCtrl** ResetTrigCtrl **EnableTrigCtrl** DisableTrigCtrl Class Class_Name IsA

ShowMembers

Streamer

fTrigSet

fTrigExe