

ATTACKING MICROCONTROLLERS

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Microcontroller Introduction

Microcontrollers can be found anywhere, from your cars stereo to missile launch panels and are usually cheap (around £2) and packed with information! They often come with crypto-engines (AES, DES and RSA are common) and hold all sorts of information like private crypto-keys for authentication or proprietary algorithm implementations in the firmware or hardware, interesting all sorts of people into the contents of a microcontroller.

Packaging and De-packaging

Typically microcontrollers are too small and fragile to use as they are fabricated (with fabrication lengths shrank to micrometers) and so they are packaged[2]. Packaging material ranges depending on the microcontroller and its intended use, but is usually hard epoxy resin [1] [2]. The packaging tries to protect the microcontroller from its external environment (humidity, radiation, temperature, crashes etc.) and also from prying eyes. Military-grade chips come with a lot of additional circuitry on the packaging whose responsibility is to detect tampering and respond in a suitable manner (even self destruction!) [2].

De-packaging is not always requierd and the methods depend on the packaging used and protective mechanisms in place, but on epoxy-packaged chips one can etch the epoxy away by using HNO_3 or H_2SO_4 and then cleaning the chip in an ultrasonic bath [1] [2]. For other packaging types, e.g. metal, ceramic or plastic, one can use similar techniques and tools, e.g. drills or a blowtorch [2]. De-packaging is usually easier than expected and removing simple epoxy resin can be done with readily available chemicals [2]. Fig. 1 shows a microcontroller in its factory resin packaging. Fig. 2 shows the exposed die after chemical decapsulation[3].

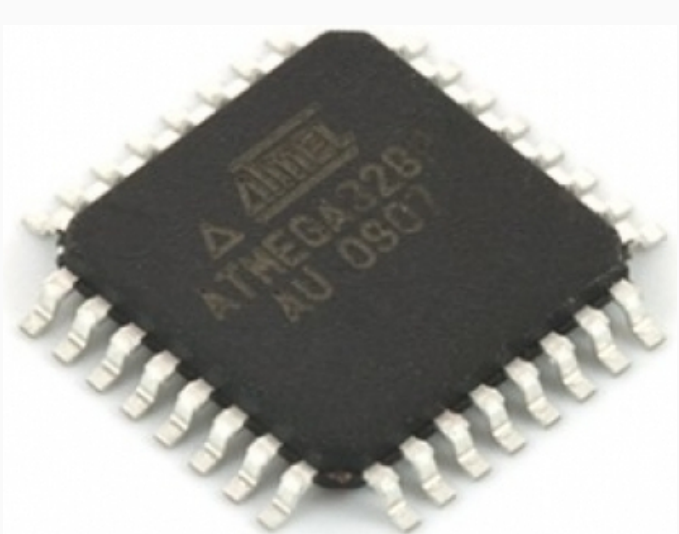


Fig. 1: ATmega328 with epoxy resin packaging.

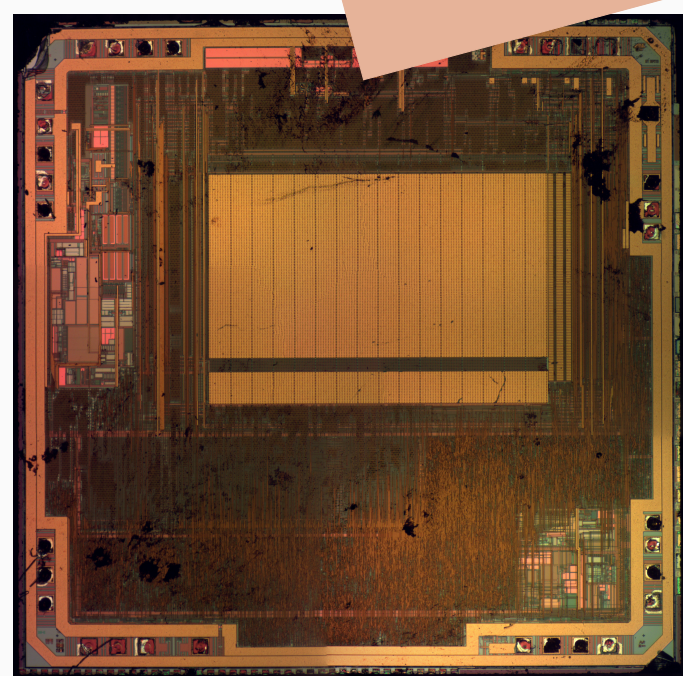
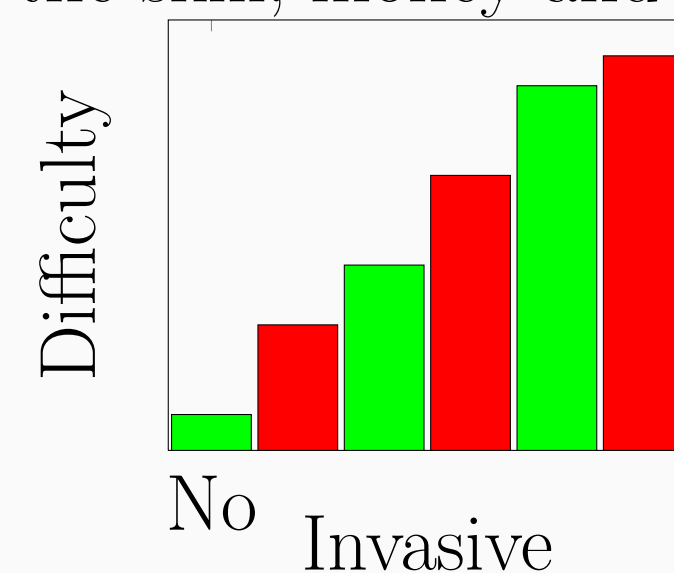


Fig. 2: The exposed die of an ATmega328.

Attack Types

Non-invasive attacks are cheap and easy to perform and require no decapsulation. Popular methods include are power analysis and fault injection, where faults may be injected by exposing the chip to environmental conditions that it was not meant to work in.

Fig. describes the relative difficulty of the attacks methods (green) and complexity of the defensive mechanisms (red), where the notions of difficulty and complexity incorporate the skill, money and machinery needed.



attacks require decapsulation and are a lot more technical, expensive (to perform and repliate) and time consuming with manufacturer-equivalent machinery used.

Sample Attack

provide atmega644 characteristics. set attack scenario, type, setup and exact details

tampering detection means detecting abnormalities in voltage, clock frequency, radiation, tilting etc.

References

- [1] Sergei P. Skorobogatov. Semi-invasive attacks – A new approach to hardware security analysis. Technical Report UCAM-CL-TR-630, University of Cambridge, Computer Laboratory, April 2005.
- [2] Bulent Yener and Andrew Zonenberg. CSCI 4.74 / 6974 : Hardware Reverse Engineering. Rensselaer Polytechnic Institute, lecture slides at : <http://security.cs.rpi.edu/courses/hwre-spring2014/>, 2014.
- [3] Andrew Zonenberg. Atmega328 decapsulation. <http://siliconpr0n.org/archive/doku.php?id=azonenberg:atmel:atmega328>, March 2014.