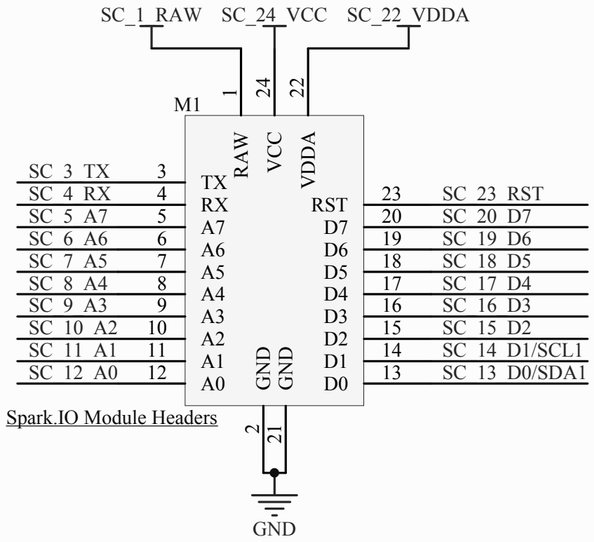
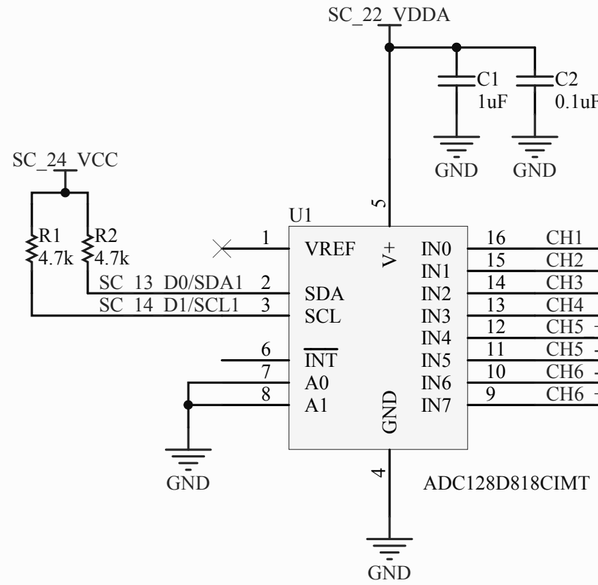


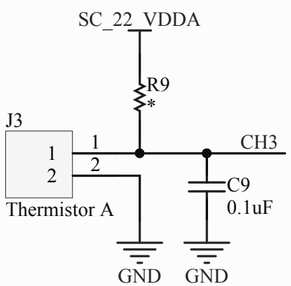
Spark.IO Module Headers

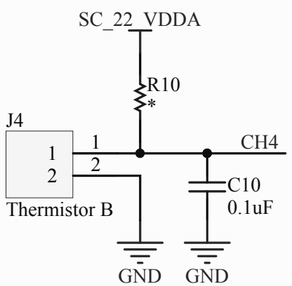
*SparkIO Module
RAW = Diode drop down from +5VUSB
VCC = 3.3V, 500mA Max
VDDA = 3.3V through 500mA ferrite, analog VDD
RST = Reset switch on Spark.io Module

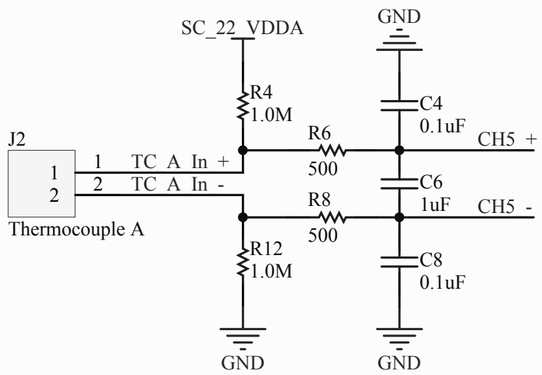
*ADC128D818 Module
*Internal VRef = 2.56V
*ADC will operate in "Mode 3" - In0 through In3 are single ended inputs, IN4/IN5 and IN7/IN6 are differential
*A1 and A0 are pulled low, so address is 001_1101b = 0x1D
*Should the interrupt line go back to the sparkIO?
*Should I add caps for an external Vref?
*Should I make a dedicated ADC_VCC with a ferrite and caps?

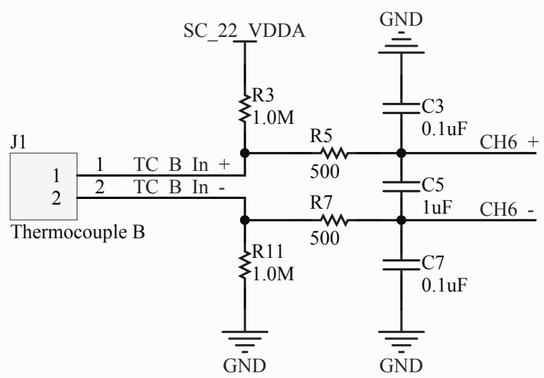


Thermistor Inputs
Resistor values are TBD as we do not know what thermistors are common in our application. 10k? 100k?









Title	***SparkIO Thermal Shield - Aux ADC***		
Size: A4	Number:*	Revision:A	
Date: 4/14/2014	Time: 11:40:12 PM	Sheet 1 of 1	
File: C:\Users\Jim\Documents\GitHub\probe\SparkIO Thermal Shield\SparkIO Thermal Shield.SchDoc			

Altium