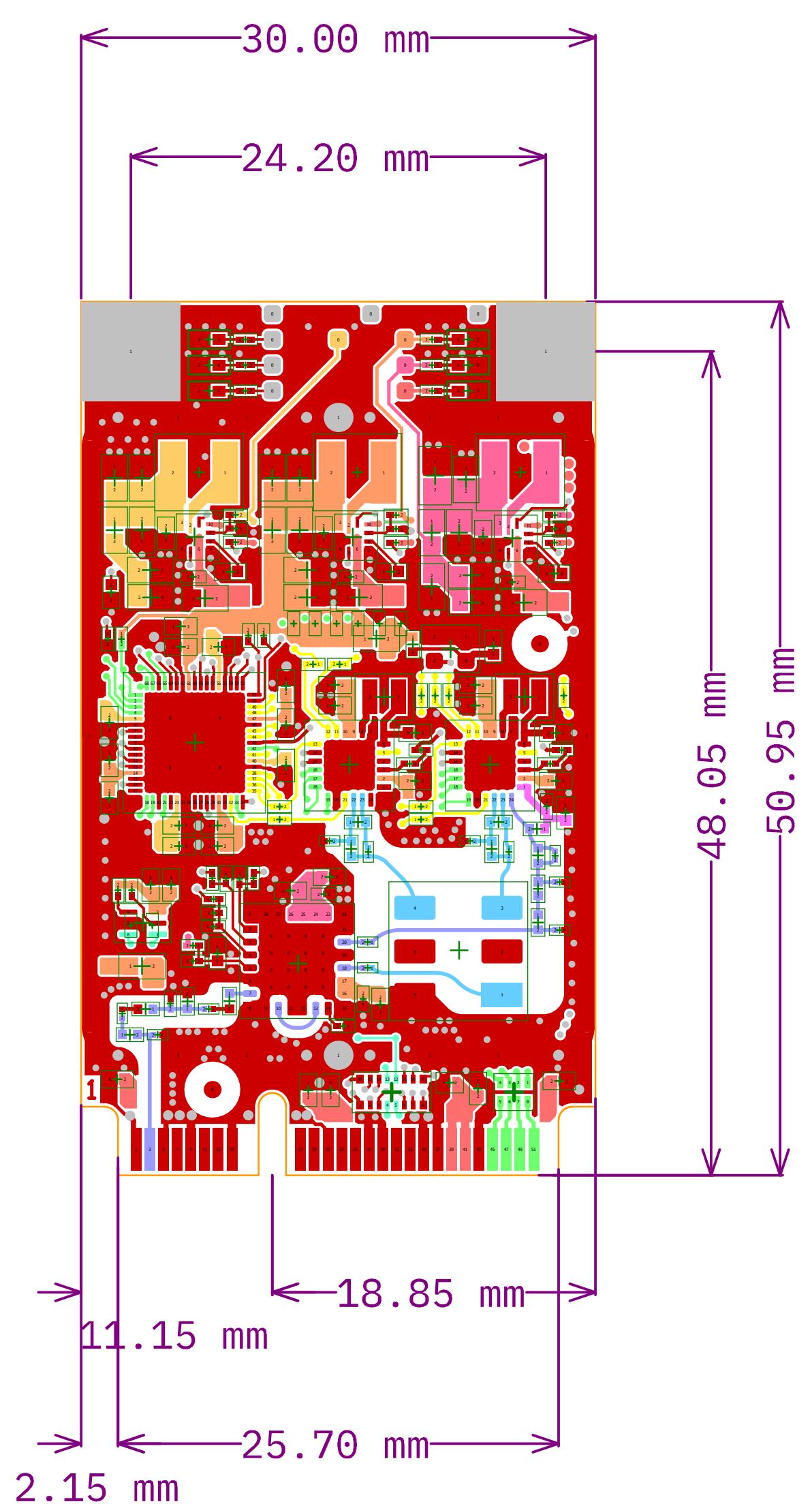


A



| Layer | Name | Material | Thickness | Constant | Gerber |
|-------|----------------------------------|----------|-----------|----------|--------|
| | Top Overlay | | | | GTO |
| | Top Solder | SM-001 | 0.025mm | 4 | GTS |
| 1 | Top | CF-003 | 0.018mm | | GTL |
| | PP-top | PP-008 | 0.130mm | 4.1 | |
| 2 | GND-1 | CF-004 | 0.035mm | | GP1 |
| | Core-1 | Core-009 | 0.180mm | 4.5 | |
| 3 | Signal-1 | CF-004 | 0.035mm | | G1 |
| | PP-mid | PP-008 | 0.130mm | 4.1 | |
| 4 | Signal-2 | CF-004 | 0.035mm | | G2 |
| | Core-2 | Core-009 | 0.180mm | 4.5 | |
| 5 | GND-2 | CF-004 | 0.035mm | | GP2 |
| | PP-bottom | PP-008 | 0.130mm | 4.1 | |
| 6 | Bottom | CF-003 | 0.018mm | | GBL |
| | Board Layer Stack Bottom Solder | SM-001 | 0.025mm | 4 | GBS |
| | Board Layer Stack Bottom Overlay | | | | GBO |

Total board thickness: 0.976mm

C

| | |
|-------------------------------------------------------------------------------------------------------------------|---------------|
| Pluton UPV | PLUTON |
| Project: Estigia comms payload - LoRa Gateway SoM | |
| Layer: Top Shape Component Guard Dimension | |
| Engineer: Juan Del Pino Mena | |
| Date: 2024-08-11 | Revision: 0.4 |
| Size: A4 | |

A

B

C

D

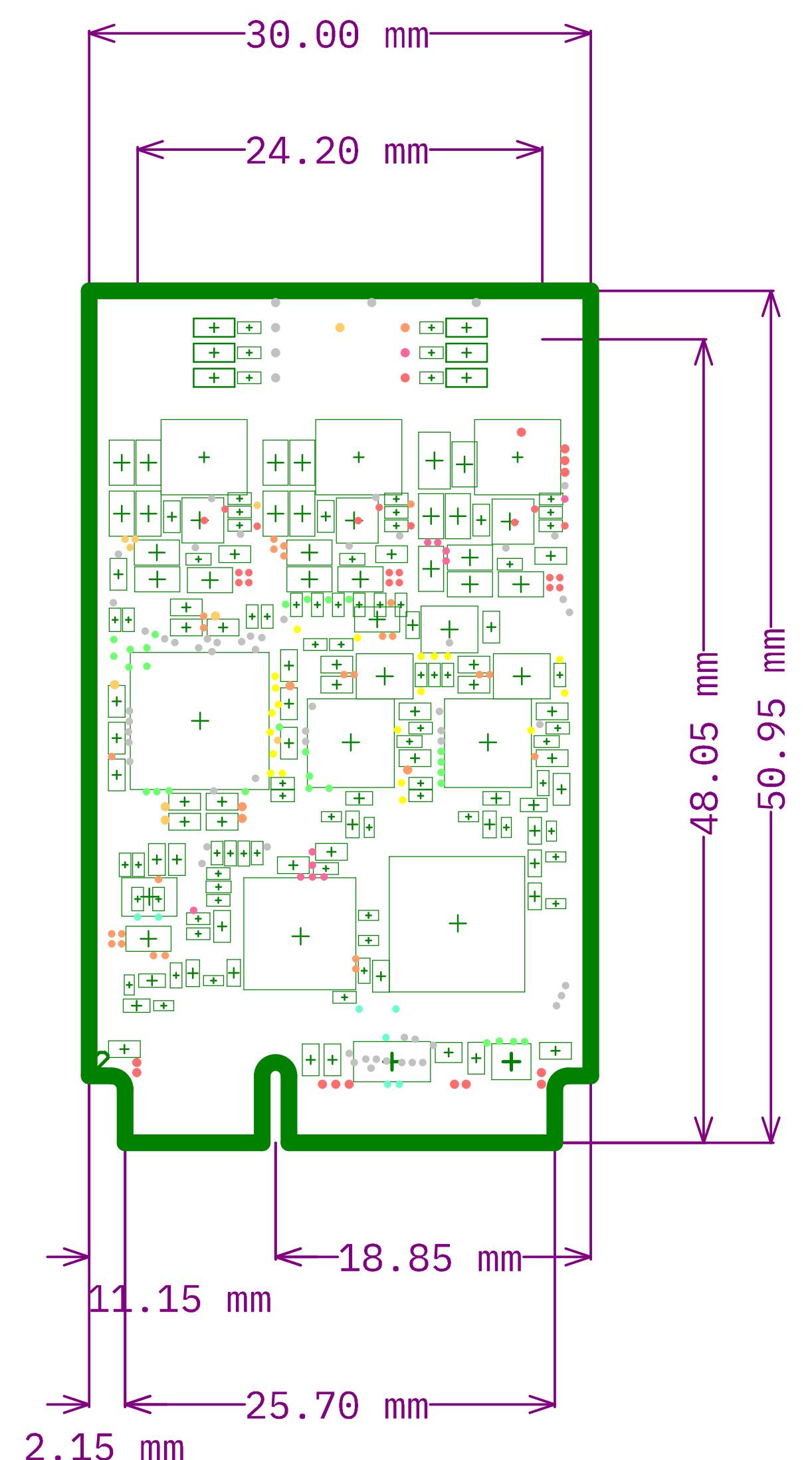
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| Layer | Name | Material | Thickness | Constant | Gerber |
|-------|----------------------------------|----------|-----------|----------|--------|
| | Top Overlay | | | | GTO |
| | Top Solder | SM-001 | 0.025mm | 4 | GTS |
| 1 | Top | CF-003 | 0.018mm | | GTL |
| | PP-top | PP-008 | 0.130mm | 4.1 | |
| 2 | GND-1 | CF-004 | 0.035mm | | GP1 |
| | Core-1 | Core-009 | 0.180mm | 4.5 | |
| 3 | Signal-1 | CF-004 | 0.035mm | | G1 |
| | PP-mid | PP-008 | 0.130mm | 4.1 | |
| 4 | Signal-2 | CF-004 | 0.035mm | | G2 |
| | Core-2 | Core-009 | 0.180mm | 4.5 | |
| 5 | GND-2 | CF-004 | 0.035mm | | GP2 |
| | PP-bottom | PP-008 | 0.130mm | 4.1 | |
| 6 | Bottom | CF-003 | 0.018mm | | GBL |
| | Board Layer Stack Bottom Solder | SM-001 | 0.025mm | 4 | GBS |
| | Board Layer Stack Bottom Overlay | | | | GBO |

Total board thickness: 0.976mm

| | | |
|------------------------------|---------------------------------------------------|--------------|
| Pluton UPV | Project: Estigia comms payload - LoRa Gateway SoM | Layer: GND-1 |
| Component Guard | Dimension | |
| Engineer: Juan Del Pino Mena | | |
| Date: 2024-08-11 | Revision: 0.4 | Size: A4 |

PLUTON

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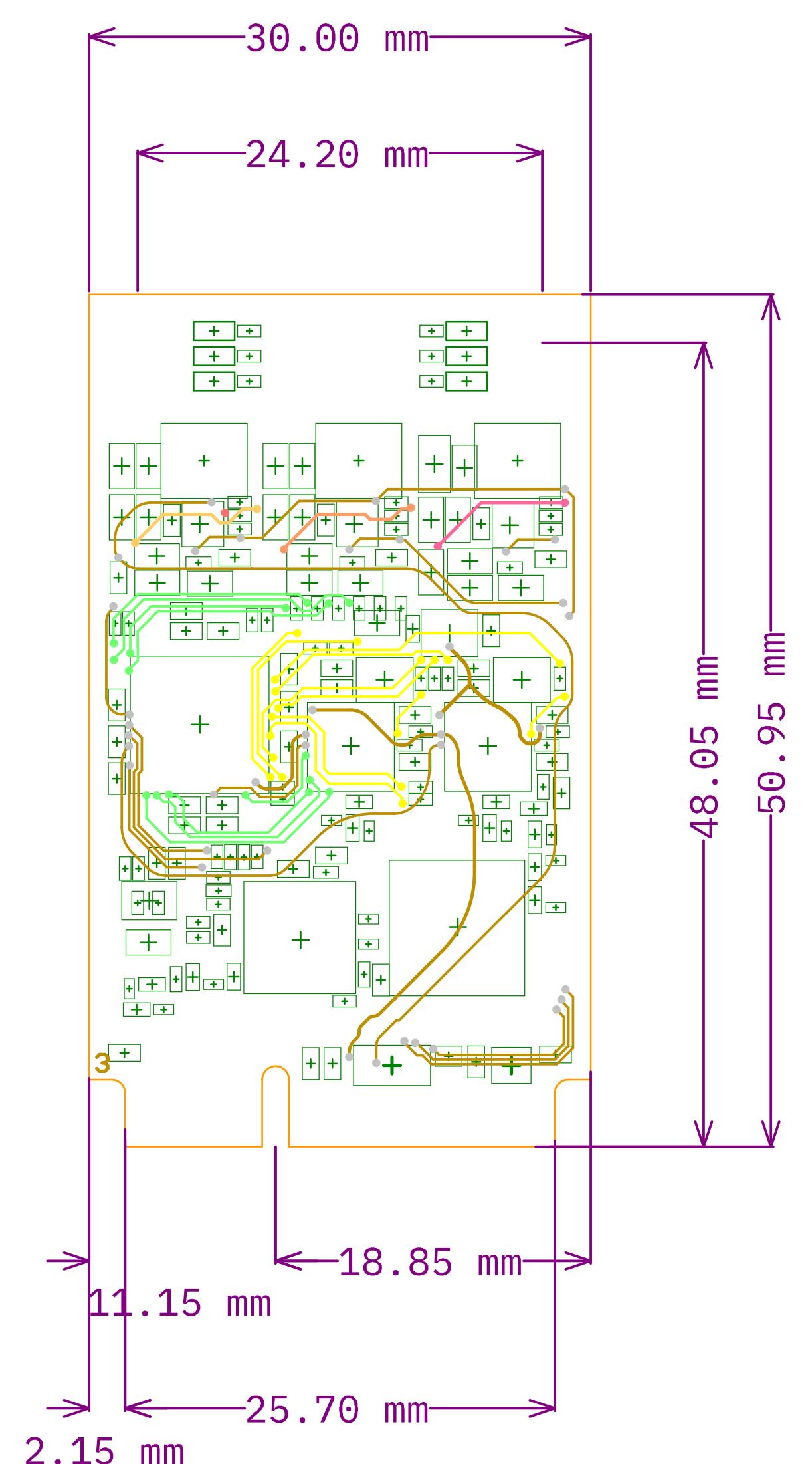
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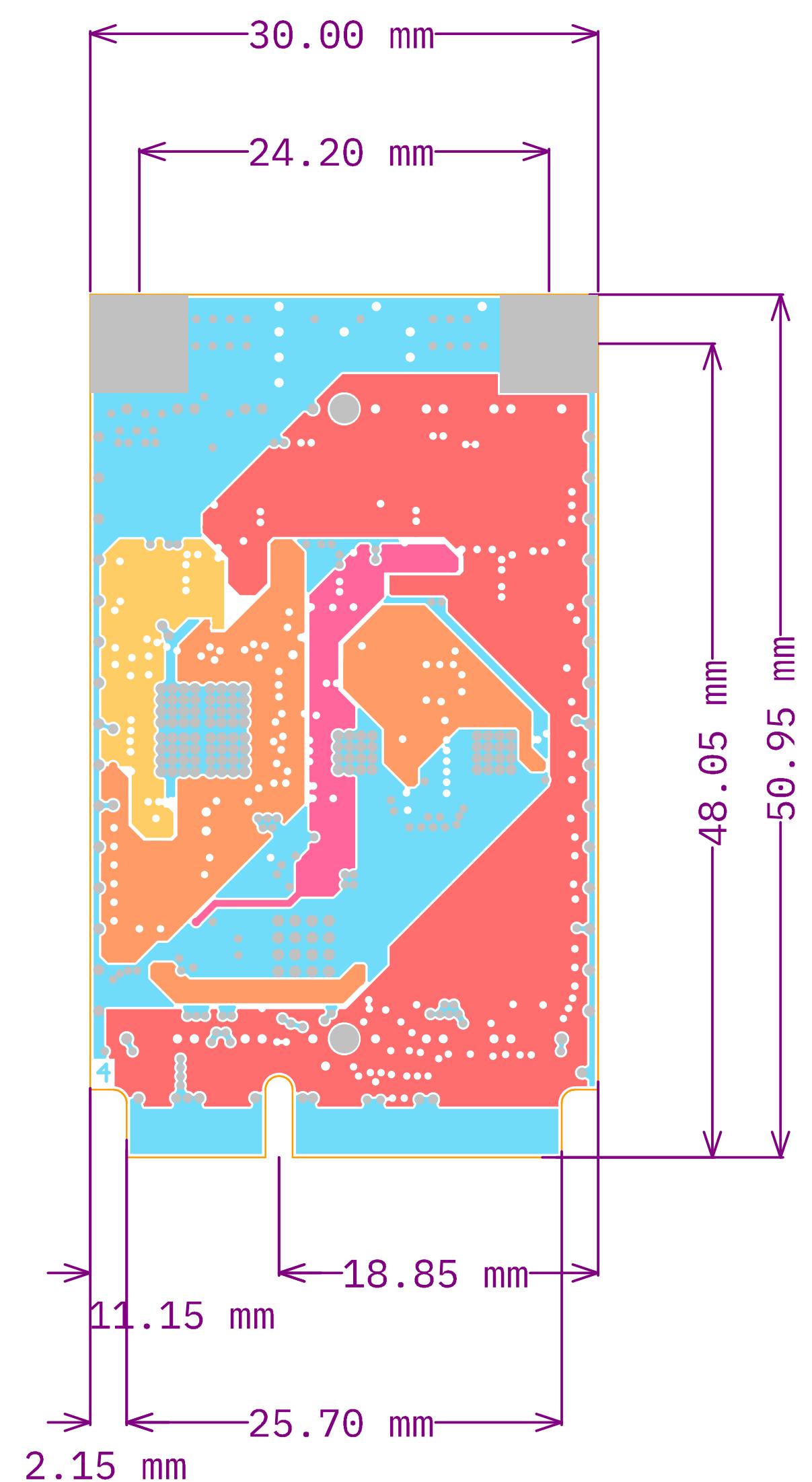
| Layer | Name | Material | Thickness | Constant | Gerber |
|-------|----------------------------------|----------|-----------|----------|--------|
| | Top Overlay | | | | GTO |
| | Top Solder | SM-001 | 0.025mm | 4 | GTS |
| 1 | Top | CF-003 | 0.018mm | | GTL |
| | PP-top | PP-008 | 0.130mm | 4.1 | |
| 2 | GND-1 | CF-004 | 0.035mm | | GP1 |
| | Core-1 | Core-009 | 0.180mm | 4.5 | |
| 3 | Signal-1 | CF-004 | 0.035mm | | G1 |
| | PP-mid | PP-008 | 0.130mm | 4.1 | |
| 4 | Signal-2 | CF-004 | 0.035mm | | G2 |
| | Core-2 | Core-009 | 0.180mm | 4.5 | |
| 5 | GND-2 | CF-004 | 0.035mm | | GP2 |
| | PP-bottom | PP-008 | 0.130mm | 4.1 | |
| 6 | Bottom | CF-003 | 0.018mm | | GBL |
| | Board Layer Stack Bottom Solder | SM-001 | 0.025mm | 4 | GBS |
| | Board Layer Stack Bottom Overlay | | | | GBO |

Total board thickness: 0.976mm

| | | |
|-----------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------|
| Pluton UPV | Project: Estigia comms payload - LoRa Gateway SoM Layer: Board Shape Signal Component Guard Dimension Engineer: Juan Del Pino Mena Date: 2024-08-11 Revision: 0.4 | Size: A4 |
| Project: Estigia comms payload - LoRa Gateway SoM | | |
| Layer: Board Shape Signal Component Guard Dimension | | |
| Engineer: Juan Del Pino Mena | | |

PLUTON

A



| Layer | Name | Material | Thickness | Constant | Gerber |
|-------|----------------------------------|----------|-----------|----------|--------|
| | Top Overlay | | | | GTO |
| | Top Solder | SM-001 | 0.025mm | 4 | GTS |
| 1 | Top | CF-003 | 0.018mm | | GTL |
| | PP-top | PP-008 | 0.130mm | 4.1 | |
| 2 | GND-1 | CF-004 | 0.035mm | | GP1 |
| | Core-1 | Core-009 | 0.180mm | 4.5 | |
| 3 | Signal-1 | CF-004 | 0.035mm | | G1 |
| | PP-mid | PP-008 | 0.130mm | 4.1 | |
| 4 | Signal-2 | CF-004 | 0.035mm | | G2 |
| | Core-2 | Core-009 | 0.180mm | 4.5 | |
| 5 | GND-2 | CF-004 | 0.035mm | | GP2 |
| | PP-bottom | PP-008 | 0.130mm | 4.1 | |
| 6 | Bottom | CF-003 | 0.018mm | | GBL |
| | Board Layer Stack Bottom Solder | SM-001 | 0.025mm | 4 | GBS |
| | Board Layer Stack Bottom Overlay | | | | GBO |

Total board thickness: 0.976mm

C

| | |
|---------------------------------------------------|---------------|
| Pluton UPV | Dimension |
| Project: Estigia comms payload - LoRa Gateway SoM | |
| Layer: Board Shape | |
| Engineer: Juan Del Pino Mena | |
| Date: 2024-08-11 | Revision: 0.4 |
| Size: A4 | |

PLUTON

A

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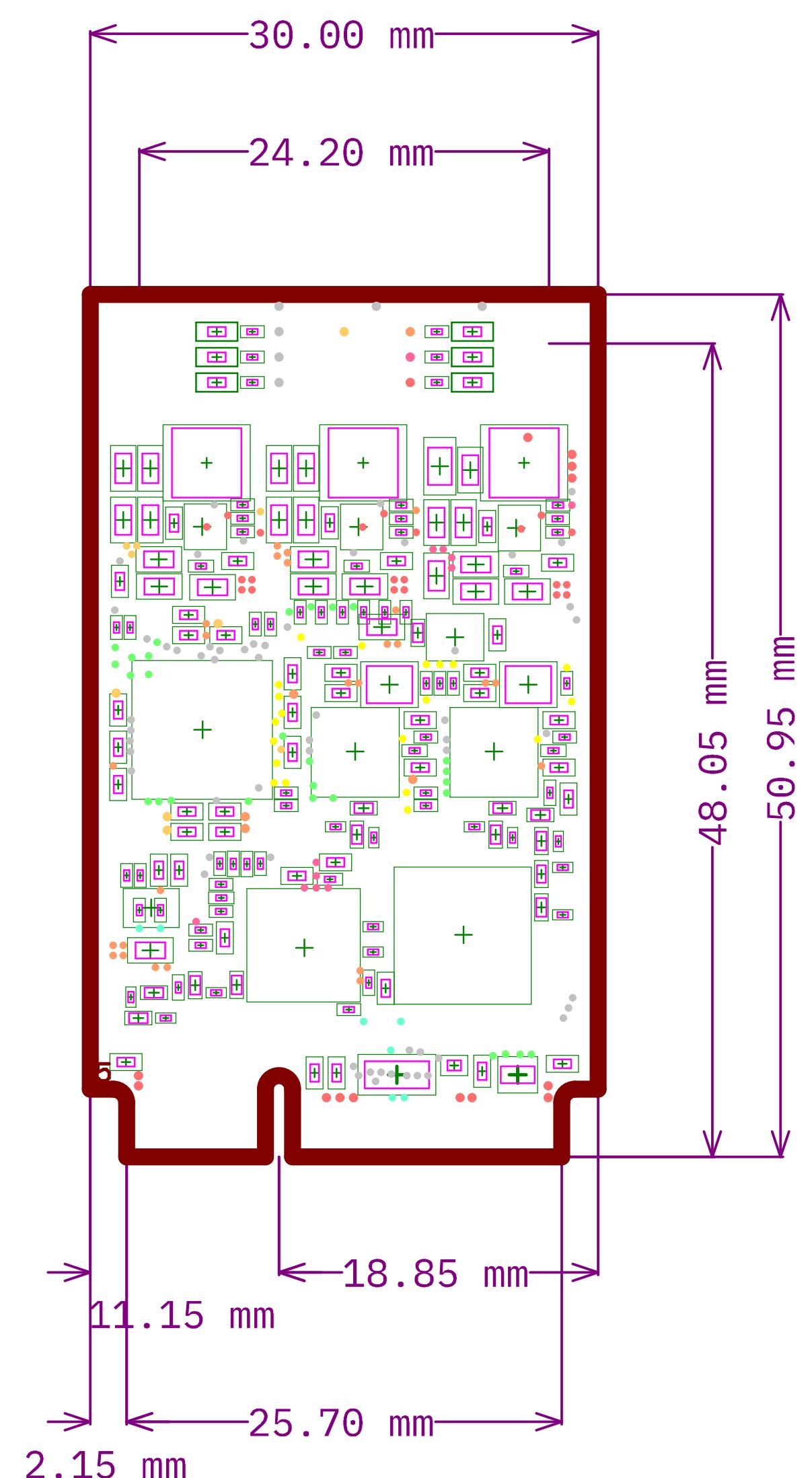
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| Layer | Name | Material | Thickness | Constant | Gerber |
|-------|----------------------------------|----------|-----------|----------|--------|
| | Top Overlay | | | | GTO |
| | Top Solder | SM-001 | 0.025mm | 4 | GTS |
| 1 | Top | CF-003 | 0.018mm | | GTL |
| | PP-top | PP-008 | 0.130mm | 4.1 | |
| 2 | GND-1 | CF-004 | 0.035mm | | GP1 |
| | Core-1 | Core-009 | 0.180mm | 4.5 | |
| 3 | Signal-1 | CF-004 | 0.035mm | | G1 |
| | PP-mid | PP-008 | 0.130mm | 4.1 | |
| 4 | Signal-2 | CF-004 | 0.035mm | | G2 |
| | Core-2 | Core-009 | 0.180mm | 4.5 | |
| 5 | GND-2 | CF-004 | 0.035mm | | GP2 |
| | PP-bottom | PP-008 | 0.130mm | 4.1 | |
| 6 | Bottom | CF-003 | 0.018mm | | GBL |
| | Board Layer Stack Bottom Solder | SM-001 | 0.025mm | 4 | GBS |
| | Board Layer Stack Bottom Overlay | | | | GBO |

Total board thickness: 0.976mm

| | | | | |
|------------------------------|---------------------------------------------------|--------------|---------------------------|----------|
| Pluton UPV | Project: Estigia comms payload - LoRa Gateway SoM | Layer: GND-2 | Component Guard Dimension | PLUTON |
| | | | | |
| Engineer: Juan Del Pino Mena | | | | |
| Date: 2024-08-11 | Revision: 0.4 | | | Size: A4 |

A

B

C

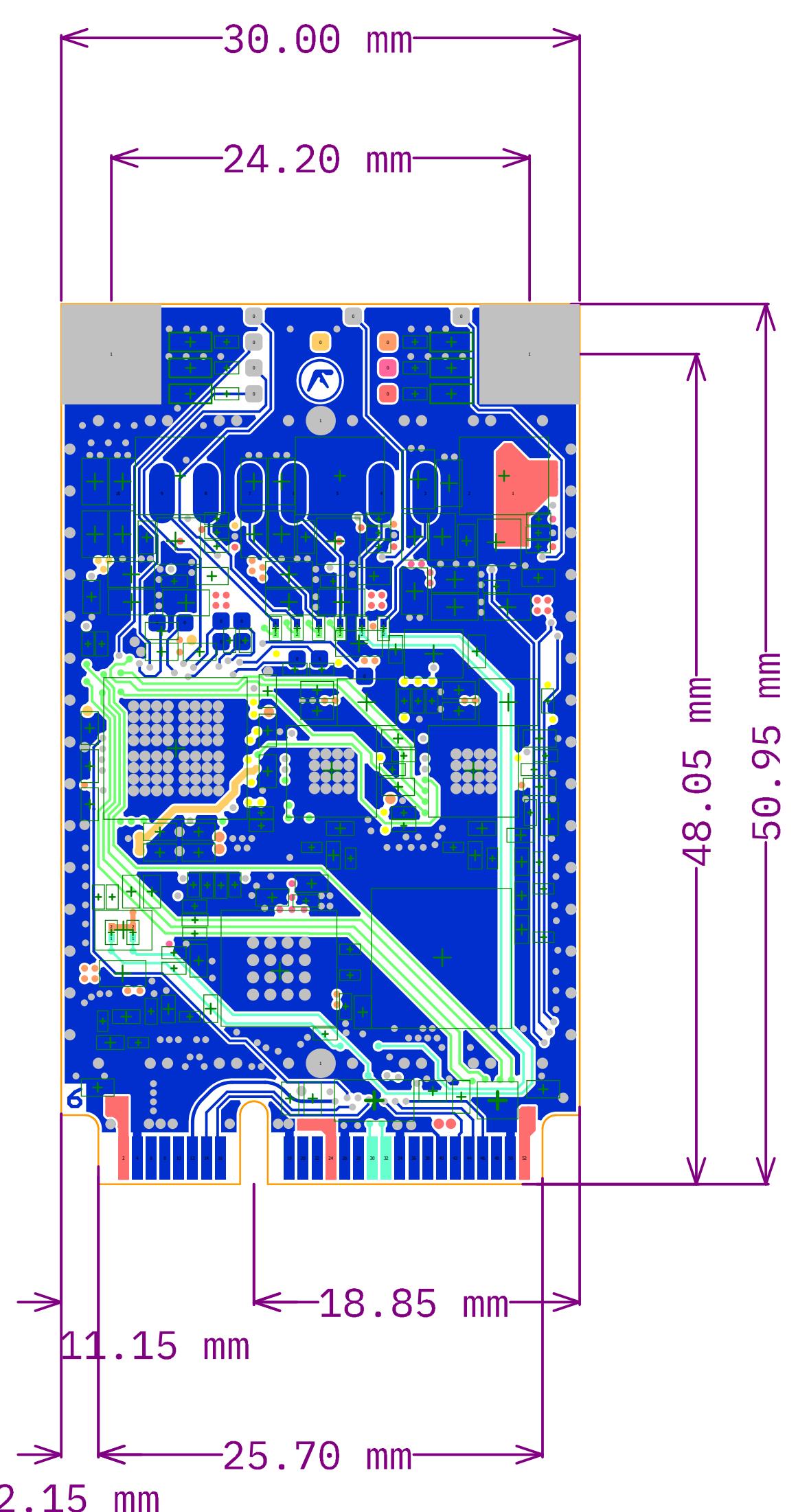
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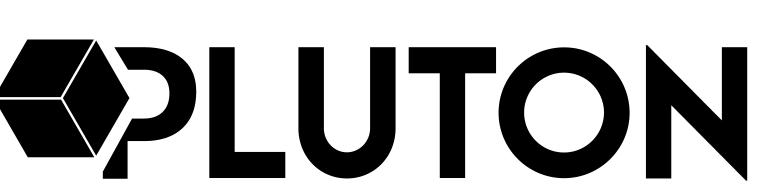
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D



| Layer | Name | Material | Thickness | Constant | Gerber |
|-------|----------------------------------|----------|-----------|----------|--------|
| | Top Overlay | | | | GTO |
| | Top Solder | SM-001 | 0.025mm | 4 | GTS |
| 1 | Top | CF-003 | 0.018mm | | GTL |
| | PP-top | PP-008 | 0.130mm | 4.1 | |
| 2 | GND-1 | CF-004 | 0.035mm | | GP1 |
| | Core-1 | Core-009 | 0.180mm | 4.5 | |
| 3 | Signal-1 | CF-004 | 0.035mm | | G1 |
| | PP-mid | PP-008 | 0.130mm | 4.1 | |
| 4 | Signal-2 | CF-004 | 0.035mm | | G2 |
| | Core-2 | Core-009 | 0.180mm | 4.5 | |
| 5 | GND-2 | CF-004 | 0.035mm | | GP2 |
| | PP-bottom | PP-008 | 0.130mm | 4.1 | |
| 6 | Bottom | CF-003 | 0.018mm | | GBL |
| | Board Layer Stack Bottom Solder | SM-001 | 0.025mm | 4 | GBS |
| | Board Layer Stack Bottom Overlay | | | | GBO |

Total board thickness: 0.976mm

| | | | |
|------------------------------|---------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------|
| Pluton UPV | Project: Estigia comms payload - LoRa Gateway SoM | Layer: Board Shape Component Guard Dimension |  |
| | | | |
| Engineer: Juan Del Pino Mena | Date: 2024-08-11 | Revision: 0.4 | Size: A4 |
| | | | |

A

B

C

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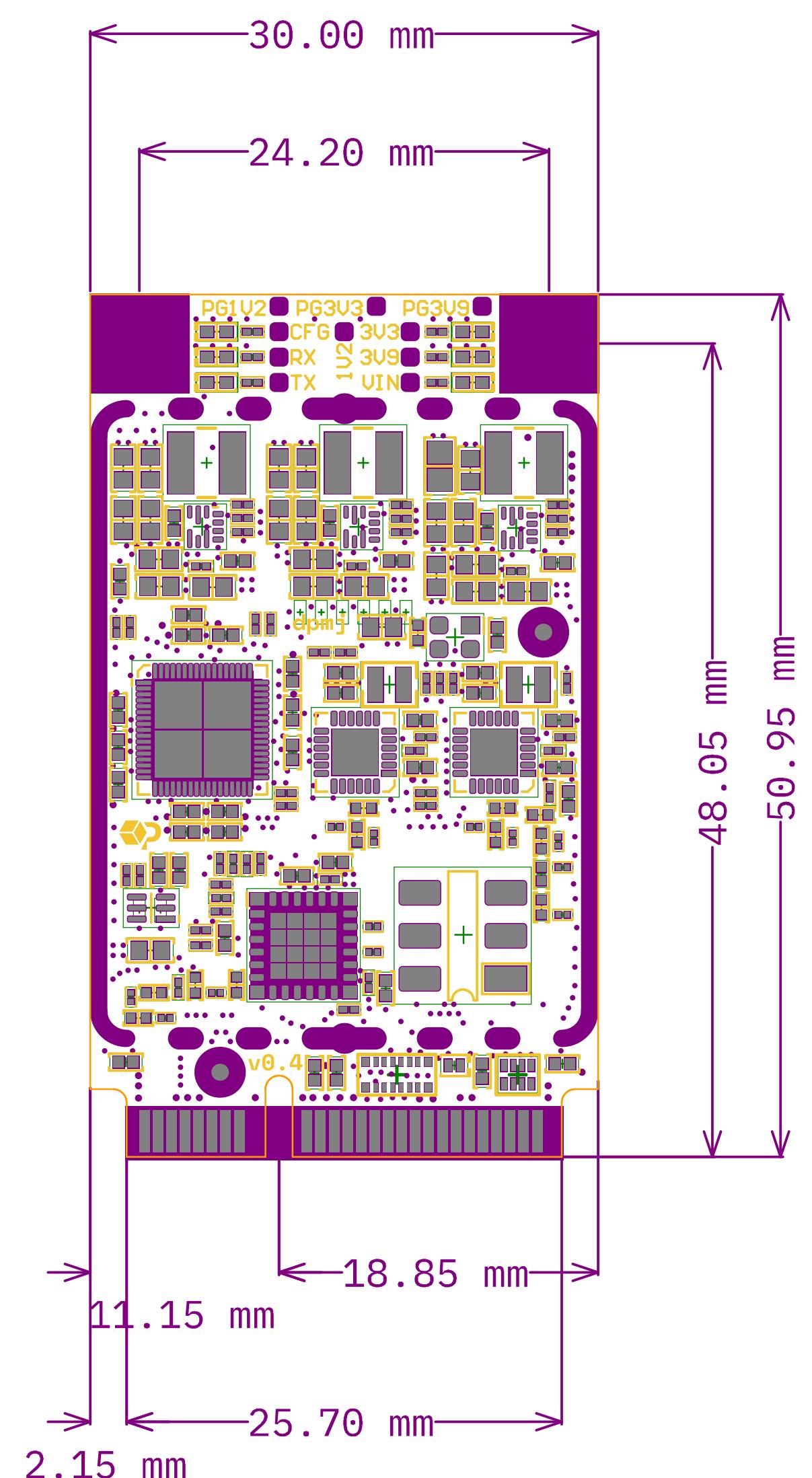
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| Layer | Name | Material | Thickness | Constant | Gerber |
|-------|----------------------------------|----------|-----------|----------|--------|
| | Top Overlay | | | | GTO |
| | Top Solder | SM-001 | 0.025mm | 4 | GTS |
| 1 | Top | CF-003 | 0.018mm | | GTL |
| | PP-top | PP-008 | 0.130mm | 4.1 | |
| 2 | GND-1 | CF-004 | 0.035mm | | GP1 |
| | Core-1 | Core-009 | 0.180mm | 4.5 | |
| 3 | Signal-1 | CF-004 | 0.035mm | | G1 |
| | PP-mid | PP-008 | 0.130mm | 4.1 | |
| 4 | Signal-2 | CF-004 | 0.035mm | | G2 |
| | Core-2 | Core-009 | 0.180mm | 4.5 | |
| 5 | GND-2 | CF-004 | 0.035mm | | GP2 |
| | PP-bottom | PP-008 | 0.130mm | 4.1 | |
| 6 | Bottom | CF-003 | 0.018mm | | GBL |
| | Board Layer Stack Bottom Solder | SM-001 | 0.025mm | 4 | GBS |
| | Board Layer Stack Bottom Overlay | | | | GBO |

Total board thickness: 0.976mm

| | | | |
|---------------------------------------------------|-------------------------------------------------------------------------------------------------------------------|----------|--|
| Pluton UPV | Project: Estigia comms payload - LoRa Gateway SoM Layer: Top Paste+Overlay Component Guard Top Dimension | PLUTON | |
| Project: Estigia comms payload - LoRa Gateway SoM | | | |
| Layer: Top Paste+Overlay | | | |
| Component Guard | | | |
| Top Dimension | | | |
| Engineer: Juan Del Pino Mena | | | |
| Date: 2024-08-11 | Revision: 0.4 | Size: A4 | |

A

B

C

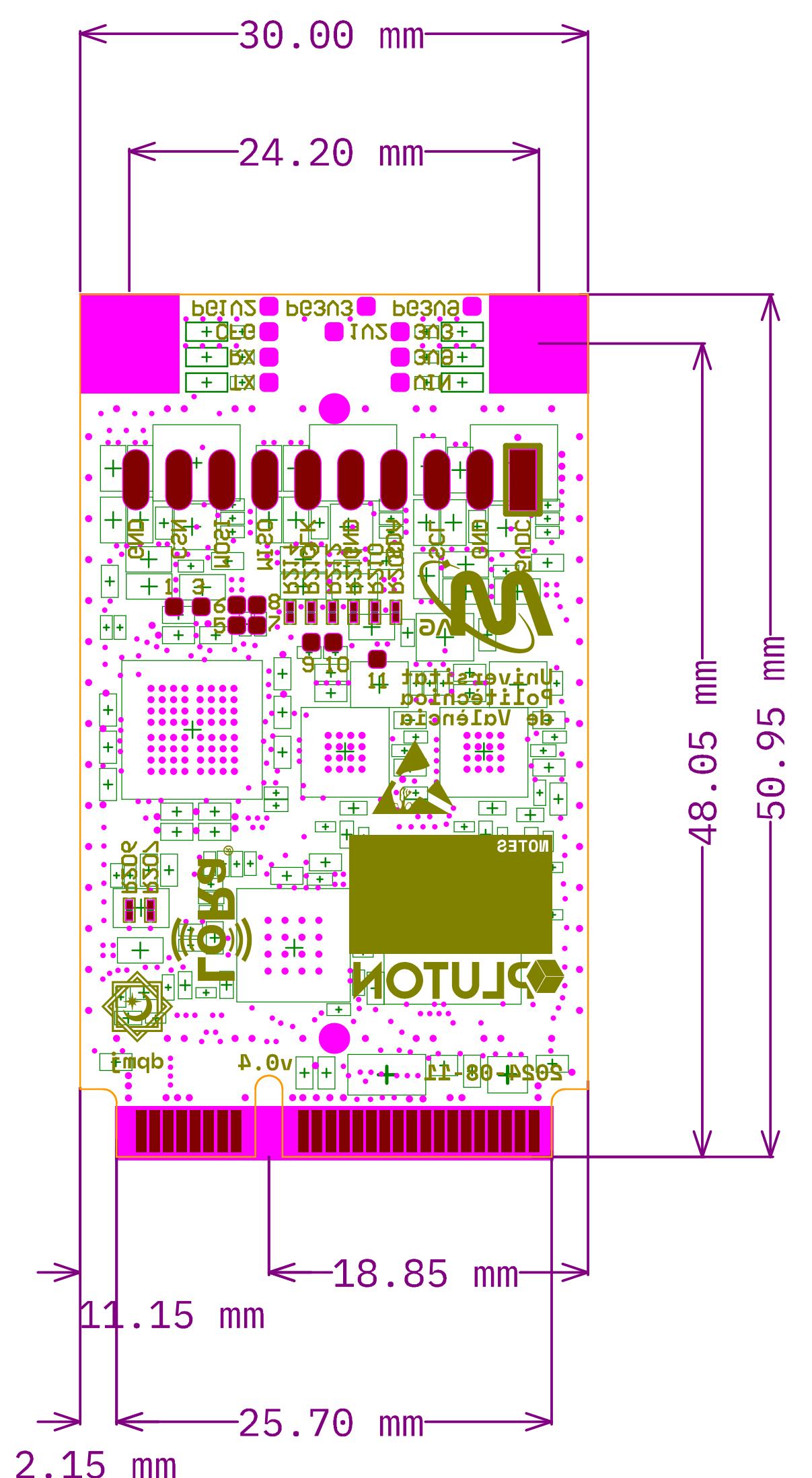
D

A

B

C

D



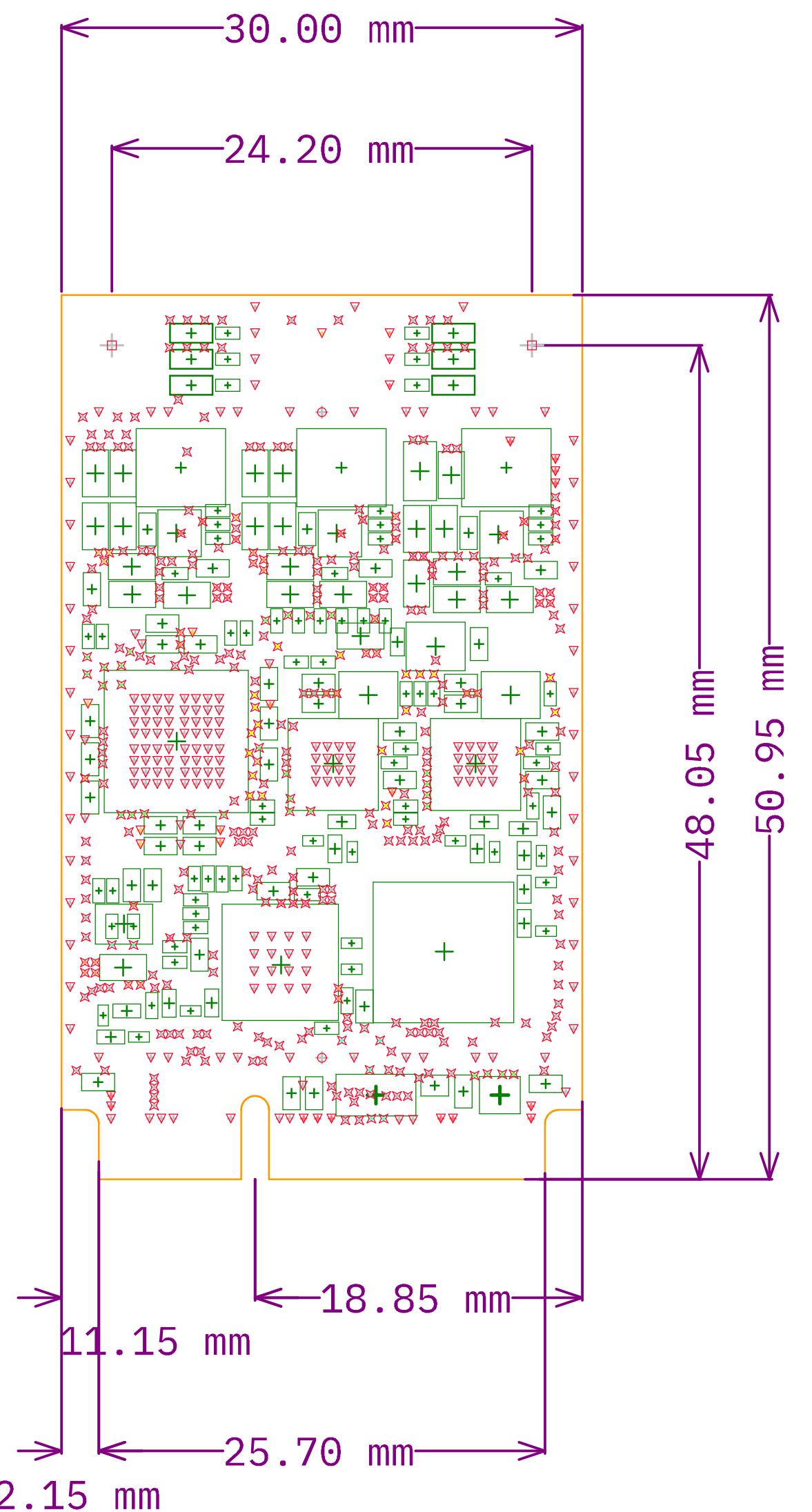
| Layer | Name | Material | Thickness | Constant | Gerber |
|-------|----------------------------------|----------|-----------|----------|--------|
| | Top Overlay | | | | GTO |
| | Top Solder | SM-001 | 0.025mm | 4 | GTS |
| 1 | Top | CF-003 | 0.018mm | | GTL |
| | PP-top | PP-008 | 0.130mm | 4.1 | |
| 2 | GND-1 | CF-004 | 0.035mm | | GP1 |
| | Core-1 | Core-009 | 0.180mm | 4.5 | |
| 3 | Signal-1 | CF-004 | 0.035mm | | G1 |
| | PP-mid | PP-008 | 0.130mm | 4.1 | |
| 4 | Signal-2 | CF-004 | 0.035mm | | G2 |
| | Core-2 | Core-009 | 0.180mm | 4.5 | |
| 5 | GND-2 | CF-004 | 0.035mm | | GP2 |
| | PP-bottom | PP-008 | 0.130mm | 4.1 | |
| 6 | Bottom | CF-003 | 0.018mm | | GBL |
| | Board Layer Stack Bottom Solder | SM-001 | 0.025mm | 4 | GBS |
| | Board Layer Stack Bottom Overlay | | | | GBO |

Total board thickness: 0.976mm

| | | | |
|---------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------|--|
| Pluton UPV | Project: Estigia comms payload - LoRa Gateway SoM Layer: Board Shape Board Components Board Layer Dimensions Board Layer Solder Bottom Overlay | Size: A4 | |
| Project: Estigia comms payload - LoRa Gateway SoM | | | |
| Layer: Board Shape | | | |
| Board Components | | | |
| Board Layer Dimensions | | | |
| Board Layer Solder | | | |
| Bottom Overlay | | | |
| Engineer: Juan Del Pino Mena | Date: 2024-08-11 | Revision: 0.4 | |
| Date: 2024-08-11 | | | |
| Revision: 0.4 | | | |
| Size: A4 | | | |

PLUTON

A



B

| Symbol | Count | Hole Size | Plated | Hole Type | Drill Layer Pair | Via/Pad | Pad Shape | Template |
|--------|-----------|-----------|--------|-----------|------------------|---------|-----------|------------|
| ☒ | 353 | 0.200mm | PTH | Round | Top - Bottom | Via | Rounded | v40h20_lib |
| ▽ | 215 | 0.300mm | PTH | Round | Top - Bottom | (Mixed) | (Mixed) | |
| ○ | 2 | 1.300mm | PTH | Round | Top - Bottom | Pad | Rounded | c170h130p0 |
| □ | 2 | 2.600mm | PTH | Round | Top - Bottom | Pad | Rectangle | s580h260 |
| | 572 Total | | | | | | | |

| Layer | Name | Material | Thickness | Constant | Gerber |
|-------|----------------------------------|----------|-----------|----------|--------|
| | Top Overlay | | | | GTO |
| | Top Solder | SM-001 | 0.025mm | 4 | GTS |
| 1 | Top | CF-003 | 0.018mm | | GTL |
| | PP-top | PP-008 | 0.130mm | 4.1 | |
| 2 | GND-1 | CF-004 | 0.035mm | | GP1 |
| | Core-1 | Core-009 | 0.180mm | 4.5 | |
| 3 | Signal-1 | CF-004 | 0.035mm | | G1 |
| | PP-mid | PP-008 | 0.130mm | 4.1 | |
| 4 | Signal-2 | CF-004 | 0.035mm | | G2 |
| | Core-2 | Core-009 | 0.180mm | 4.5 | |
| 5 | GND-2 | CF-004 | 0.035mm | | GP2 |
| | PP-bottom | PP-008 | 0.130mm | 4.1 | |
| 6 | Bottom | CF-003 | 0.018mm | | GBL |
| | Board Layer Stack Bottom Solder | SM-001 | 0.025mm | 4 | GBS |
| | Board Layer Stack Bottom Overlay | | | | GBO |

Total board thickness: 0.976mm

C

D

| | |
|------------------------------------------------------------------------------------------------------------------------------------------|---------------|
| Pluton UPV | |
| Project: Estigia comms payload - LoRa Gateway SoM | |
| Layer: Board Shape Component Guard | |
| Drill Drawing Drill Guide Dimension | |
| Engineer: Juan Del Pino Mena | |
| Date: 2024-08-11 | Revision: 0.4 |
| Size: A4 | |