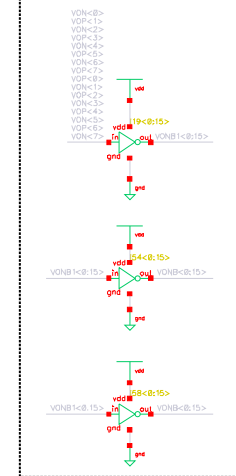
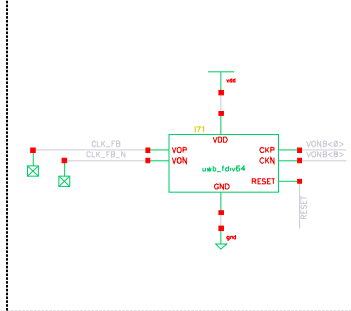


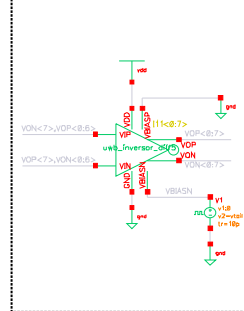
### Oscillator Buffers



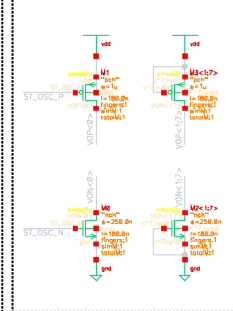
### Freq. Divider 1 (Tx. Trigger)



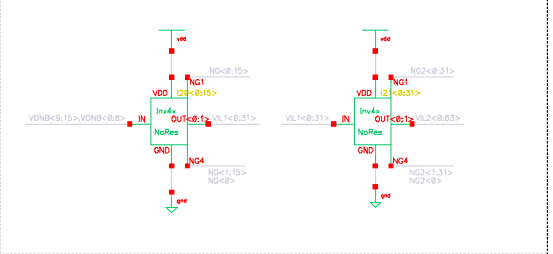
### Ring Oscillator



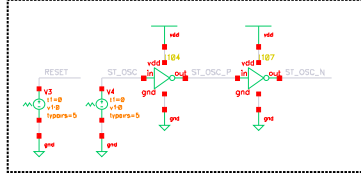
### Start-up + Dummies



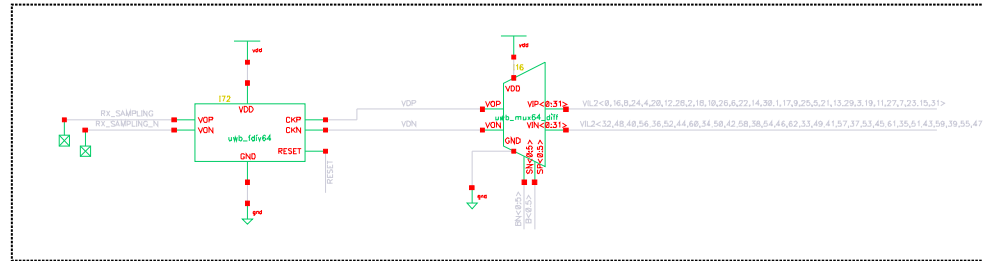
### Interpolator x4



### Startup sequence



### Freq. Divider 2 (Rx. Sampling)



### Counter + Delay adjust + Clock reshape + Buffers

