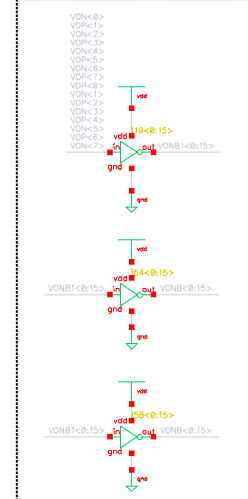
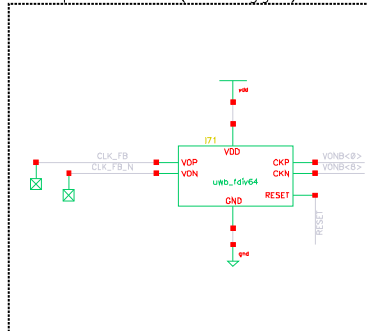


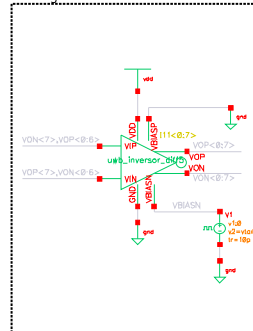
Oscillator Buffers



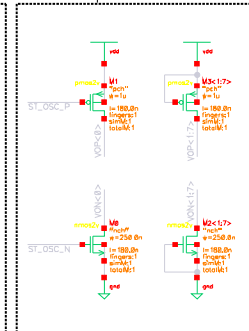
Freq. Divider 1 (Tx. Trigger)



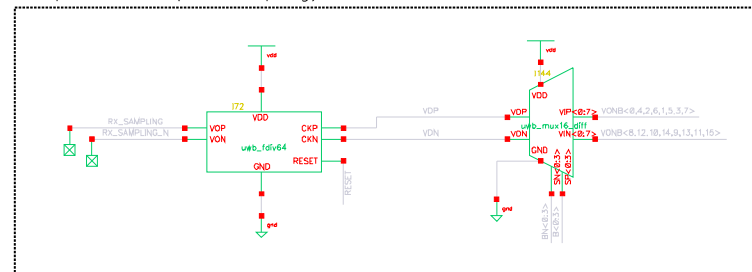
Ring Oscillator



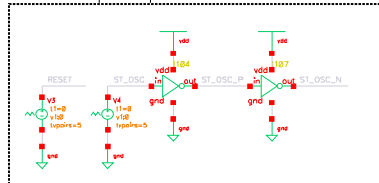
Start-up + Dummies



Freq. Divider 2 (Rx. Sampling)



Startup sequence



Counter + Delay adjust + Clock reshape + Buffers

