

# Computer Fundamentals 1st Year of Bachelor in Computer Science Engineering

# Practice 2 A

Realization of logic functions with SSI devices (II)

Simplification through the Karnaugh method

Simulation in Digital Works

Academic year 2020/2021

Computer Fundamentals Practice 2

### Goals

- Simplification of logic functions.
- Transformation of functions through algebraic manipulation.
- Implementation of functions by means of a single kind of logic gates.
- Introduction to the simulation software Digital Works.
- Initiation in the implementation of functions via logic gates.

### **Available material**

- PC equipped with the software package Digital Works 3.0.5.0.
- 2-input NAND gates (I.C. 7400).
- 2-input NOR gates (I.C. 7402).

# **Specifications**

Given the function:

$$F_1(D, C, B, A) = \sum_4 (0, 1, 6, 8, 14, 15) \# \sum_b (5, 7)$$

## **Operating process**

- 1. Represent the truth table of the function F<sub>1</sub>.
- **2.** Obtain the simplified expression in form of sum of products (SOP) of the function  $F_1$  following the Karnaugh method.
- **3.** Transform the expression of function  $F_1$  got in the exercise 2 in order it can be implemented using exclusively NAND gates.
- 4. Draw in Digital Works the logic diagram of the expression obtained in the exercise 3.
- **5.** Draw in Digital Works the **hardware diagram** of the  $F_1$  expression got in the exercise 4 and check its right operation.
- **6.** Obtain the simplified expression in form of product of sums (POS) of the function F<sub>1</sub> through the Karnaugh method.
- **7.** Transform the expression of function F<sub>1</sub> obtained in the exercise 6 in such a way that it can be implemented by means of only NOR gates.
- **8.** Draw in Digital Works the **logic diagram** of the expression got in the exercise 7.
- **9.** Draw in Digital Works the **hardware diagram** of the F1 expression achieved in the exercise 8 and check its right operation.
- **10.** (Opt) Implement in the laboratory the circuit related to exercise 5. For this exercise you can use the practice board Sidac GPT 783 71.
- **11.** (Opt) Implement in the laboratory the circuit related to exercise 9. For this exercise you can use the practice board Sidac GPT 783 71 or any practice board simulator.

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