

Sample

```
graph TD; Sample[Sample] --> AddVertex1[Add Vertex]; AddVertex1 --> AddVertex2[Add Vertex]; AddVertex2 --> Rewire1[Rewire Vertices]; Rewire1 --> Rewire2[Rewire Vertices]; Rewire2 --> Iteration[Iteration]; Iteration --> Sample;
```

The diagram is a flowchart illustrating a sampling process. It begins with a grey box labeled 'Sample'. A downward arrow leads to a green-bordered box labeled 'Add Vertex'. This is followed by a blue-bordered box labeled 'Add Vertex', then another green-bordered box labeled 'Rewire Vertices', and finally a blue-bordered box labeled 'Rewire Vertices'. A feedback loop is shown on the right, with an arrow labeled 'Iteration' pointing from the bottom back to the 'Sample' box.

Iteration

Add Vertex

Add Vertex

Rewire Vertices

Rewire Vertices