

October 19, 2021

COVER LETTER

To,
HR Manager,
Human Resource Department
Ampere EV

Subject: Application for the post of Sr. Manager, MBS Development

Respected Sir / Maam,

I'm writing to apply for the Sr. Manager, MBS Development at Ampere EV . I have more than 4 years of experience for Sr. Manager, MBS Development , and my professional expertise aligns closely with the responsibilities outlined in your job advertisement.

Ampere EV would benefit from my skills in the following areas:

- * QT/Visual Studio/Visual Code
- * STM32, Microchip, TI BQ Series Component based C/C++ Bare-Metal | Firmware/RTOS/Linux Device Driver Developer using STM CubeIDE, QT, MPLab Harmony/XIDE, Keil, Visual Studio 2019
- * Eagle/Altium/KiCAD PCB Designer | MATLAB Simulations
- * SoC and SoH Low-dynamic profile, High-dynamic profile,
- * DETECTION AND PROTECTION AGAINST BATTERY OVERCHARGING,
- * C/C++ bare metal using VS2019, QT

I believe Ampere EV will be a great success for many years to come, and my extensive expertise will help ensure your establishment succeeds well into the future. My time spent in this industry has prepared me for such an opportunity, and I sincerely hope I can contribute soon as a member of your team.

It would be a privilege to discuss your Sr. Manager, MBS Development position in more detail. I'm happy to come by whenever it's most convenient for you. Thank you for your time, and I'm looking forward to hearing from you.



(Er. Vipinkumar R. Pawar)

VipinKumar Rajendra Pawar

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CAREER OBJECTIVES

To excel among the professionals, where I can utilize my skills with honesty, strong commitments and dedications, for progressive growth of the industry

SKILLS

Technical	QT/Visual Studio/Visual Code
	STM32, Microchip, TI BQ Series Component based C/C++ Bare-Metal Firmware/RTOS/Linux Device Driver Developer using STM CubeIDE, QT, MPLab Harmony/XIDE, Keil, Visual Studio 2019 Eagle/Altium/KiCAD PCB Designer MATLAB Simulations
Documentation	Power Point Presentation, Word, Excel (Expert Level)
	Project Report and Planning Funding Report and Budgeting
Interpersonal / Team	Interpersonal Skill Management
	Stress Management Time Management Problem Solving capabilities

PROFESSIONAL EXPERIENCE

Organization	:Institute of Investigation in Remote Sensing & GIS (IIRSG)
Designation	Sr. Manager, MBS Development
Experience in Years	4
Working Since	10/20/2017
Responsibilities	: * SoC and SoH Low-dynamic profile, High-dynamic profile, Long-term profile

- * Direct current supply voltage,
Overvoltage,
Superimposed alternating voltage,
Slow decrease and increase of supply voltage,
Discontinuities in supply voltage,
Reversed voltage,
Ground reference and supply offset,
Open circuit tests,
Short circuit protection,
Withstand voltage,
Insulation resistance,
- * DETECTION AND PROTECTION AGAINST BATTERY OVERCHARGING,
DETECTION AND PROTECTION AGAINST BATTERY OVER DISCHARGING,
DETECTION AND PROTECTION AGAINST SINGLE CELL OVER AND UNDER VOLTAGE,
DETECTION AND PROTECTION AGAINST OVERCURRENT,
DETECTION OF A SHORT CIRCUIT,
MEASUREMENT OF TEMPERATURES AND PROTECTION AGAINST OVERUSE,
DETECTION OF INSULATION FAULTS,
TEST OF PRE-CHARGE INTERLOCK FUNCTIONS,
FUNCTIONAL BEHAVIOR FOR FULL TEMPERATURE RANGE
- * INITIAL TEMPERATURE CYCLING,
RAPID TEMPERATURE CHANGE,
MECHANICAL SHOCKS,
MECHANICAL VIBRATION
- * C/C++ bare metal using VS2019, QT
- * Microchip Harmony, QT C/C++,
- * BMS core algorithm development - SOC, SOH, Cell balancing, firmware design / implementation, BMS software model based development
- * BMS software model based development , Battery/Cell plant model development, Design Unit tests and debugging workflows for embedded systems.
- * Process throughout V Cycle, Bench testing, MIL, SIL and HIL Testing , Multilayer PCB Design
- * System design expertise, hardware, software integration and verification knowledge.

Organization ANITAS MICROSYSTEMS INDIA

Designation Embedded Design Engineer

Experience 6

Working Since 01 November 2011

Responsibilities

- * Microcontroller PIC, ARM, Arduino Firmware development
- * Multilayer PCB Design (Eagle, National Instruments, KiCAD)
- * Design Unit tests and debugging workflows for embedded systems.
- * CAN / I2C / SPI / UART Communication
- * Product Design and Manufacturing Planning

EDUCATION

Masters in Engineering

Specialization VLSI & Embedded Systems

Year : 2014

University Pune University

CGPA: 7.25

Bachelor of Engineering

Specialization: Electronics & Telecommunication Engineering

Year : 2011

University: Mumbai University

Percentage: 60.19%

PROFESSIONAL MEMBER**MIEEE**, California**MIAENG**, Hong Kong**MATSC**, Washington DC, USA**AP-MICROCHIP**, USA**MIFERP**, Chennai, India**MIRED**, New York, USA**PUBLICATIONS AND ACHIEVEMENTS**

2021	Excellent Researcher Award 2021 by SHEN
2020	Research Excellent Award 2020 by InSc

Total 5 Research Papers Published in International journals**Total 4 International Conference Presentations****DECLARATION**

I hereby certify that all the information furnished above is correct to the best of my knowledge and belief and I promise to abide by all the norms laid down by your esteemed organization.



(Er. Vipinkumar R. Pawar)