

## PERSONAL INFORMATION



# Uglješa Dragišić

Birth Date: 19<sup>th</sup> June 1979,  
 Address: Planinska 4, Sr. Kamenica, 21208, Serbia  
 Telephone: +381 63 102 9 109  
 E-mail: [ugljesa.dragisic@gmail.com](mailto:ugljesa.dragisic@gmail.com)  
 Networks: [LinkedIn](#), [Xing](#), [GitHub](#)

## PROFESSIONAL EXPERIENCE

**Company: Xpansa LTD, Minsk, Belarus**

**Position: Software Developer and Project Manager**

**July 2016 - Present**

- Developing add-on-s, install, setup and deploy (plugin) for ODOO.
- Using Linux, OS X, Python, Java Script, JQuery, HTML, CSS and XML.
- Project management and customer support for project WIREBOXICONSOLUTIONS.

**Company: Simple-Task LTD, Novi Sad, Serbia**

**Position: Software Developer and Teach Lead and Sr. Embedded Software Engineer**

**April 2015 – July 2016**

- Developing add-on-s, install, setup and deploy (plugin) for ODOO.
- Using Linux, OS X, Python, Java Script, JQuery, HTML, CSS and XML.
- Project management and customer support for projects FACC, IWFLOORS and BTLPS ODOO.

**Company: ComData LTD, Novi Sad, Serbia**

**Position: Senior Software Engineer**

**January 2015 – April 2015**

- Optimizing C++ MFC code for loading tables with ODBC 5.0 using Visual Studio 2013 and MSSQL 2012.
- I also provide examples for loading table with .NET C++ and C# Entity Framework ADO.NET.
- Working on site in Heilbronn office for 1 week.

**Company: RT-RK Computer Based Systems, Novi Sad, Serbia**

**Position: Software Engineer**

**June 2014 – January 2015**

- Binding platform test, SIL on VS2013 and remote access for zFAS platform for TTTech Automotive.
- Building using Bullfrog Python framework images for MICROSAR and VxWorks,
- Setting Vector HIL and running platform tests on Vector CANoe for zFAS and SIL platform tests.
- Leading one software engineer for two months.
- Training and working on site in Vienna office of TTTech for 1 month.

**October 2012 – June 2014**

- Participate in project of developing HF direction finder for VTI
- Developing Spectrum segmentation for signal processing platform using real-time-space analysis.
- Developing and implementation data mining algorithm for spectrum segmentation to detect emissions.
- Coding using Visual Studio C++, creating DB using MySQL over ODBC connector.
- Streaming signals, creating multithreading and buffering structure for signal processing on Intel I7 PC.
- Developing mathematical model and implementing space-time analysis for objects in application EOB.
- Working on site about 2 months, preparing for certification tests in field vehicle.

**September 2012 – October 2012**

- MHEG STB targeted for South African market based on ST/SmartDTV platform and DVBT2 support.
- Coding using C++ and GCC compiler and cross compiler, fixing make and build scripts.

**January 2011 – September 2012**

- Implementing remote network projector for Vestel TV.
- Build code with make scripts and debug embedded C on Embedded Linux.
- Implementing MPEG2, MPEG1 and H.264 in DTV middleware and streaming RTP from library live555.
- Using VLC script to capture PC desktop images and transcode it over RTP.

**May 2011 – January 2012**

- Tracking developers and providing delivering packages for Zoran TV.
- Fixing C code, fixing build scripts and builds images for Embedded Linux and RT Linux.
- Using Perforce to track software versions, branch and merge developing branches.

**November 2009 – May 2011**

- Supporting Telvent DMS (from 2012 Schneider Electronics DMS) Smart Grid ADMS V3 development.
- Developing calculation engine as multithread and multicore services with Visual Studio C# and C++.
- Using Bugzilla and Issue tracking to track task issues and using HG to track versions via GIT.
- Implementing Shared Memory in CLI, services communication WCF and GUI for services test in WPF.
- Developing statistical testing tool for Telvent DMS Smart Grid, architecture, frontend and data system.

- Working on site in Novi Sad Telvent DMS office as support for 19 months.

#### **March 2008 – November 2009**

- Supporting graphical composing tool for Cirrus DSP, added Cambridge 3D sound room and optimize 12 outputs graphical sound filter.
- Using Bugzilla to track support and CVS for versioning support.
- Documenting code, implementation concept, tool use manual instructions writing.
- Leading two internship software engineers for six months.

#### **January 2008 – March 2008**

- Supporting Cirrus DSP assemblers debugger Hydra.
- Fixing bugs, implementing features, using CVS and Bugzilla, supporting stubs on remote debugger.
- Maintain GUI created in Visual Studio MFC and verify WHDL based DSP simulator.
- Porting Eclipse JDT IDE for assembler debugger, connecting remoter debugger libs via JNI.
- Coding extensions for Cirrus DSP Assembler perspective in Java and presenting prototype.

#### **June 2007 – January 2008**

- Part of team that developing embedded C compiler for Cirrus DSP Assembler.
- Implementing memory zones handling in Visual Studio C++ backend and qualifiers in EDG C frontend.
- Coding Deja GNU tests for compiler regression testing, coding in embedded C and assembler DSP app. to verify compiler code density and MIPS density.

### **Company: RT-RK Computer Based Systems, Novi Sad, Serbia**

#### **Position: Internship Software Engineer**

#### **July 2006 – June 2007**

- Implementation conditional assignment optimization in Microsoft APX embedded C compiler.
- Coding in C++ on ACE-s CoSy IDE framework on Sun UNIX and debugging with GDB.
- Porting GDB debugger for embedded C compiler and connecting to simulation target.
- Coding in C++, Compiling in GCC for Sun UNIX and debugging with GDB.

#### **May 2006 – July 2006**

- Recoding C implementation of cosines transformation in to Micronas DSP MAS.
- Coding cosines transformation in MAS Assembler, comparing outputs of MAS like C implementation and MAS assembler implementation, measuring MIPS and used memory.

## **EDUCATION**

Master of Science (M.Sc.) Electrical and Computer Engineering – Department for Computing Science and Control Engineering (2006), Faculty of Technological Science, University of Novi Sad, Serbia

## **PERSONAL SKILLS**

#### **Personal Skills:**

- Well organized, efficient and professional person
- Ambitious, confident, reliable and flexible individual

#### **Language Skills:**

- Serbian (Native language)
- English (Professional working proficiency)
- German (Elementary proficiency)

## **PROFESSIONAL SKILLS**

**Programming Languages:** C, C++, C#, Java, Bash, Make, Python, Google Spreadsheet, HTML, JQuery, Java Script, CSS and SQL.

**Libraries and Concepts:** OOP, .NET, XML, JNI, EDG, Live555, BullFrog, Docker, ORM and OpenERP.

**Applications:** SVN, CVS, GIT, Perforce, Microsoft Visual Studio (V6, 2003, 2005, 2008, 2010 and 2013), Microsoft Office, GDB, ODBC, Bugzilla, Issue Tracker, vi editor and Basecamp.

**Platforms:** Microsoft Windows: 98/2000/XP/VISTA/7/8, Linux; UNIX, RTLinux, VxWorx, MICROSAR and MAC OS X.

## **PUBLICATIONS**

1. One solution of real-time spectrum segmentation in HF direction finder based on time-frequency-space analysis - Uglješa Dragišić, Goran Krunic, Nikola Teslić – 6th International Scientific Conference on Defensive Technologies OTEH 2014
2. An approach for development an EOB application in direction finder for HF frequency range - Goran Krunic, Uglješa Dragišić, Željko Jurca, Tomislav Maruna – 6th International Scientific Conference on Defensive Technologies OTEH 2014