

Understanding PCB Layer Stack-Up for High-Performance Electronics

Why stack-up?

The irreversible development of modern electronics has been increasingly pushing PCBs towards such demands as miniaturization, light weight, high speed, better functionality and reliability, and longer lifetime, which results in the popularity of multilayer PCBs. Combined by a type of semi-solid adhesive which is called "prepreg", two or more single and/or double-sided PCBs are stacked together to generate multilayer PCBs through reliable predefined mutual connection between them. There are three or more conductive layers in one multilayer PCB with two layers outside and one layer synthesized in the insulation board. With the increase of PCB complexities and densities, it's possible for some issues to take place such as noise, stray capacitance and cross talk when layer arrangement gets inefficient design.

Planning optimal multilayer stack-up is one of the most important elements in determining the Electromagnetic Compatibility (EMC) performance of a product. A well-designed layer stack-up can both minimize the radiation and can stop circuit from being interfered by external noise sources. Well-stacked PCB substrates can also reduce signal cross talk and impedance mismatch issues. However, an inferior stack-up may get EMI (Electromagnetic Interference) radiation rising, because reflections and ringing in the system as a result of impedance mismatch can dramatically lower products' performance and reliability. This article then focuses on layer stack up definition, its designing rules and essential considerations.

What is Stack-up?

Stack-up refers to the arrangement of copper layers and insulating layers that make up a PCB prior to board layout design. While a layer stack-up allows you to get more circuitry on a single board through the various PCB board layers, the structure of PCB stackup design confers many other advantages:

- A PCB layer stack can help you minimize your circuit's vulnerability to external noise as well as minimize radiation and reduce impedance and crosstalk concerns on high-speed PCB layouts.
- A good layer PCB stack-up can also help you balance your need for low-cost, efficient manufacturing methods with concerns about signal integrity issues
- The right PCB layer stack can enhance the Electromagnetic Compatibility of your design as well.

It will very often be to your benefit to pursue a stacked PCB configuration for your printed circuit board-based applications.

For multilayer PCBs, general layers include ground plane (GND plane), power plane (PWR plane), and inner signal layers. Here's a sample of an 8-layer PCB stackup.

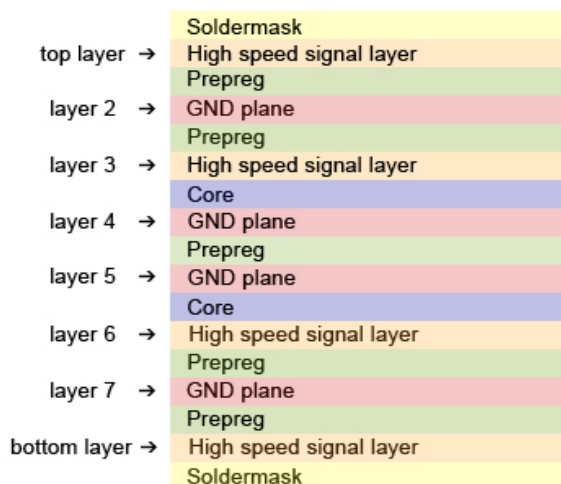


Figure 1 Layer Stack-up Example of An 8-layer PCB

PCB Layer Stackup

In accordance with this figure, it's obviously clear to indicate layer distribution in PCBs conforms to a symmetrical or balanced structure. Apart from layer distribution, the spacing between layers should be taken seriously as well. To meet the requirement of miniaturization, minimum trace spacing must be obtained while planning layer stack-up. The space between layers can be either core or prepreg. Multilayer boards usually consist of at least one or more cores and prepregs. Cores are made up of a copper-plated glass-reinforced epoxy laminate sheets. The thickness of core is in the range from 0.1mm to 0.3mm.

Prepreg is the common term for a reinforcing fabric which has been pre-impregnated with a resin system. This resin system (typically epoxy) already includes the proper curing agent. The main function of prepreg is to stack all layers into a whole board by high temperature. The following table shows physical and chemical attributes of main categories of Prepreg, that is, 7628, 2116 and 1080.

Type	Resin Content (%)	Resin Flow (%)	Gelation Time (s)	Thickness (mm)	Thickness after Solidification (mm)
7628	48.0±3.0	30.0±5.0	100±20	0.18	0.23±0.02
2116	53.0±3.0	31.0±5.0	105±20	0.10	0.13±0.03
1080	64.0±3.0	41.0±5.0	105±20	0.06	0.09±0.01

Actually, thickness of each type of prepreg isn't always stable and adjustments will be made to meet specific PCB thickness demand. Some factors have to be taken into consideration when determining the count of prepreg including thickness of inner layer, product design thickness requirement or manufacturing technology requirement, features of prepreg, practical performance and the actual thickness after stack-up trial. Below image indicates a sample 4-layer PCB whose layer distribution and thickness are depicted.



Figure 2 Example of a 4-layer PCB Thickness

In this example, as copper thickness is stable, PCB maker will adjust thickness of prepreg and core to meet thickness need. There're 2 methods to be compatible with the adjustment mentioned above.

- **Interlayer offset.** Resin recession groove is used in board side design instead of choked flow pad. In regards with stack-up positioning, hot melting plus rivet plus dowel method can be used to solve stack-up offset problem.
- **Stack-up measling.** In the process of board arrangement, silicon pads can be added, together with the assistance of epoxy plate so that pressure will remain balanced, which can both help eliminating stack-up measles and effectively control the uniformity of board thickness.

Based on chemical and physical attributes of prepreg, you have access to estimate thickness or copper weight of final circuit board through an easy calculation.

To obtain specific thickness of prepreg, different types of prepregshave to be combined together to achieve required thickness. For example, the thickness of 0.14mm derives from combination of two sheets of 1080 prepregs while that of 0.19mm from combination of 2116 and 1080 prepregs.

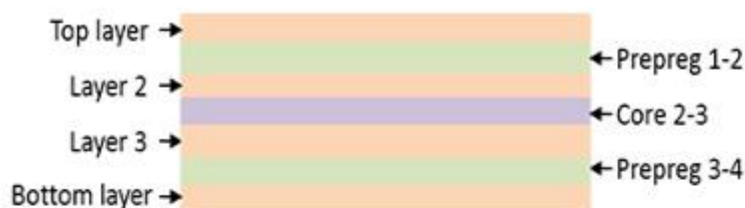
The standard PCB Layer Stack-up

We provide multilayer circuit boards with layers in the range from 4 to 32 layers, board thickness from 0.4mm to 3.2mm, copper thickness from 18μm to 210μm (0.5oz to 6oz), inner layer copper thickness from 18μm to 70μm (0.5oz to 2oz), and minimal spacing between layers to 3mil.

Following images present our commonly used stack-up for Standard PCB service. The actual layer stack-up will be determined by PCB material and many other elements. If you have special requirements on PCB layer stackup, please send your circuit board thickness and layer count by email. We'll share corresponding stack-up accordingly.

Note: Our PCB Prototype Service offers limited possibilities on custom layer stackup. If your application asks for specific layer stack-up, we recommend Standard PCB Service.

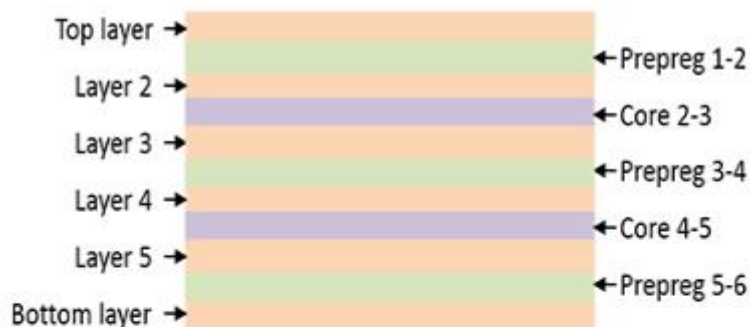
A commonly-used Layer Stackup for 4-layer PCBs



Stackup Thickness of 4-Layer PCB (mm)

Finished Thickness	Tolerance	Prepreg1-2	Core2-3	Prepreg3-4
0.4	±0.1	0.08	0.10	0.08
0.6	±0.1	0.14	0.13	0.14
0.8	±0.1	0.14	0.33	0.14
1.0	±0.1	0.24	0.33	0.24
1.2	±10%	0.24	0.53	0.24
1.6	±10%	0.31	0.73	0.31
2.0	±10%	0.24	1.33	0.24
2.5	±10%	0.24	1.83	0.24
3.0	±10%	0.31	2.13	0.31

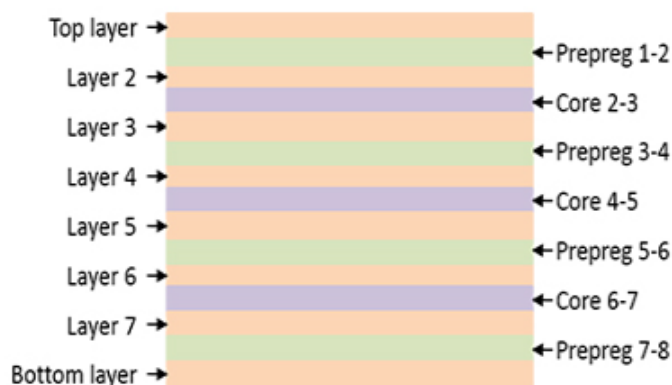
A commonly-used Layer Stackup for 6-layer PCBs



Stackup Thickness of 6-Layer PCB (unit: mm)

Finished Thickness	Tolerance	Prepreg 1-2	Core 2-3	Prepreg 3-4	Core 4-5	Prepreg 5-6
0.6	±0.1	0.08	0.065	0.14	0.065	0.08
0.8	±0.1	0.08	0.13	0.14	0.13	0.08
1.0	±0.1	0.14	0.13	0.24	0.13	0.14
1.2	±10%	0.24	0.13	0.24	0.13	0.24
1.6	±10%	0.24	0.33	0.14	0.33	0.24
2.0	±10%	0.24	0.53	0.24	0.53	0.24
2.5	±10%	0.24	0.73	0.24	0.73	0.24
3.0	±10%	0.31	0.93	0.24	0.93	0.31

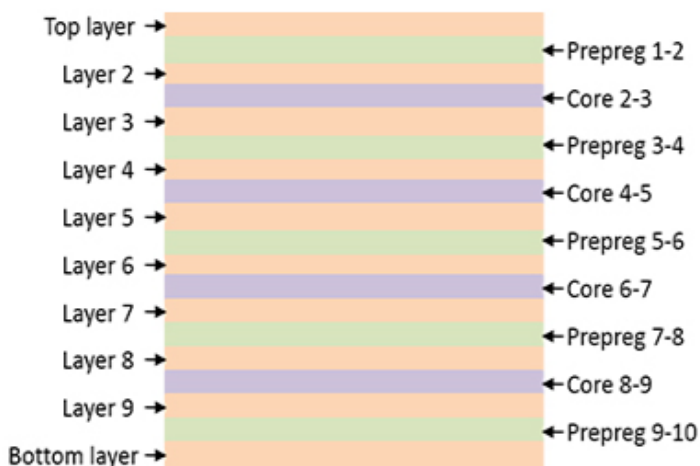
A commonly-used Layer Stackup for 8-layer PCBs



Stackup Thickness of 8-Layer PCB (unit: mm)

Finished Thickness	Tolerance	Prepreg 1-2	Core 2-3	Prepreg 3-4	Core 4-5	Prepreg 5-6	Core 6-7	Prepreg 7-8
1.0	±0.1	0.08	0.10	0.14	0.10	0.14	0.10	0.08
1.2	±10%	0.08	0.13	0.14	0.13	0.14	0.13	0.08
1.6	±10%	0.19	0.13	0.24	0.13	0.24	0.13	0.19
2.0	±10%	0.19	0.33	0.14	0.33	0.14	0.33	0.19
2.5	±10%	0.14	0.53	0.14	0.53	0.14	0.53	0.14
3.0	±10%	0.24	0.53	0.31	0.53	0.31	0.53	0.24

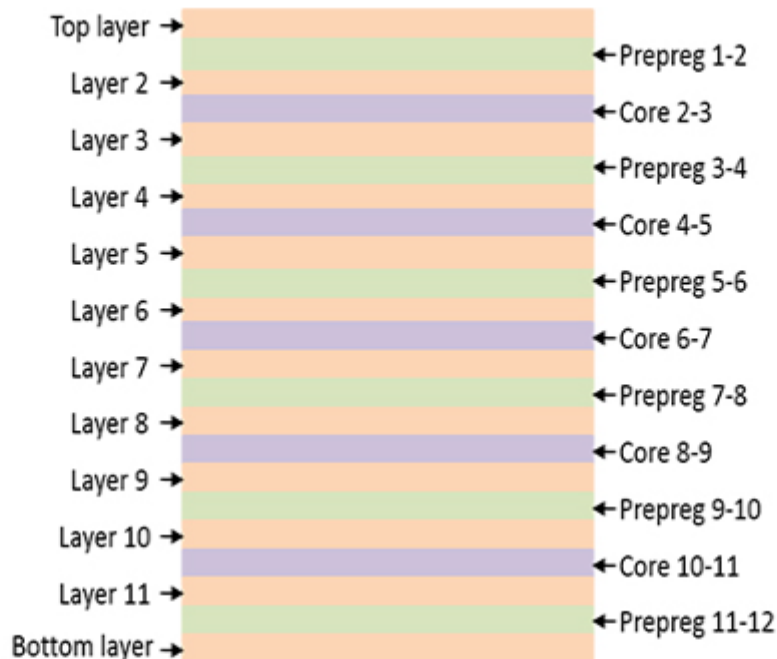
A commonly-used Layer Stackup for 10-layer PCBs



Stackup Thickness of 10-Layer PCB (unit: mm)

Finished Thickness	Tolerance	Prepreg 1-2	Core 2-3	Prepreg 3-4	Core 4-5	Prepreg 5-6	Core 6-7	Prepreg 7-8	Core 8-9	Prepreg 9-10
1.2	±10%	0.08	0.10	0.14	0.10	0.14	0.10	0.14	0.10	0.08
1.6	±10%	0.14	0.13	0.14	0.13	0.14	0.13	0.14	0.13	0.14
2.0	±10%	0.19	0.13	0.24	0.13	0.24	0.13	0.24	0.13	0.19
2.5	±10%	0.14	0.33	0.14	0.33	0.24	0.33	0.14	0.33	0.14
3.0	±10%	0.26	0.33	0.24	0.33	0.24	0.33	0.24	0.33	0.26

A commonly-used Layer Stackup for 12-layer PCBs



Stackup Thickness of 12-Layer PCB (unit: mm)

Finished Thickness	Tolerance	Prepreg 1-2	Core 2-3	Prepreg 3-4	Core 4-5	Prepreg 5-6	Core 6-7	Prepreg 7-8	Core 8-9	Prepreg 9-10	Core 10-11	Prepreg 11-12
1.6	±10%	0.19	0.065	0.14	0.065	0.14	0.065	0.14	0.065	0.14	0.065	0.19
2.0	±10%	0.14	0.10	0.14	0.10	0.24	0.10	0.24	0.10	0.14	0.10	0.14
2.5	±10%	0.19	0.13	0.24	0.13	0.24	0.13	0.24	0.13	0.24	0.13	0.19
3.0	±10%	0.14	0.33	0.14	0.33	0.14	0.33	0.14	0.33	0.14	0.33	0.14

Choose Torttai for Your PCB Layer Stack-Up Needs

If you like the idea of PCB stack-up for your electronic applications, the BEST place you need to go for layer stack up is Torttai. We offer full-service turnkey solution for all things concerning PCBs. We can help you with:

- Develop your layer stackup in PCB design
- Fabricate your stack PCBs
- Create prototypes to test your designs for any possible flaws
- Do full PCB runs