UM1670
User manual

Discovery kit with STM32F429ZI MCU

Introduction

The STM32F429 Discovery kit (32F429IDISCOVERY) allows users to easily develop applications with the STM32F429 high-performance STM32 with ARM® Cortex®-M4 core. It includes an ST-LINK/V2 or ST-LINK/V2-B embedded debug tool, a 2.4" QVGA TFT LCD, an external 64-Mbit SDRAM, an ST MEMS gyroscope, a USB OTG micro-AB connector, LEDs and push-buttons.

The board comes with a comprehensive STM32 software HAL library with various packaged software examples, as well as a direct access to ARM® mbed™ on-line resources at http://mbed.org.

Figure 1. STM32F429 Discovery board

1. Picture not contractual

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1 Features

The STM32F429 Discovery board offers the following features:

- STM32F429ZIT6 microcontroller featuring 2 Mbytes of Flash memory, 256 Kbytes of RAM in an LQFP144 package
- On-board ST-LINK/V2 on 32F429IDISCOVERY or ST-LINK/V2-B on STM32F429I-DISC1
- ARM® mbed™ -enabled (http://mbed.org) with ST-LINK/V2-B only
- USB ST-LINK with re-enumeration capability and three different interfaces:
  - Virtual com port (with ST-LINK/V2-B only)
  - Mass storage (with ST-LINK/V2-B only)
  - Debug port
- Board power supply: through the USB bus or from an external 3 V or 5 V supply voltage
- 2.4" QVGA TFT LCD
- 64-Mbit SDRAM
- L3GD20, ST MEMS motion sensor 3-axis digital output gyroscope
- Six LEDs:
  - LD1 (red/green) for USB communication
  - LD2 (red) for 3.3 V power-on
  - Two user LEDs: LD3 (green), LD4 (red)
  - Two USB OTG LEDs: LD5 (green) VBUS and LD6 (red) OC (overcurrent)
- Two push-buttons (user and reset)
- USB OTG with micro-AB connector
- Extension header for LQFP144 I/Os for a quick connection to the prototyping board and an easy probing
- Comprehensive free software including a variety of examples, part of STM32CubeF4 package or STSW-STM32138 for legacy standard libraries usage

2 Product marking

Evaluation tools marked as "ES" or "E" are not yet qualified and therefore they are not ready to be used as reference design or in production. Any consequences deriving from such usage will not be at ST charge. In no event, ST will be liable for any customer usage of these engineering sample tools as reference design or in production.

"E" or "ES" marking examples of location:

- On the targeted STM32 that is soldered on the board (for illustration of STM32 marking, refer to the section “Package information” of the STM32 datasheet available at *www.st.com*).
- Next to the evaluation tool ordering part number, that is stuck or silk-screen printed on the board.

3 Ordering information

To order the Discovery kit for the STM32F429 line of microcontrollers, refer to *Table 1*.

Table 1. List of the order codes

Order code	ST-LINK version
32F429IDISCOVERY	ST-LINK/V2
STM32F429I-DISC1	ST-LINK/V2-B (mbed-enabled)

4 Conventions

Table 2 provides the definition of some conventions used in the present document.

Table 2. ON/OFF conventions

Convention	Definition
Jumper JPx ON	Jumper fitted
Jumper JPx OFF	Jumper not fitted
Solder bridge SBx ON	SBx connections closed by solder
Solder bridge SBx OFF	SBx connections left open

5 Quick start

The STM32F429 Discovery is a low-cost and easy-to-use development kit to quickly evaluate and start a development with an STM32F4 microcontroller.

Before installing and using the product, accept the Evaluation Product License Agreement from www.st.com/stm32f4-discovery.

For more information on the STM32F429 Discovery board and for demonstration software, visit www.st.com/stm32f4-discovery.

5.1 Getting started

Follow the sequence below to configure the STM32F429 Discovery board and launch the DISCOVER application:

1. Ensure that the jumpers JP3 and CN4 are set to "ON" (Discovery mode).
2. Connect the STM32F429 Discovery board to a PC using a USB cable type A/mini-B through the USB ST-LINK connector CN1, to power the board, the LEDs LD2 (PWR) and LD1 (COM).
3. The following applications are available on the screen:
   - Clock/Calendar and Game
   - Video Player and Image Browser (play videos and view images from the USB mass storage connected to CN6)
   - Performance monitor (watch the CPU load and run a graphical benchmark)
   - System Info
4. The demo software, as well as other software examples are available at the www.st.com/stm32f4-discovery webpage.
5. Develop applications starting from the examples.

5.2 System requirements

- Windows® OS (XP, 7,8)
- USB type A to mini-B cable

5.3 Development toolchains supporting the STM32F429 Discovery kit

- IAR® EWARM (IAR Embedded Workbench®)
- Keil® MDK-ARM™
- GCC-based IDEs (free AC6: SW4STM32, Atollic® TrueSTUDIO®,...)
- ARM® mbed™ online

6 Hardware layout

The STM32F429 Discovery board has been designed around the STM32F429ZIT6 microcontroller in a 144-pin LQFP package.

Figure 2 illustrates the connections between the STM32F429ZIT6 and its peripherals (ST-LINK/V2 or ST-LINK/V2-B, push-buttons, LED, USB OTG, ST MEMS Gyroscope, Accelerometer, Magnetometer and connectors).

Figure 3 and *Figure 4* help to locate these connections on the STM32F429 Discovery board.

Figure 2. Hardware block diagram

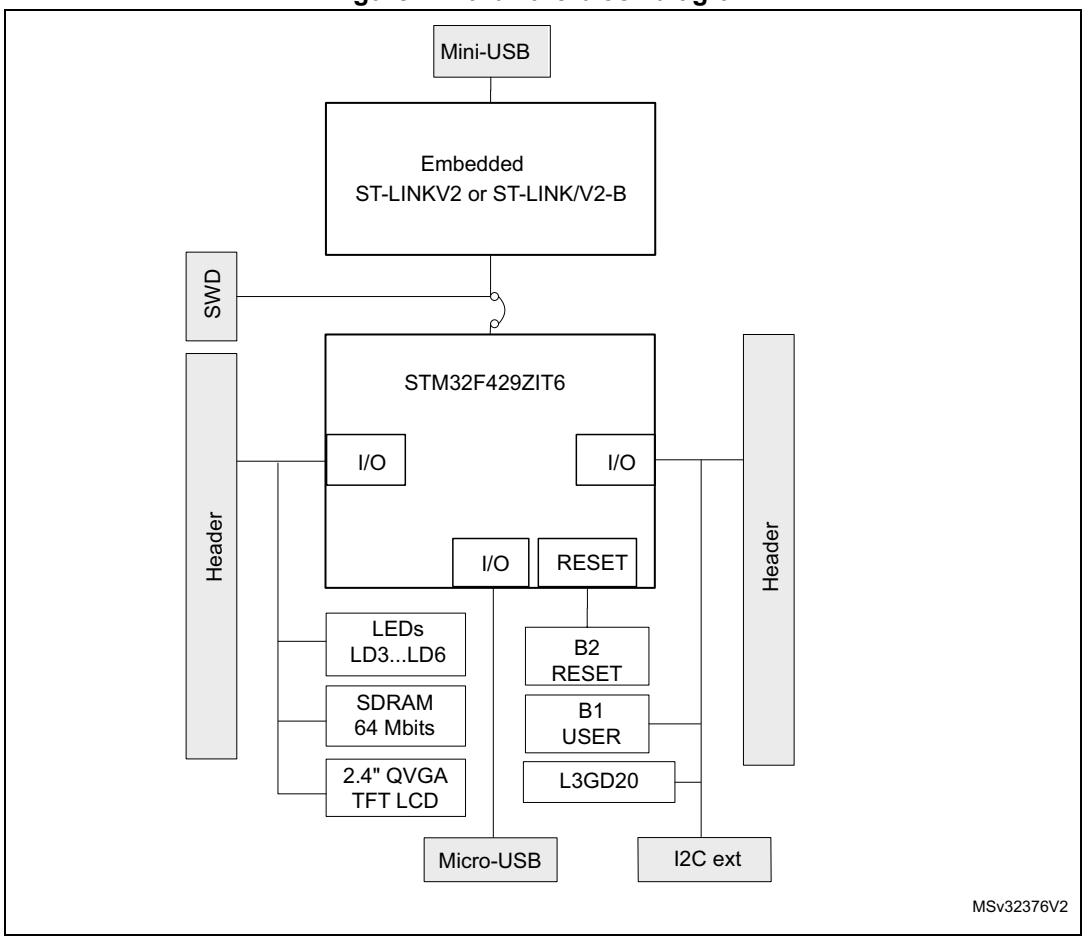


Figure 3. Top layout

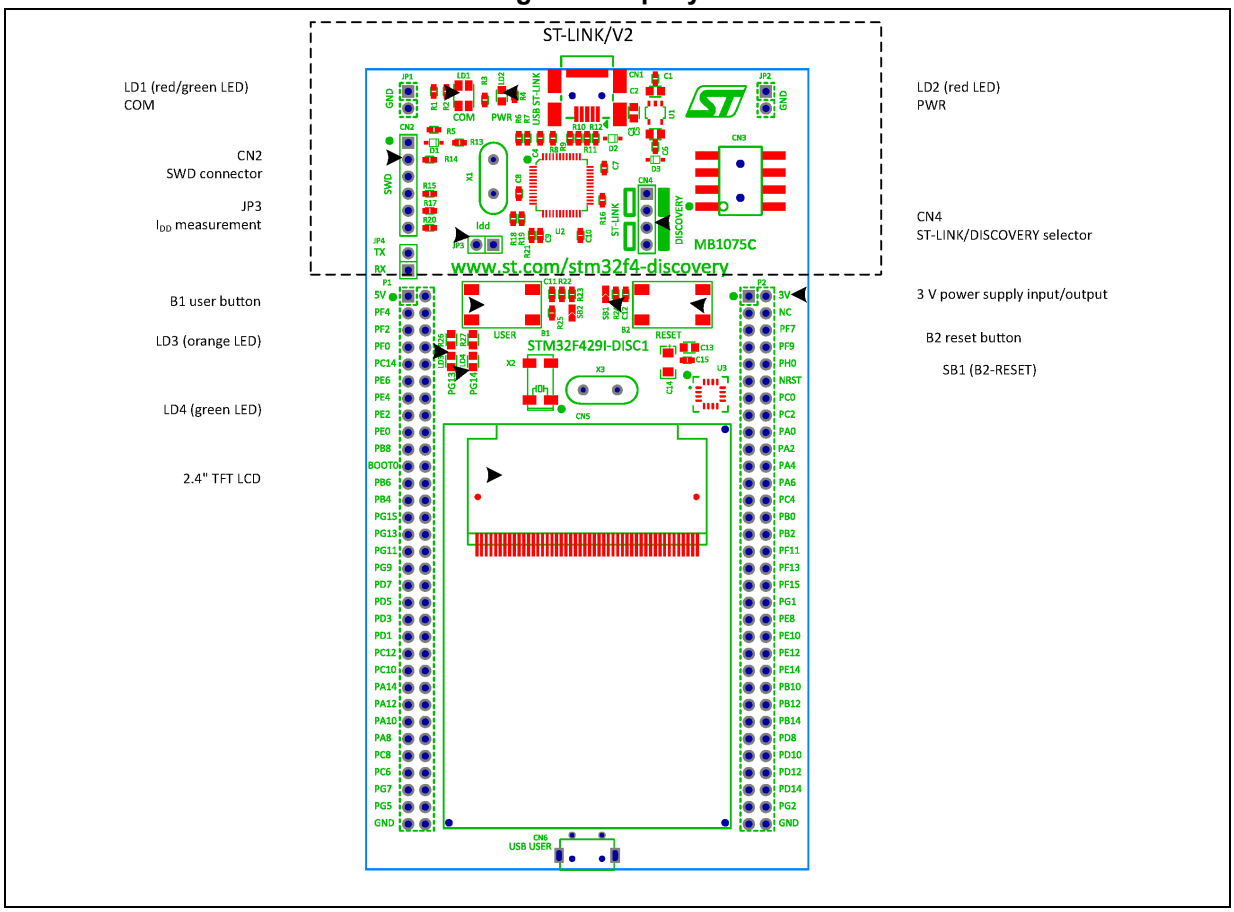
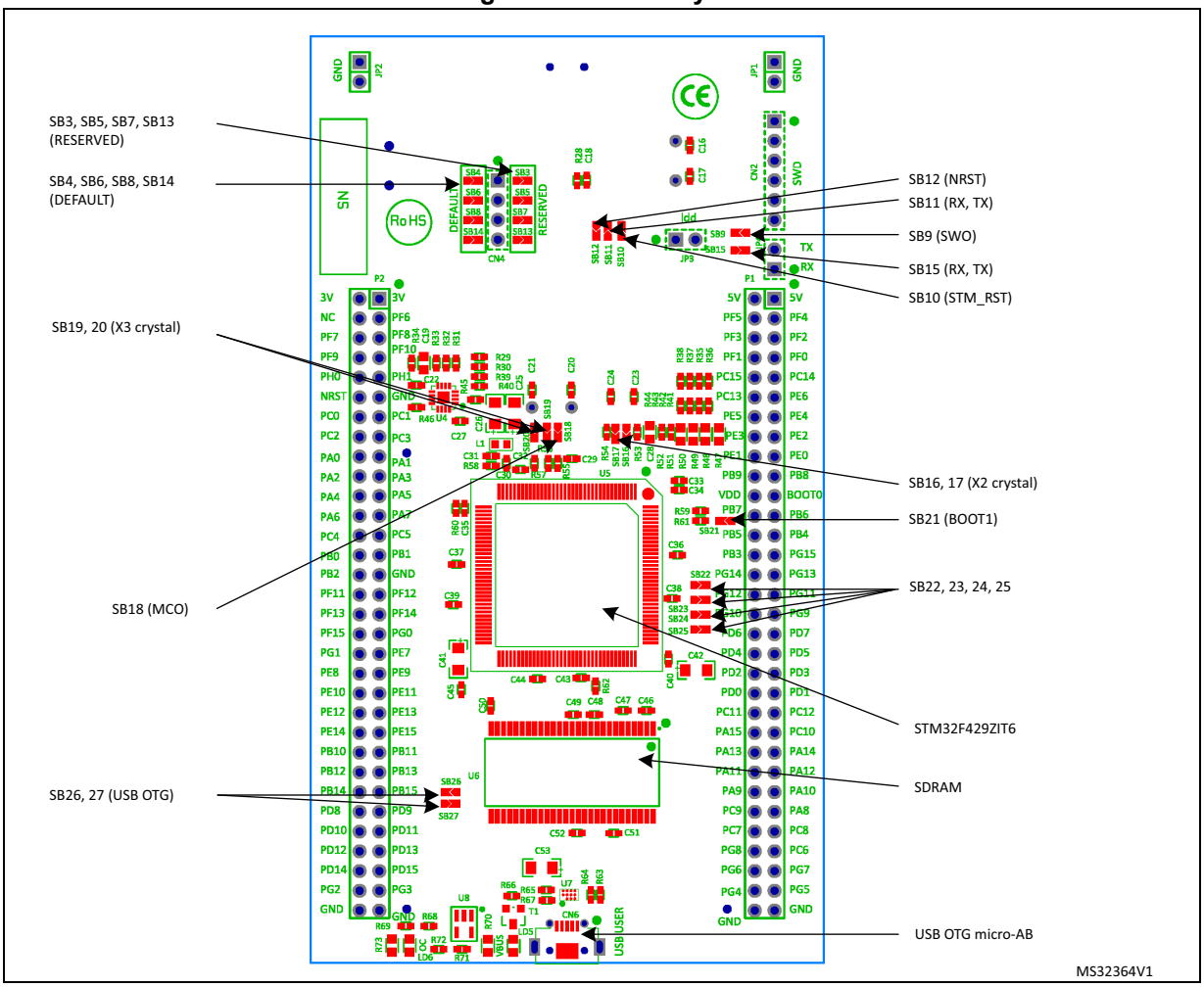


Figure 4. Bottom layout



6.1 Embedded ST-LINK/V2 (or V2-B)

The ST-LINK/V2 on 32F429IDISCOVERY or the ST-LINK/V2-B on STM32F429I-DISC1 is embedded as programming and debugging tool. Virtual COM port and USB mass storage features are supported by the ST-LINK/V2-B only for mbed compatibility.

The ST-LINK/V2-B makes the STM32F4429I-DISC1 boards mbed enabled. The embedded ST-LINK/V2 (or V2-B) supports only SWD for STM32 devices. For information about debugging and programming features refer to *ST-LINK/V2 in-circuit debugger/programmer for STM8 and STM32*, User manual (UM1075), which describes in detail all the ST-LINK/V2 features.

The changes on ST-LINK/V2-B versus ST-LINK/V2 version are listed below.

New features supported on ST-LINK/V2-B are:

- Virtual Com Port interface on USB
- Mass storage interface on USB

Features not supported on ST-LINK/V2-B are:

- SWIM interface
- Minimum supported application voltage limited to 3 V
- USB power management request for more than 100 mA power on USB

Known limitation:

- Activating the readout protection on the ST-LINK/V2-B target, prevents the target application from running afterwards. The target readout protection must be kept disabled on the ST-LINK/V2-B boards.

There are two different ways to use the embedded ST-LINK/V2-B, depending on the jumper states (see *Table 3*):

- Programming/debugging the STM32 on board (refer to *Section 6.1.4: Using ST-LINK/V2 (or V2-B) to program/debug the STM32F429ZIT6 on board* to program/debug the STM32F429ZIT6 on board)
- Programming/debugging the STM32 in an external application board, using a cable connected to SWD connector CN2 (refer to chapter: *Section 6.1.5: Using ST-LINK/V2 (or V2-B) to program/debug an external STM32 application*)

Table 3. Jumper states

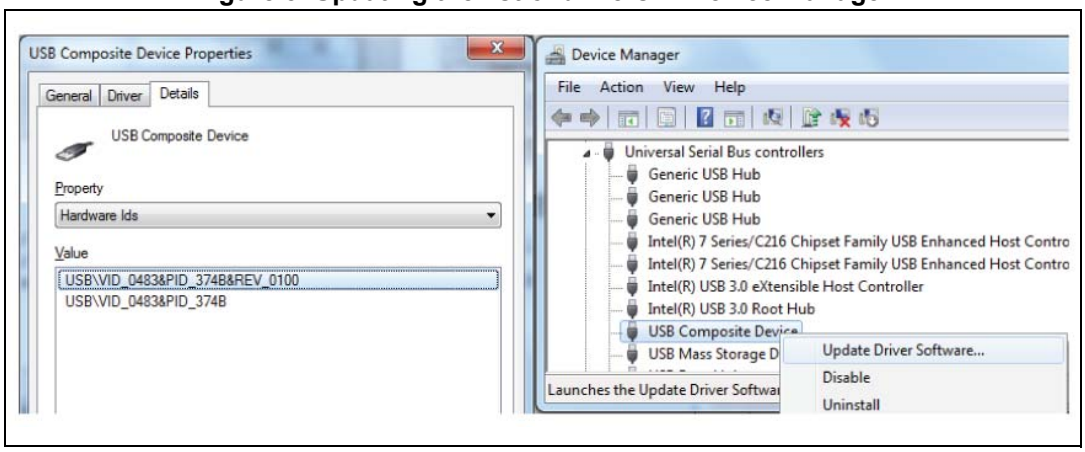
Jumper state	Description
Both CN4 jumpers ON	ST-LINK/V2 (or V2-B) functions enabled for on-board programming (default)
Both CN4 jumpers OFF	ST-LINK/V2 (or V2-B) functions enabled for application through external CN3 connector (SWD supported)

6.1.1 Drivers

The ST-LINK/V2 (or V2-B) requires a dedicated USB driver, which, for Windows® XP, 7, 8, can be found at the *www.st.com* website. In case the STM32 Discovery is connected to the PC before the driver is installed, some Discovery interfaces may be declared as “Unknown” in the PC device manager. In this case the user must install the driver files (*Figure 5: Updating the list of drivers in Device Manager*), and from the device manager he must update the driver of the connected device.

Note: Prefer using the "USB Composite Device" handle for a full recovery.

Figure 5. Updating the list of drivers in Device Manager



6.1.2 ST-LINK/V2 (or V2-B) firmware upgrade

The ST-LINK/V2 (or V2-B) embeds a firmware upgrade mechanism for in-situ upgrade through the USB port. As the firmware may evolve during the life time of the ST-LINK/V2 (or V2-B) product (for example new functionalities, bug fixes, support for new microcontroller families), it is recommended to visit the *www.st.com* website, before starting to use the Discovery board and periodically, to stay up-to-date with the latest firmware version.

6.1.3 VCP configuration

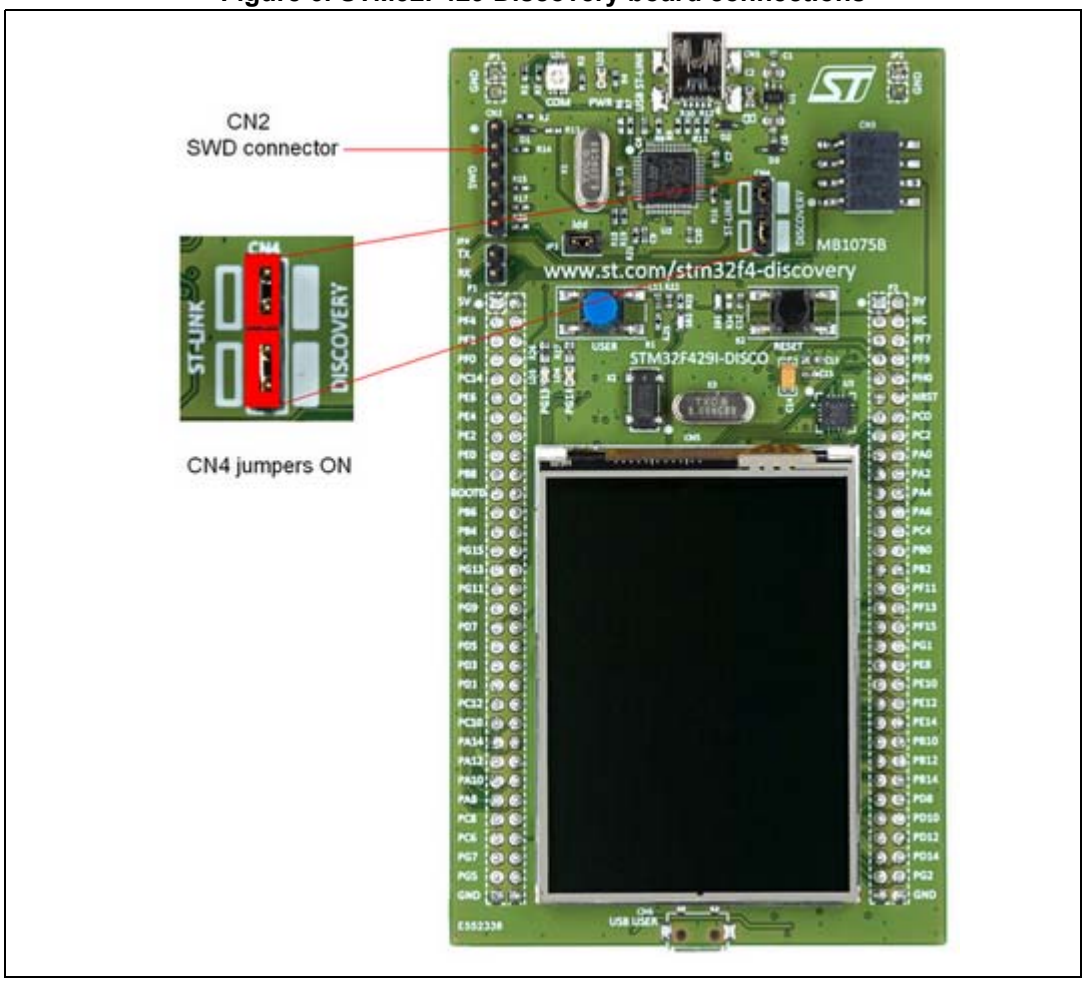
The ST-LINK/V2-B on STM32F429I-DISC1 supports Virtual Com Port (VCP) on U2 pin 12 (ST-LINK_TX) and U2 pin 13 (ST-LINK_RX), which are connected to the STM32F429 target STM32 USART1 (PA9, PA10) for mbed support, thanks to SB11 and SB15 solder bridges.

The SB11 (PA9) and SB15 (PA10) default configurations for 32F429IDISCOVERY and STM32F429I-DISC1 are given in *Table 5: Solder bridges*.

6.1.4 Using ST-LINK/V2 (or V2-B) to program/debug the STM32F429ZIT6 on board

To program the STM32F429ZIT6 on board, simply plug in the two jumpers on CN4, as shown marked in red in *Figure 6*, but do not use the CN3 connector as it could disturb the communication with the STM32F429ZIT6 of the STM32F429 Discovery board.

Figure 6. STM32F429 Discovery board connections



6.1.5 Using ST-LINK/V2 (or V2-B) to program/debug an external STM32 application

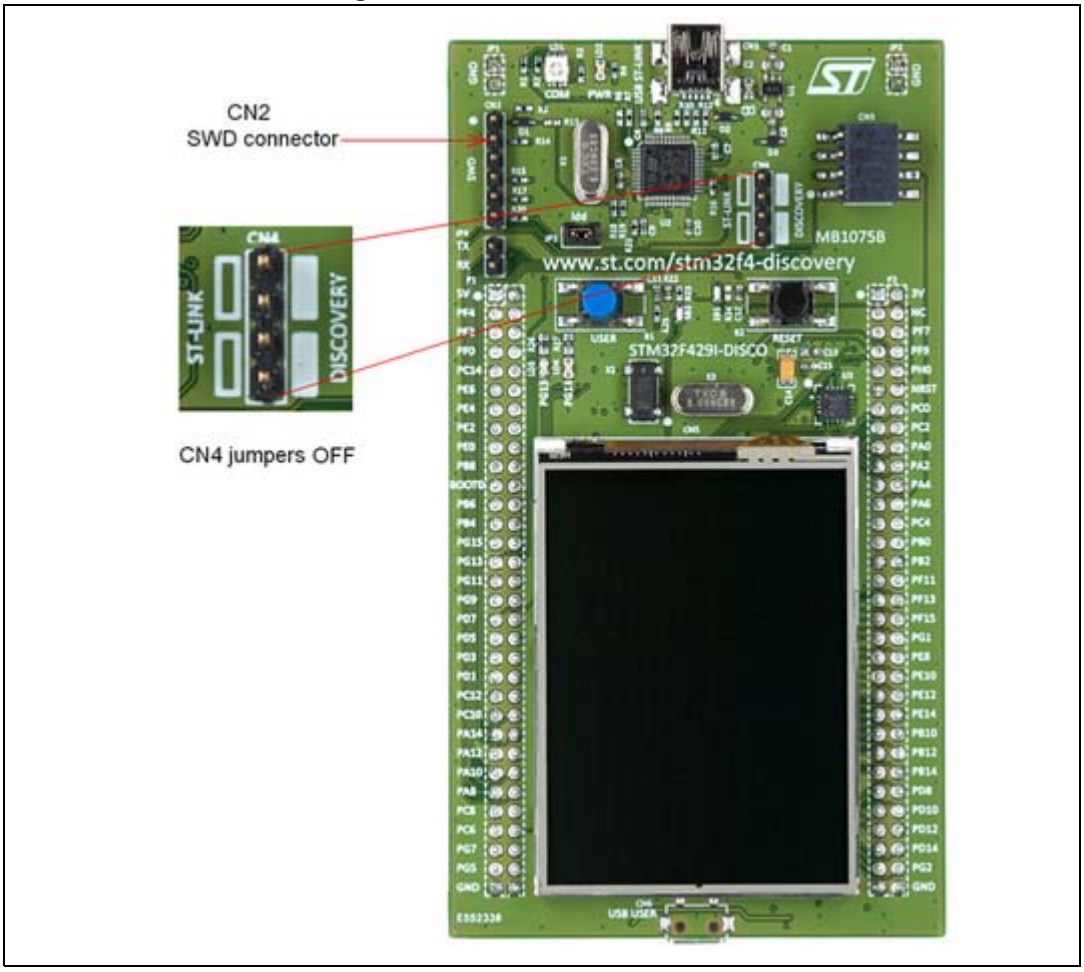
It is very easy to use the ST-LINK/V2 (or V2-B) to program the STM32 on an external application. Simply remove the two jumpers from CN4 as shown in *Figure 7* and connect the application to the CN3 debug connector according to *Table 4*.

Note: SB7 must be OFF if CN2 pin 5 is used in an external application.

Table 4. Debug connector CN2 (SWD)

Pin	CN2	Designation
1	VDD_TARGET	VDD from application
2	SWCLK	SWD clock
3	GND	Ground
4	SWDIO	SWD data input/output
5	NRST	RESET of target STM32
6	SWO	Reserved

Figure 7. ST-LINK/V2 connections



6.2 Power supply and power selection

The power supply is provided either by the host PC through the USB cable, or by an external 5 V power supply.

The D1 and D2 diodes protect the 5 V and 3 V pins from external power supplies:

- 5 V and 3 V can be used as output power supplies when another application board is connected to pins P1 and P2.
  In this case, the 5 V and 3 V pins deliver a 5 V or 3 V power supply and the power consumption must be lower than 100 mA.
- 5 V and 3 V can also be used as input power supply, e.g. when the USB connectors are not connected to the PC.
  In this case, the STM32F429 Discovery board must be powered by a power supply unit or by an auxiliary equipment complying with the standard EN-60950-1: 2006+A11/2009, and must be Safety Extra Low Voltage (SELV) with limited power capability.

Note: The board can also be powered through the USB USER connector and is protected by D4 and D5 diodes when both USBs are connected (in which case, the 5 V power is around 4.4 volts).

6.3 LEDs

- LD1 COM:
  LD1 default status is red. LD1 turns to green to indicate that communications are in progress between the PC and the ST-LINK/V2.
- LD2 PWR:
  The red LED indicates that the board is powered.
- User LD3:
  The green LED is a user LED connected to the I/O PG13 of the STM32F429ZIT6.
- User LD4:
  The red LED is a user LED connected to the I/O PG14 of the STM32F429ZIT6.
- User LD5:
  The green LED indicates when VBUS is present on CN6 and is connected to PB13 of the STM32F429ZIT6.
- User LD6:
  The red LED indicates an overcurrent from VBUS of CN6 and is connected to the I/O PC5 of the STM32F429ZIT6.

6.4 Push-buttons

- B1 USER:
  User and Wake-Up button connected to the I/O PA0 of the STM32F429ZIT6.
- B2 RESET:
  The push-button connected to NRST is used to RESET the STM32F429ZIT6.

6.5 USB OTG supported

The STM32F429ZIT6 is used to drive only USB OTG full speed on this board. The USB micro-AB connector (CN6) allows the user to connect a host or device component, such as a USB key, mouse, and so on.

Two LEDs are dedicated to this module:

- LD5 (green LED) indicates when VBUS is active
- LD6 (red LED) indicates an overcurrent from a connected device

6.6 Gyroscope MEMS (ST MEMS L3GD20)

The L3GD20 is an ultra-compact, low-power, three-axis angular rate sensor. It includes a sensing element and an IC interface able to provide the measured angular rate to the external world through the I^2C/SPI serial interface.

The L3GD20 has a full-scale of ±245/±500/±2000 dps and is capable of measuring rates with a user-selectable bandwidth.

The STM32F429ZIT6 controls this motion sensor through the SPI interface.

6.7 TFT LCD (Thin-film-transistor liquid-crystal display)

The TFT LCD is a 2.41" display of 262 K colors. Its definition is QVGA (240 x 320 dots) and is directly driven by the STM32F429ZIT6 using the RGB protocol. It includes the ILI9341 LCD controller and can operate with a 2.8 ±0.3 V voltage.

The STM32F429ZIT6 controls this motion sensor through the SPI interface.

6.8 64-Mbit SDRAM (1Mbit x 16-bit x 4-bank)

The 64-Mbit SDRAM is a high speed CMOS, dynamic random-access memory designed to operate in 3.3 V memory systems containing 67,108,864 bits. It is internally configured as a quad-bank DRAM with a synchronous interface. Each 16,777,216-bit bank is organized as 4,096 rows by 256 columns by 16 bits. The 64-Mbit SDRAM includes an auto-refresh, a power-saving and a power-down modes. All signals are registered on the positive edge of the clock signal, CLK.

The STM32F429ZIT6 reads and writes data at 80 MHz.

6.9 JP3 (Idd)

Jumper JP3, labeled Idd, allows the consumption of STM32F429ZIT6 to be measured by removing the jumper and connecting an ammeter.

- Jumper on: STM32F429ZIT6 is powered (default).
- Jumper off: an ammeter must be connected to measure the STM32F429ZIT6 current, (if there is no ammeter, the STM32F429ZIT6 is not powered).

6.10 OSC clock

6.10.1 OSC clock supply

The following information indicates all configurations for clock supply selection.

- **MCO from ST-LINK** (from MCO of the STM32F429ZIT6)
  This frequency cannot be changed, it is fixed at 8 MHz and connected to PH0-OSC_IN of the STM32F429ZIT6. The configuration needed is:
  - SB18 closed, SB19 open, R56 removed
  - SB20, R57, C20, C21, X3 = do not care
- **Oscillator on board** (from X3 crystal)
  For typical frequencies and its capacitors and resistors, refer to the STM32F429ZIT6 datasheet. The configuration needed is:
  - SB18, SB19, SB20 open
  - R56, R57, C20, C21, X3 soldered
- **Oscillator from external PH0** (from external oscillator through pin 10 of the P2 connector)
  The configuration needed is:
  - SB19 closed, SB18 open, R56 removed
  - SB20, R57, C20, C21, X3 = do not care
- **No external oscillato**r (from internal oscillator HSI only).
  PH0 and PH1 can be used as GPIO. The configuration needed is:
  - SB18 open, SB19 closed, SB20 closed, R56 removed, R57 removed
  - C20, C21, X3 = do not care

6.10.2 OSC 32 KHz clock supply

The following information indicates all configurations for the 32 KHz clock supply selection.

- **Oscillator on board** (from X2 Crystal, not provided).
  The configuration needed is:
  - SB16 open, SB17 open
  - R53, R54, C23, C24, X2 soldered
- **Oscillator from external PC14** (from external oscillator through pin 9 of P1 connector)
  The configuration needed is:
  - SB16 closed, R53 removed
  - SB17, R54, C23, C24, X2 = do not care
- **No external oscillator** (PC14 and PC15 can be used as GPIO.
  The configuration needed is:
  - SB16 closed, SB17 closed, R53 removed, R54 removed
  - C23, C24, X2 = do not care

6.11 Solder bridges

Table 5. Solder bridges

Bridge	State (1)	Description
SB19, 20 (X3 crystal)	**OFF**	X3, C20, C21, R56 and R57 provide a clock. PH0, PH1 are disconnected from P2.
	ON	PH0, PH1 are connected to P2. Remove only R56 and R57.
SB4, 6, 8,14 (default)	ON	Reserved, do not modify.
SB3, 5, 7,13 (reserved)	OFF	Reserved, do not modify.
SB22, 23, 24, 25	OFF	Reserved, do not modify.
SB16, 17 (X2 crystal)	OFF	X2, C23, C24, R53 and R54 deliver a 32 KHz clock. PC14, PC15 are not connected to P2
	ON	PC14, PC15 are only connected to P2. Remove only R53 and R54.
SB1 (B2-RESET)	**ON**	B2 push-button is connected to NRST of STM32F429ZIT6.
	OFF	B2 push-button is not connected to NRST of STM32F429ZIT6.
SB2 (B1-USER)	**ON**	B1 push-button is connected to PA0.
	OFF	B1 push-button is not connected to PA0.
SB11, 15 (RX,TX)	OFF	STM32F429 USART1 is not connected to ST-LINK, so VCP is disabled (Default configuration on 32F429IDISCOVERY).
	ON	STM32F429 USART1 is connected to ST-LINK, so VCP is enabled (default configuration on STM32F429I-DISC1).
SB12 (NRST)	**ON**	NRST signal of connector CN2 is connected to NRST of STM32F429ZIT6.
	OFF	NRST signal is not connected.
SB9 (SWO)	**OFF**	SWO signal is not connected.
	ON	SWO signal of connector CN3 is connected to PB3.
SB10 (STM_RST)	**OFF**	No incidence on NRST signal of STM32F429ZIT6.
	ON	NRST signal of STM32F429ZIT6 is connected to GND.
SB21 (BOOT0)	**ON**	BOOT0 signal of STM32F429ZIT6 is at level "0" through 510 Ω pull-down.
	OFF	BOOT0 signal of STM32F429ZIT6 is at level "1" through 10 KΩ pull-up (not provided).
SB26, 27 (USB OTG)	**OFF**	PB14 and PB15 are only used for USB OTG and not connected to P2 to avoid noise.
	ON	PB14 and PB15 are connected to P2.
SB18 (MCO)	**OFF**	MCO signal of STM32F429ZIT6 is not used.
	ON	MCO clock signal from STM32F429ZIT6 is connected to OSC_IN of STM32F429ZIT6.

1. Default SBx state is shown in bold.

6.12 Extension connectors

The male headers P1 and P2 can connect the STM32F429 Discovery board to a standard prototyping/wrapping board. STM32F429ZIT6 GPIOs are available on these connectors. P1 and P2 can also be probed by an oscilloscope, a logical analyzer or a voltmeter.

Table 6. STM32 pin description versus board function

STM32 pin		Board function																		
Main function	LQFP144	System	VCP	SDRAM	LCD-TFT	LCD-RGB	LCD-SPI	L3GD20	USB	LED	Push-button	I^2C Ext	Touch panel	Free I/O	Power supply	CN2	CN3	CN6	P1	P2
BOOT0	138	BOOT0	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	21	-
NRST	25	NRST	-	-	RESET	RESET	RESET	-	-	-	B2	-	-	-	-	5	-	-	-	12
PA0	34	-	-	-	-	-	-	-	-	-	B1	-	-	-	-	-	-	-	-	18
PA1	35	-	-	-	-	-	-	INT1	-	-	-	-	-	-	-	-	-	-	-	17
PA2	36	-	-	-	-	-	-	INT2	-	-	-	-	-	-	-	-	-	-	-	20
PA3	37	-	-	-	DB3	B5	-	-	-	-	-	-	-	-	-	-	-	-	-	19
PA4	40	-	-	-	VSYNC	VSYNC	-	-	-	-	-	-	-	-	-	-	-	-	-	22
PA5	41	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	21
PA6	42	-	-	-	DB6	G2	-	-	-	-	-	-	-	-	-	-	-	-	-	24
PA7	43	-	-	-	-	-	-	-	-	-	-	I2C_EXT_RST	-	-	-	-	4	-	-	23
PA8	100	-	-	-	-	-	-	-	-	-	-	SCL	SCL	-	-	-	3	-	53	-
PA9	101	-	USART1_TX	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	52	-
PA10	102	-	USART1_RX	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	51	-

Table 6. STM32 pin description versus board function (continued)

STM32 pin		Board function																		
Main function	LQFP144	System	VCP	SDRAM	LCD-TFT	LCD-RGB	LCD-SPI	L3GD20	USB	LED	Push-button	I^2C Ext	Touch panel	Free I/O	Power supply	CN2	CN3	CN6	P1	P2
PA11	103	-	-	-	DB14	R4	-	-	-	-	-	-	-	-	-	-	-	-	50	-
PA12	104	-	-	-	DB15	R5	-	-	-	-	-	-	-	-	-	-	-	-	49	-
PA13	105	SWDIO	-		-	-	-	-	-	-	-	-	-	-	-	4	-	-	48	-
PA14	109	SWCLK	-	-	-	-	-	-	-	-	-	-	-	-	-	2	-	-	47	-
PA15	110	-	-	-	-	-	-	-	-	-	-	-	INT	-	-	-	-	-	46	-
PB0	46	-	-	-	DB13	R3	-	-	-	-	-	-	-	-	-	-	-	-	-	28
PB1	47	-	-	-	DB16	R6	-	-	-	-	-	-	-	-	-	-	-	-	-	27
PB2	48	BOOT1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	30
PB3	133	SWO	-	-	-	-	-	-	-	-	-	-	-	-	-	6	-	-	28	-
PB4	134	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	25	-
PB5	135	-	-	SDCKE1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	26	-
PB6	136	-	-	SDNE1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	23	-
PB7	137	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	24	-
PB8	139	-	-	-	DB4	B6	-	-	-	-	-	-	-	-	-	-	-	-	19	-
PB9	140	-	-	-	DB5	B7	-	-	-	-	-	-	-	-	-	-	-	-	20	-
PB10	69	-	-	-	DB8	G4	-	-	-	-	-	-	-	-	-	-	-	-	-	48
PB11	70	-	-	-	DB9	G5	-	-	-	-	-	-	-	-	-	-	-	-	-	47
PB12	73	-	-	-	-	-	-	-	ID	-	-	-	-	-	-	-	-	4	-	50

Table 6. STM32 pin description versus board function (continued)

STM32 pin		Board function																		
Main function	LQFP144	System	VCP	SDRAM	LCD-TFT	LCD-RGB	LCD-SPI	L3GD20	USB	LED	Push-button	I^2C Ext	Touch panel	Free I/O	Power supply	CN2	CN3	CN6	P1	P2
PB13	74	-	-	-	-	-	-	-	VBUS	Green	-	-	-	-	-	-	-	1	-	49
PB14	75	-	-	-	-	-	-	-	DM	-	-	-	-	-	-	-	-	2	-	52 (1)
PB15	76	-	-	-	-	-	-	-	DP	-	-	-	-	-	-	-	-	3	-	51 (2)
PC0	26	-	-	SDNWE	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	14
PC1	27	-	-	-	-	-	-	CS	-	-	-	-	-	-	-	-	-	-	-	13
PC2	28	-	-	-	CSX	CSX	CSX	-	-	-	-	-	-	-	-	-	-	-	-	16
PC3	29	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	15
PC4	44	-	-	-	-	-	-	-	PSO	-	-	-	-	-	-	-	-	-	-	26
PC5	45	-	-	-	-	-	-	-	QC	Red	-	-	-	-	-	-	-	-	-	25
PC6	96	-	-	-	HSYNC	HSYNC	-	-	-	-	-	-	-	-	-	-	-	-	57	-
PC7	97	-	-	-	DB10	G6	-	-	-	-	-	-	-	-	-	-	-	-	56	-
PC8	98	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	55	-
PC9	99	-	-	-	-	-	-	-	-	-	-	SDA	SDA	-	-	-	1	-	54	-
PC10	111	-	-	-	DB12	R2	-	-	-	-	-	-	-	-	-	-	-	-	45	-
PC11	112	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	44	-
PC12	113	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	43	-
PC13	7		-		-	-	-	-	-	-	-	-	-	-	-	-	-	-	12	-
PC14	8	OSC32_IN	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	9	-

Table 6. STM32 pin description versus board function (continued)

STM32 pin		Board function																		
Main function	LQFP144	System	VCP	SDRAM	LCD-TFT	LCD-RGB	LCD-SPI	L3GD20	USB	LED	Push-button	I^2C Ext	Touch panel	Free I/O	Power supply	CN2	CN3	CN6	P1	P2
PC15	9	OSC32_OUT	-		-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	-
PD0	114	-	-	D2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	42	-
PD1	115	-	-	D3	-	-	-	-	-	-	-	-	-	-	-	-	-	-	41	-
PD2	116	-	-		-	-	-	-	-	-	-	-	-	-	-	-	-	-	40	-
PD3	117	-	-		DB11	G7	-	-	-	-	-	-	-	-	-	-	-	-	39	-
PD4	118	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	38	-
PD5	119	-	-		-	-	-	-	-	-	-	-	-	-	-	-	-	-	37	-
PD6	122	-	-		DB0	B2	-	-	-	-	-	-	-	-	-	-	-	-	36	-
PD7	123	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	35	-
PD8	77	-	-	D13	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	54
PD9	78	-	-	D14	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	53
PD10	79	-	-	D15	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	56
PD11	80	-	-		TE	-	-	-	-	-	-	-	-	-	-	-	-	-	-	55
PD12	81	-	-		RDX	-	-	-	-	-	-	-	-	-	-	-	-	-	-	58
PD13	82	-	-		WRX	-	DCX	-	-	-	-	-	-	-	-	-	-	-	-	57
PD14	85	-	-	D0	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	60
PD15	86	-	-	D1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	59
PE0	141	-	-	NBL0	-	-	-	-	-	-	-	-	-	-	-	-	-	-	17	-
PE1	142	-	-	NBL1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	18	-
PE2	1	-	-		-	-	-	-	-	-	-	-	-	-	-	-	-	-	15	-
PE3	2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	16	-
PE4	3	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	13	-
PE5	4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	14	-

Table 6. STM32 pin description versus board function (continued)

STM32 pin		Board function																		
Main function	LQFP144	System	VCP	SDRAM	LCD-TFT	LCD-RGB	LCD-SPI	L3GD20	USB	LED	Push-button	I^2C Ext	Touch panel	Free I/O	Power supply	CN2	CN3	CN6	P1	P2
PE6	5	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	11	-
PE7	58	-	-	D4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	37
PE8	59	-	-	D5	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	40
PE9	60	-	-	D6	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	39
PE10	63	-	-	D7	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	42
PE11	64	-	-	D8	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	41
PE12	65	-	-	D9	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	44
PE13	66	-	-	D10	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	43
PE14	67	-	-	D11	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	46
PE15	68	-	-	D12	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	45
PF0	10	-	-	A0	-	-	-	-	-	-	-	-	-	-	-	-	-	-	7	-
PF1	11	-	-	A1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	8	-
PF2	12	-	-	A2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	5	-
PF3	13	-	-	A3	-	-	-	-	-	-	-	-	-	-	-	-	-	-	6	-
PF4	14	-	-	A4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	3	-
PF5	15	-	-	A5	-	-	-	-	-	-	-	-	-	-	-	-	-	-	4	-
PF6	18	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	3
PF7	19	-	-	-	DCX	-	SCL	SCK	-	-	-	-	-	-	-	-	-	-	-	6
PF8	20	-	-	-	-	-	-	MISO	-	-	-	-	-	-	-	-	-	-	-	5
PF9	21	-	-	-	SDA	-	SDI/SDO	MOSI	-	-	-	-	-	-	-	-	-	-	-	8
PF10	22	-	-	-	ENABLE	DE	-	-	-	-	-	-	-	-	-	-	-	-	-	7
PF11	49	-	-	SDNRAS	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	32
PF12	50	-	-	A6	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	31
PF13	53	-	-	A7	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	34

Table 6. STM32 pin description versus board function (continued)

STM32 pin		Board function																		
Main function	LQFP144	System	VCP	SDRAM	LCD-TFT	LCD-RGB	LCD-SPI	L3GD20	USB	LED	Push-button	I^2C Ext	Touch panel	Free I/O	Power supply	CN2	CN3	CN6	P1	P2
PF14	54	-	-	A8	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	33
PF15	55	-	-	A9	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	36
PG0	56	-	-	A10	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	35
PG1	57	-	-	A11	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	38
PG2	87	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	62
PG3	88	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	61
PG4	89	-	-	BA0	-	-	-	-	-	-	-	-	-	-	-	-	-	-	62	-
PG5	90	-	-	BA1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	61	-
PG6	91	-	-	-	DB17	R7	-	-	-	-	-	-	-	-	-	-	-	-	60	-
PG7	92	-	-	-	DOTLCK	CLK	-	-	-	-	-	-	-	-	-	-	-	-	59	-
PG8	93	-	-	SDCLK	-	-	-	-	-	-	-	-	-	-	-	-	-	-	58	-
PG9	124	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	33	-
PG10	125	-	-	-	DB7	G3	-	-	-	-	-	-	-	-	-	-	-	-	34	-
PG11	126	-	-	-	DB1	B3	-	-	-	-	-	-	-	-	-	-	-	-	31	-
PG12	127	-	-	-	DB2	B4	-	-	-	-	-	-	-	-	-	-	-	-	32	-
PG13	128	-	-	-	-	-	-	-	-	Green	-	-	-	-	-	-	-	-	29	-
PG14	129	-	-	-	-	-	-	-	-	Red	-	-	-	-	-	-	-	-	30	-
PG15	132	-	-	SDNCAS	-	-	-	-	-	-	-	-	-	-	-	-	-	-	27	-
PH0	23	OSC_IN	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10

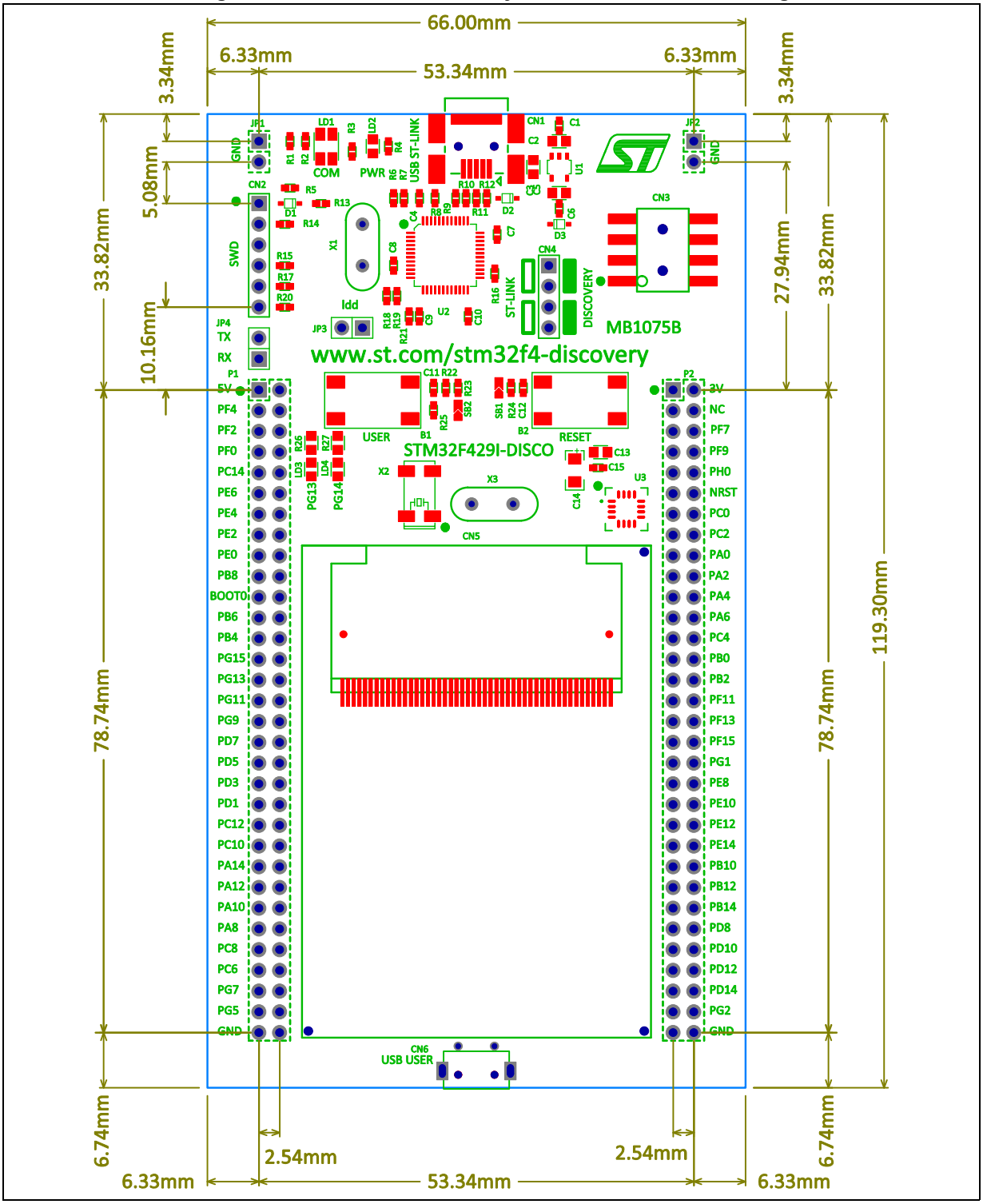
Table 6. STM32 pin description versus board function (continued)

STM32 pin		Board function																		
Main function	LQFP144	System	VCP	SDRAM	LCD-TFT	LCD-RGB	LCD-SPI	L3GD20	USB	LED	Push-button	I^2C Ext	Touch panel	Free I/O	Power supply	CN2	CN3	CN6	P1	P2
PH1	24	OSC_OUT	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	9
-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	VDD	-	-	-	22	-
-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	3 V	-	5	-	-	1
-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	3 V	-	-	-	-	2
-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	5 V	-	8	-	1	-
-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	5 V	-	-	-	2	-
-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	GND	3	7	5	63	11
-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	GND	-	-	-	64	29
-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	GND	-	-	-	-	63
-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	GND	-	-	-	-	-

1. If SB27 is ON.
2. If SB26 is ON.

7 Mechanical drawing

Figure 8. STM32F429 Discovery board mechanical drawing



8 Electrical schematics

Figure 9. STM32F429I-DISC1 Discovery board

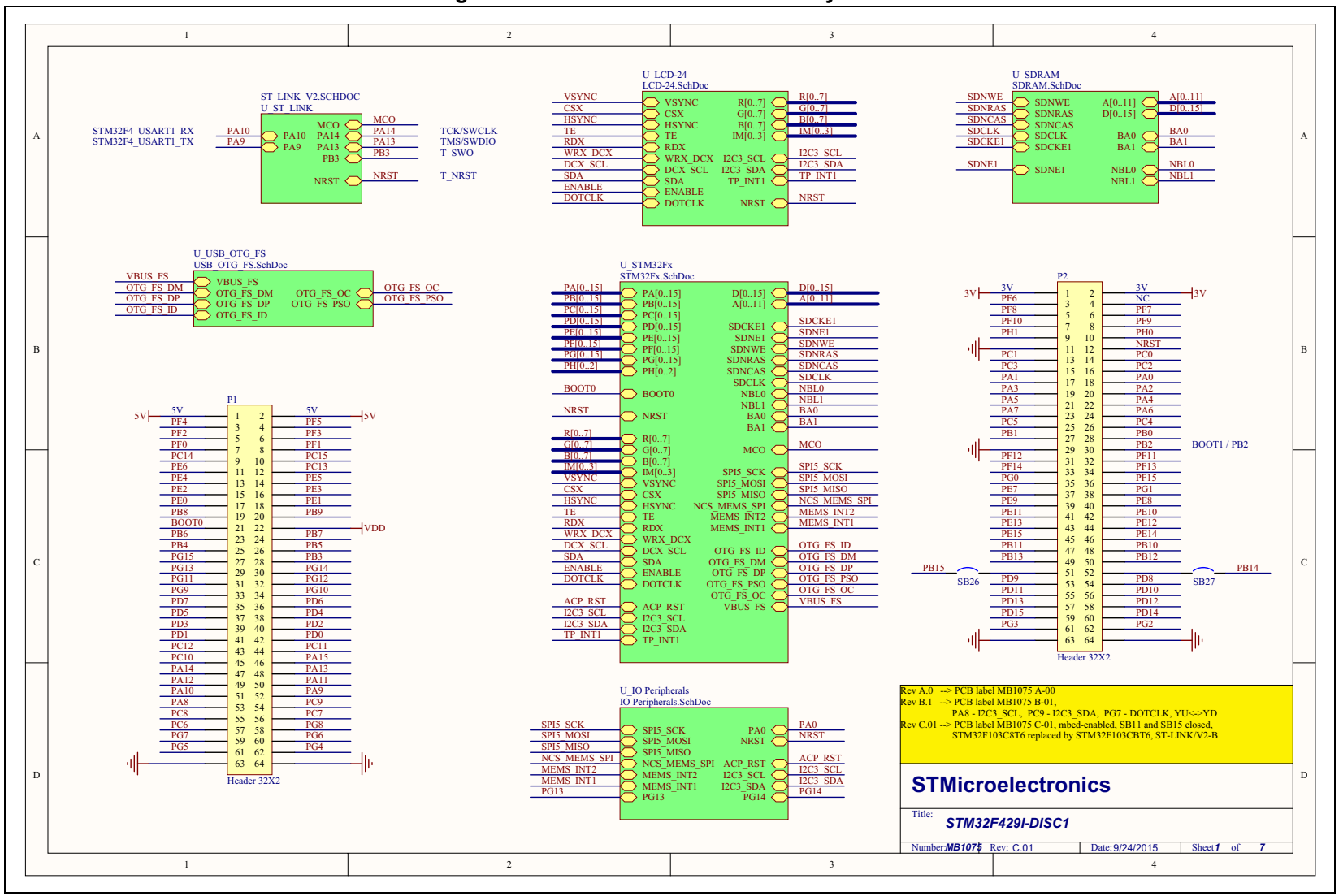


Figure 10. ST-LINK/V2 (SWD only)

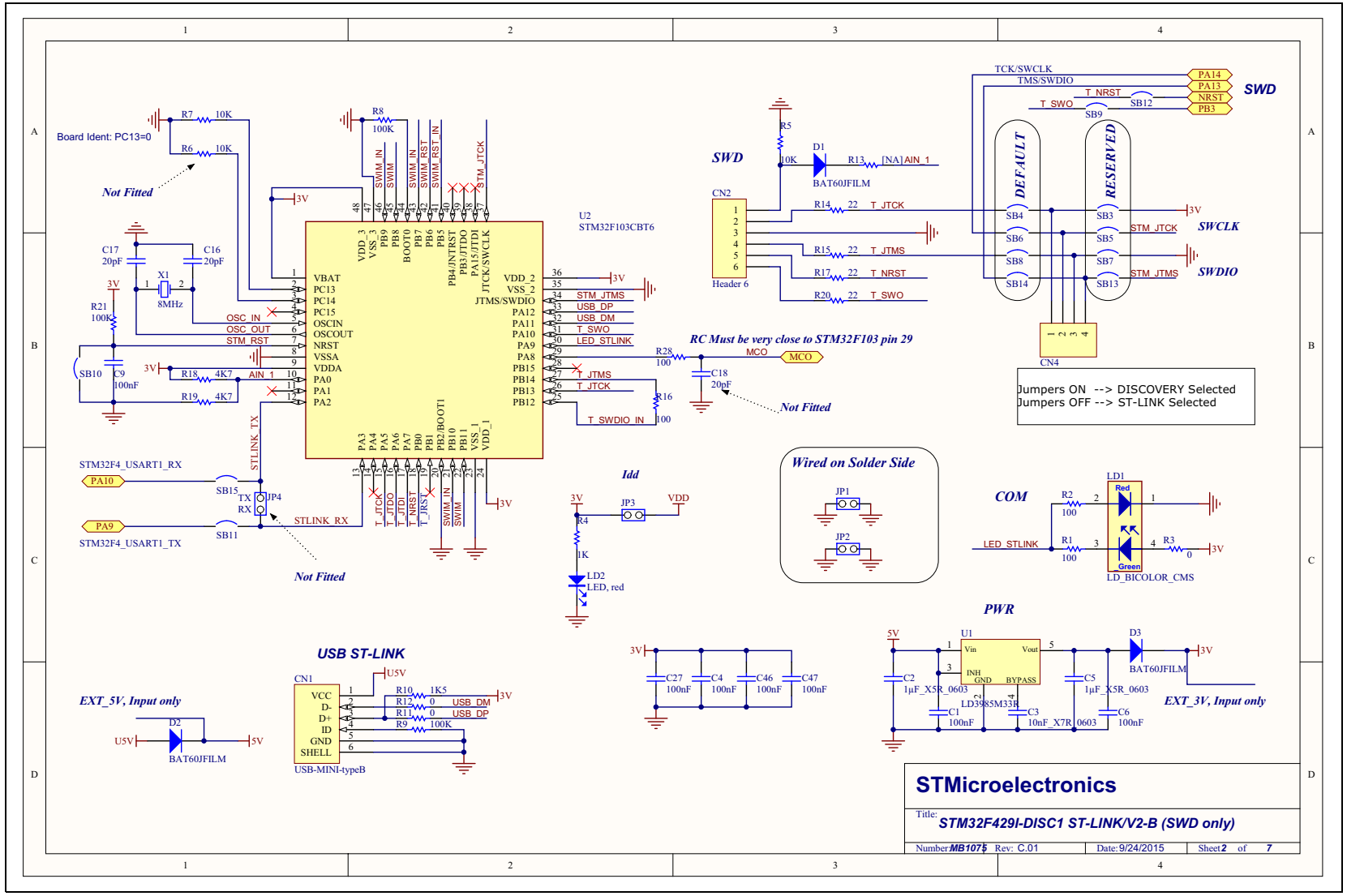


Figure 11. USB OTG FS

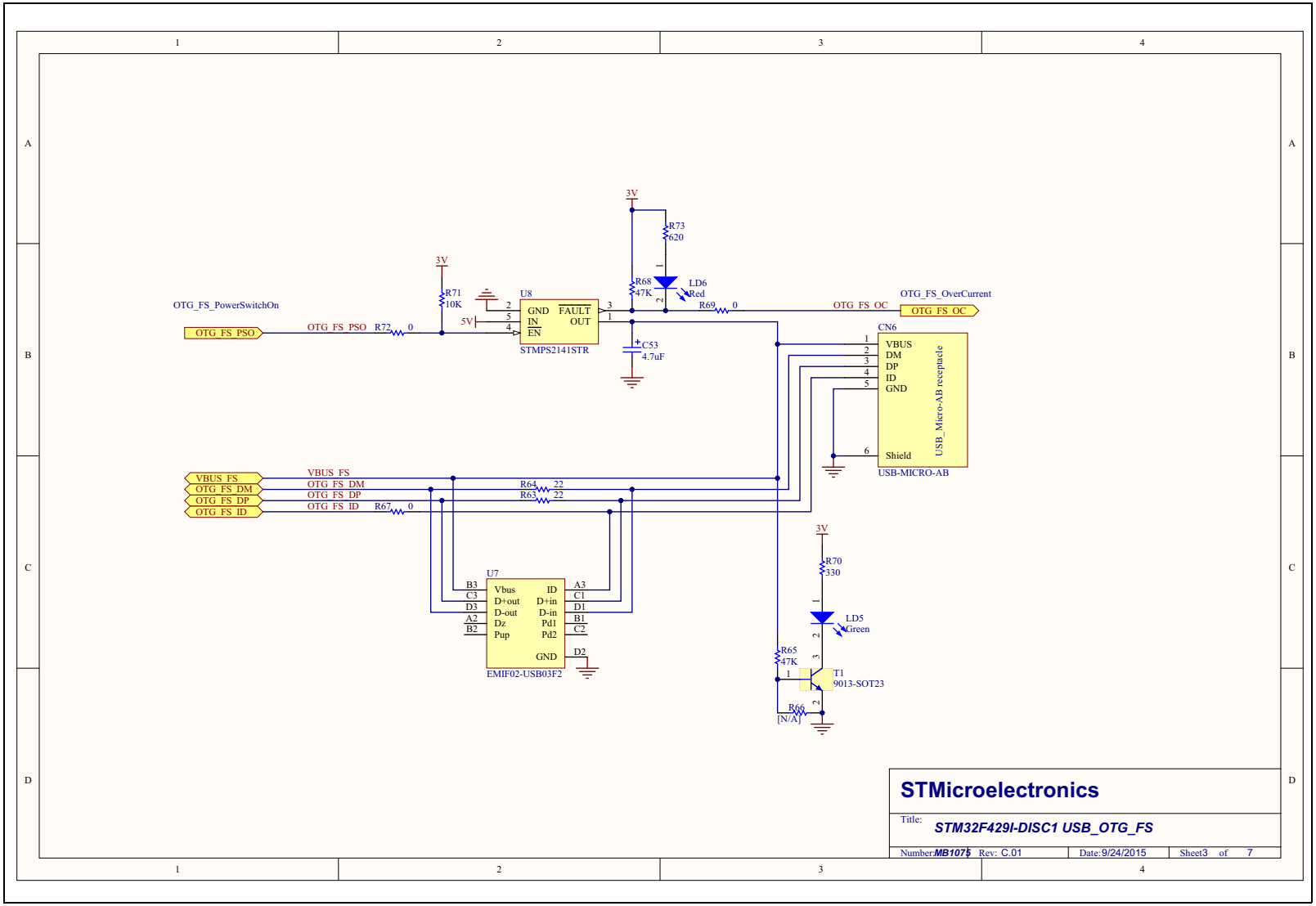


Figure 12. 64-Mbit SDRAM

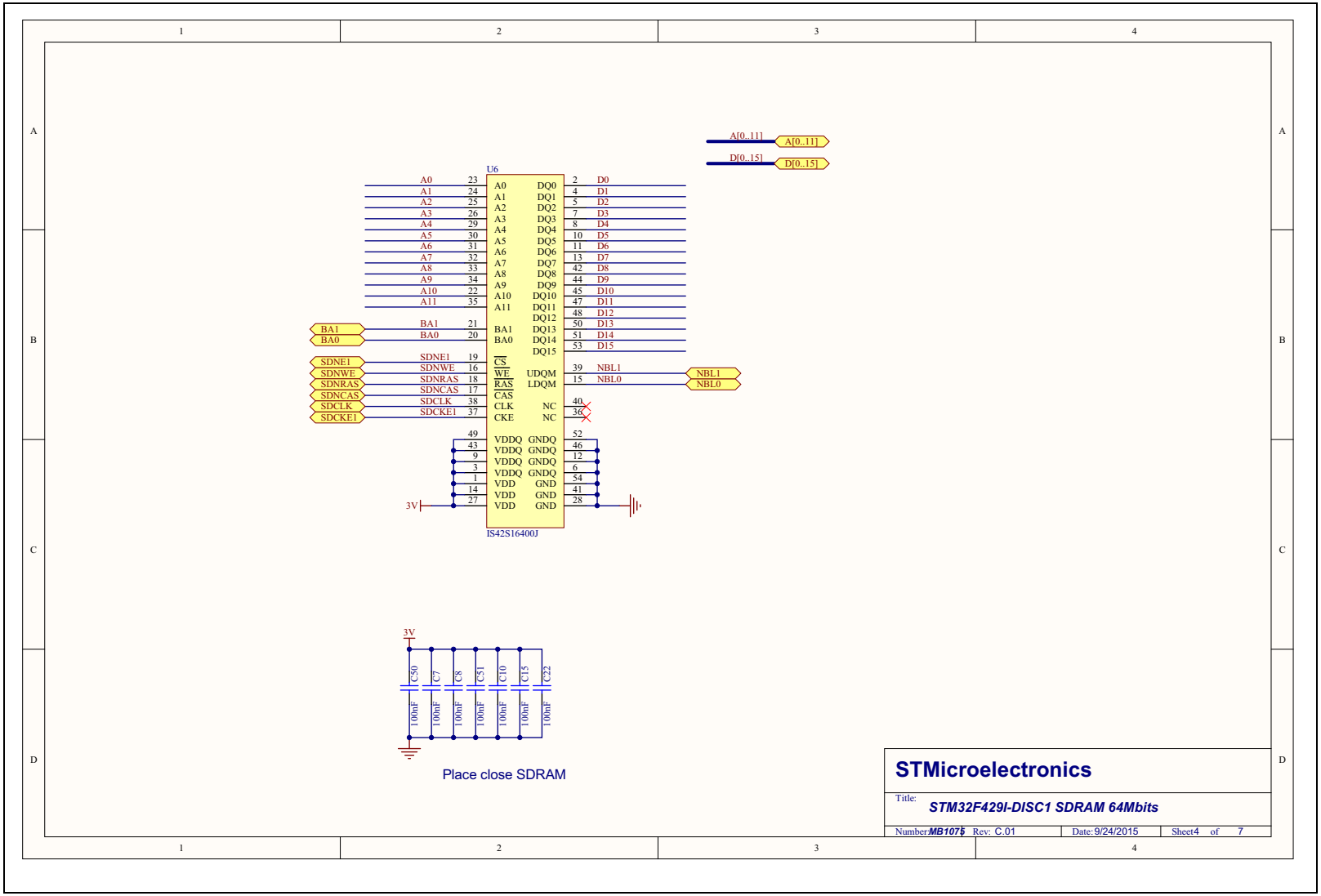


Figure 13. STM32F429ZIT6 MCU

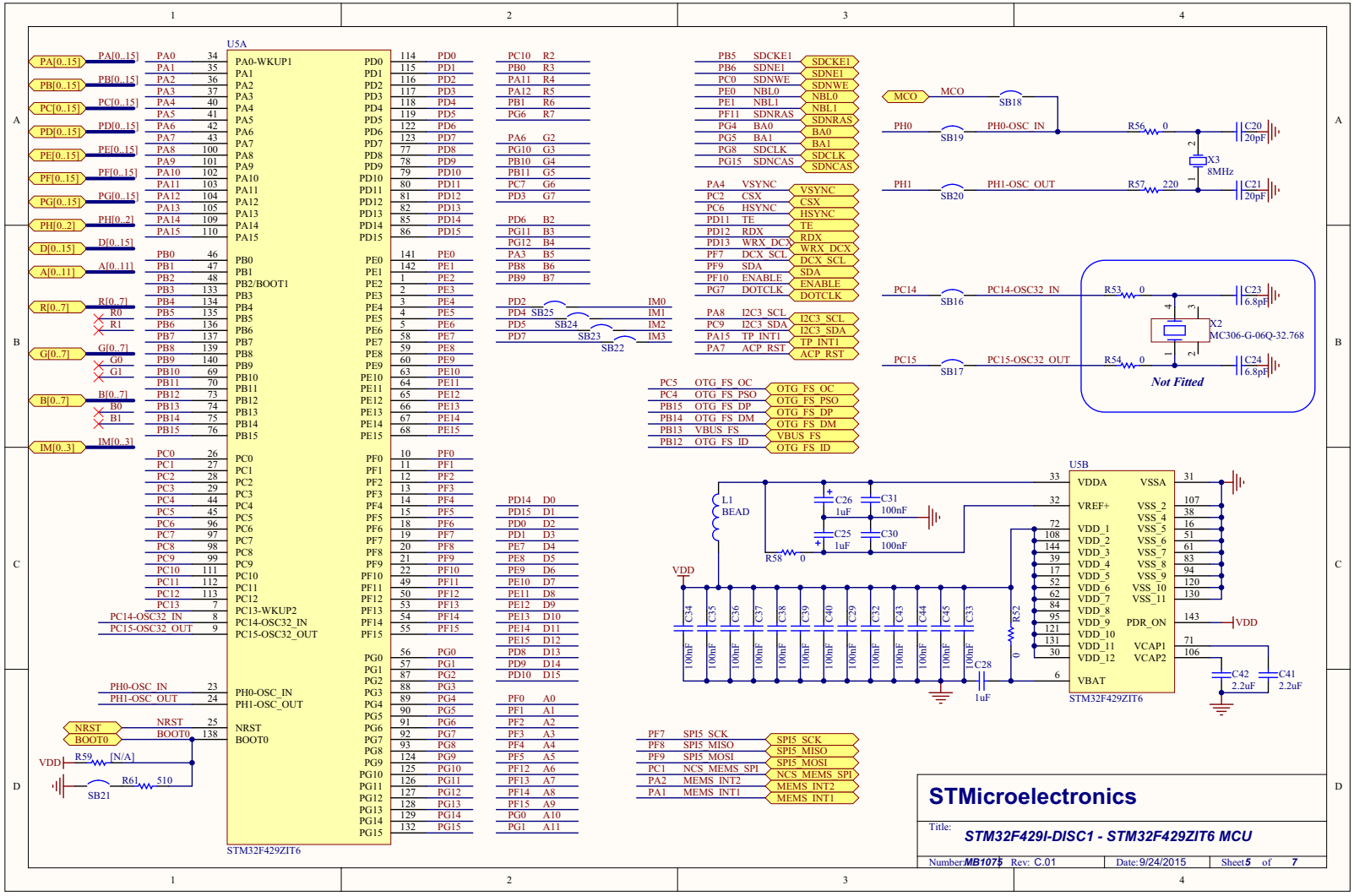


Figure 14. Peripherals

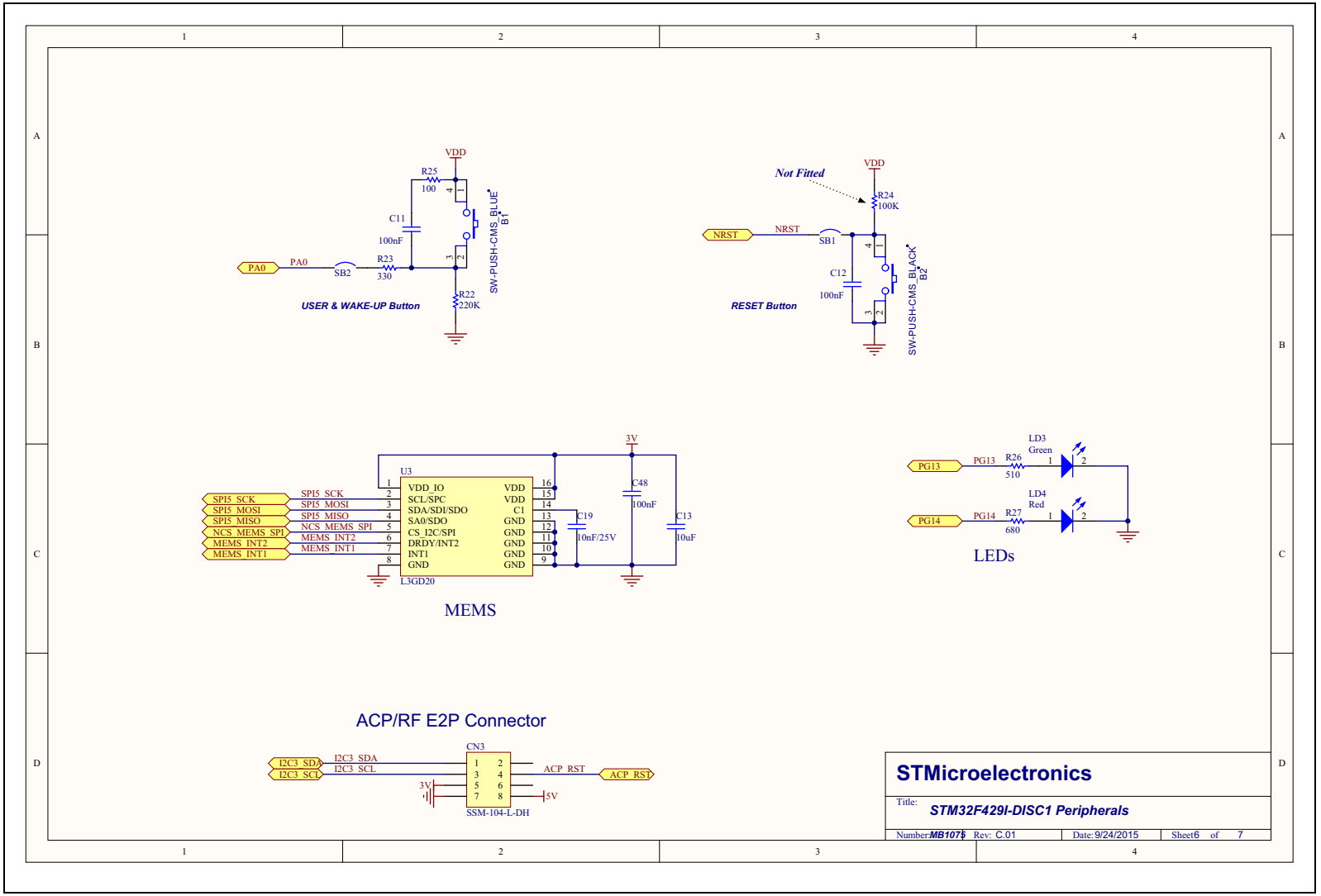
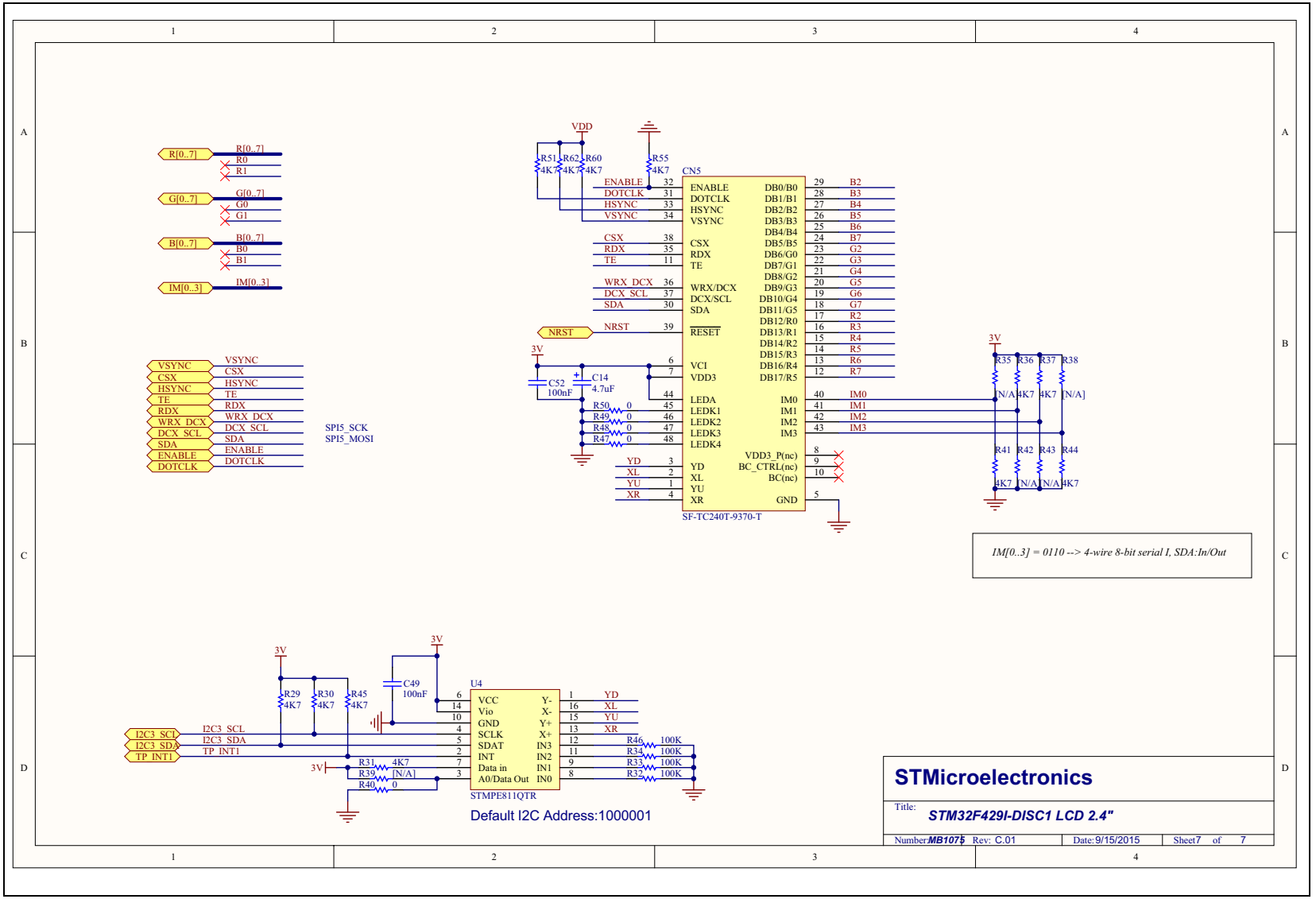


Figure 15. LCD 2.4



9 Revision history

Table 7. Document revision history

Date	Revision	Changes
10-Sep-2013	1	Initial release.
04-Mar-2016	2	New revision to introduce STM32F429I-DISC1 additional CPN that corresponds to mbed-enabled Discovery Kit. Updated: *Section : Introduction*, *Figure 1: STM32F429 Discovery board*, *Section 5: Quick start*, *Section 6: Hardware layout*, *Figure 2: Hardware block diagram*, *Figure 3: Top layout*, *Section 6.1: Embedded ST-LINK/V2 (or V2-B)*, *Table 5: Solder bridges*, *Table 6: STM32 pin description versus board function*, *Section 8: Electrical schematics*.

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