




Krzysztof Malek

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Experienced Senior Software Engineer, Team Leader, Software Architect with deep expertise in networking devices and protocols gained in many multinational projects. Started from wireless related projects (UMTS/WCDMA, WiMAX) then moved to data center / server networking, Ethernet switches and network adapters related software development (strong expertise in NIC Teaming, 802.1Q, SerDes, QoS, Congestion Management) and finally switched to the cable modems related software architecture and development (Cable Modem platform's Power Management, Packet Processor FW and SW). Experienced in network device drivers design and development for both Windows and Linux OSes. Strongly involved in innovation and path-finding (PoCs architecture and development).

Skills

- Embedded Systems (SW architecture, development and debugging)
- Kernel drivers for both Linux and Windows OSes
- Software Development Life Cycle
- Tech & Team Leadership
- Agile techniques and tools

Experience

APR 2018 – PRESENT

Software Architect, Intel Corp., Connected Home Division

Software Architect responsible for projects' technical direction, design, and development of software components for Intel SoCs specializing in networking processing. Makes high level design choices for the software structure, frameworks, protocols, and algorithms.

Designed and implemented SW support for fast IP protocol translation method in Intel cable modem. Involved in ramping-up of new development team. Taking role of Software Architecture responsible for Packet Processing and Power Management software components.

OCT 2016 – APR 2018

Development Team Leader, Intel Corp., Networking Division

Leader of high speed Intel Ethernet NIC and SoC devices driver's development team (Windows & Linux). Responsible for team's members ramp-up, coaching and software tasks' assignment based on project's priorities.

Participating in projects' requirements and priorities definition (cooperation with PM/VPM and other projects' stakeholders), providing input for validation scope required for Product Quality criteria for Software milestones.

SEP 2011 – OCT 2016

Senior Software Engineer, Intel Corp., Switching & Routing Division

Responsible for software components design, technical documentation, development and debugging of software components (C, perl, python) for Intel Ethernet Switch products. Focused on different areas of Ethernet networking functionalities like SerDes (customer support), QoS & Congestion Management (development) and MAC table management (design) software API. Participated in new chip's version analysis/verification from both hardware and software perspective. Strongly involved in innovation projects related to SDN (Software Defined Networking) application using Intel NICs and Ethernet Switch products.

MAR 2008 – SEP 2011

Network Software Engineer, Intel Corp., LAN Access Division

Windows network (NDIS) intermediate driver's maintenance, new features design and development. Implemented new features for network teaming NDIS driver responsible for manageability of advanced network services like fail-over, load balancing, ports aggregation, and VLAN configuration. Participated in whole path of many customer's issues reports starting with gathering information directly from customers through analysis/issues' fix providing and finally new SW package build. Designed and implemented new teaming mode (Virtual Machine Load Balancing) supported by the driver.

FEB 2004 – MAR 2008

Software Engineer, Intel Corp., Modular Platform Comm. Division

WiMAX Base Station Control Plane development and sustaining (C++), Physical Layer (802.16d - OFDM and 802.16e - OFDMA) development and maintenance (DSPs VHDL/assembler). Analysis and development of DSPs (picoChip) modules responsible for WiMAX PHY layer (especially focused on channels estimations functionalities). Development of RP3 over Ethernet driver (based on OBSAI standard), analysis and requirements specification of FPGA module for WiMAX Base-Band module. Responsible for integration of Base-Band module with Radio module, including radio module requirements specification (RFQ) and hands-on inter-operability (bring up) verifications.

MAY 2002 – FEB 2004

Software Engineer Jr., Intel Corp., Modular Platform Comm. Division

IXP2xxx data plane development (micorcode, microC). Development of IP tunneling (IPv6 over IPv4, IPv4 over IPv6) microblocks; Inter Chassis Forwarding microblocks for 3G Radio Network Controller project.

MAY 200 – AUG 2001

Software Architect Jr., Unimor Radiocom

Software development and requirements specification of radio FM radio transceiver (C/assembler, x51 family micro-controllers).

Education

1996-2001

Master of Science / Gdansk University of Technology

Graduated in Department of Radio-communication Systems and Networks, diploma thesis and research about Zero Forcing Equalization method.

1991-1996

Technician / Sec. Tech. School of Electrical Engineering in Wejherowo

Activities

Participated in University projects' activities by providing training sessions (MATLAB), consulting MsC thesis and developing sample applications (C# security library).

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