Zami Ncube

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**PERSONAL STATEMENT**

A Master of Science graduate in Power Electronics and Control, with good communication skills and confident in dealing with people from all walks of life. Ambitious and focused whilst appreciating the need to remain flexible and with very good customer facing skills and dedicated to maintaining high quality standards. Ability to function effectively as a team player as well as working independently to achieve organisational objectives. Very good analytical, design and problem-solving skills with strong attention to detail. Interested in Electronic Design, Control and Embedded systems and VHDL FPGA programming, I believe that my meticulous written and verbal abilities, alongside my essay-based degree, will prepare me for a role within this exciting and challenging industry.

**EDUCATION**

**10-2018 to 09-2019 MSc Power Electronics and Control (Commendation Award)**

University of Hertfordshire

*Core modules:*

* Advanced Power Electronics and Control, Artificial Intelligence, Control of Engineering Systems, Digital Signal Processing and Processes, Embedded Control Systems, Operations Management, Operations Research and Renewable Energy Systems and Smart Grids Technology.
* Embedded C/C++ washing machine and ATM cycle design on PIC microprocessor projects

*Final year project/Dissertation:*

* MSc Project: Design and Analysis of Approximate Computing circuits using FPGAs Using software technique, Bit-Width Reduction /Truncation to design and implement a generic VHDL approximate adder Synthesised and Implemented in Xilinx Vivado IDE and Zynq 7000 programmable Logic. Approximate computing is an exciting paradigm with noticeable area, power-saving, and improved computational speeds.

**09-2013 to 07-2016 BEng Electronic Engineering Honours (First Class)**

University of Huddersfield,

*Core modules:*

* Analogue Systems Communications, Digital Systems, Production Design and Manufacture, Embedded Systems, Signal Analysis and Control.

*Final year project/Dissertation:*

* Final Year Project: Design of an FPGA based Transmission line error detector

**TECHNICAL SKILLS**

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| **Proficient in:** | **Intermediate Level:** |
| * VHDL | * ALTIUM |
| * C/C++ | * SOLIDWORKS (self-taught) |
| * FPGA | * PCB Schematic Capture |
| * EAGLE | * FPGA Test bench creation |
| * MATLAB | * Eagle CAD |
| * Xilinx Vivado / Quartus II | * Analogue design |
| * Digital /Analogue Design | * Embedded C/C++ |
| * National Instruments Mutlisim | * Python * Arm /pic processors |
| * Digital design |  |

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**RELEVANT WORK EXPERIENCE**   
  
**02-2017 to 10-2018 MBDA Missile Systems,** *Components Engineer (Stevenage*)

* Provide technical expertise to design engineering, purchasing, manufacturing and supplier quality assurance to maintain quality control plans and to improve existing supply base/delivery and reduce defects
* Review and approve new part, suggest suitable alternatives where required
* Participate in Product design and parts list reviews for the purpose of component selection and standardization in accordance with parts management plan based on military, industry and ISO standards, and monitor reliability prediction of the components in board level.
* Generating procurement specifications to define, characterize and generate validation process for the customized components with source-controlled drawing and (Commercial off-the-shelf) COTS parts.
* Perform electrical characterization, reliability and product performance testing on new component and identify cost reduction opportunities generate and maintain approved parts list, cross reference drawing and Source Control Drawings; select and approve alternate/second sources of supply Review and control parts under RoHS, REACH regulation, develop mitigation and control plan for lead free parts for defence application
* Part obsolescence Management; recommend alternative/replacement part, investigate End of Life and LTB;
* Reviews, investigation and response to Product Change Notices alerts to minimise impact on products due to part obsolescence

**09-2016 to 02-2017 JBrand/ Wincor Nixdorf***,**ATM Engineer (Contract /Based in Huddersfield)*

* Performing ATM upgrades for the new polymer notes
* Compliance Checks ATM audits and communicating to the project manager to ensure smooth completion of the project
* Customer advice and training
* ATM repairs

**01-2007 to 07-2008 OmniCash Ltd, London**,*ATM (Cash Machines) Installations and Repair Engineer*)

* Performing installation of cash machine hardware
* Hardware diagnostics, maintenance, and repairs
* Software upload, upgrade and testing, and
* Customer product training
* Customer interaction while performing repairs, installations.

**02-2010 to 05-2011 Lloyds Banking Group, Leeds**,*User Access Management Support Analyst Engineer* (Contract)

* Providing second and third level support to Colleague IT Service Desk for incidents relating to profile and access issues
* Virtual Technical Specialist to Tier1 Team
* Exchange 2003/2010 Administration and Support
* AD administration, VPN Support and provisioning
* Oracle Identity management Tools
* Data Migration / Secure Copy

**(OTHER) SKILLS**

* Soldering
* Report writing/
* Xilinx Zynq 7000
* Excellent fault finding and diagnosis skills
* Modelsim

**INTERESTS**

* Watching and playing Football.
* Going to gym
* Tennis
* Travelling and exploring cities
* Reading

**REFERENCES**

Available upon request