CURRICULUM VITAE of Arthur William Eyke

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**Personal Statement**

I am a realtime software design/test engineer with many years of hardware design experience thus have an appreciation of both disciplines. Taken on responsibility for specific project areas but also liaised with related departments to ensure good design, testability and manufacture. My recent role in Grid Control gave me an insight to a new industry and technology. My work on Medical products in recent years has enabled me to embrace my attention to detail in design, test and validation for the full life cycle of medical devices. Position at Penlon hit twice by redundancy. Looking for a Permanent rather than contract role.

Ongoing software work. From Dec 2014 I took over the maintenance and updating of a local charity website. Also test/repair of computers/laptops.

**Jan 2018** Online JAVA course.

**Technology Experience**

**Discipline** *C Assembler Altium PCB Designer ARM MCU PIC SVN Agile/Jira VxWorks V&V LABVIEW/AgilentVEE S.C.M.(Perforce)*

*Safety Critical and Quality Standards 510(k) BS EN 62304:2006, BS EN 60601-1:2006*

**Industrial Experience**

**Hardware & Embedded Software Engineer (Permanent) FUNDAMENTALS LTD., Swindon.**

Mar – Oct 2018

* Coding an STM32F303 ARM MCU in “C” for correcting distortion in Current Transformers (CTs).
* Schematic design (upgrade) of Tap Position Indicator incorporating PIC24FV16KA using Altium Designer.
* Design PCB for Tap Position Indicator using Altium Designer PCB package .

**Oct 2013 – Feb 2018**

Repairing Computers/Laptops and maintaining a local charity website. HTML and CSS design experience.

**Software Design Eng. (Contract) PENLON, Abingdon.**

Oct 2012 – Sept 2013

* “C” programmer working on built-in test code for Digital Flow Meter using Enterprise Architect Design tool. Processor being ARM Cortex-M3 using IAR Embedded Workbench.
* Verification for type approval on both Medical Ventilators and Digital Flow Meters. Using “C” and MPE Forth. Static Analysis on “C” code using PC-Lint with unit testing using VectorCast.
* Design/Test code for product enhancements to meet Safety Critical and Quality Standards (BS EN 62304:2006, BS EN 60601-1:2006) also to FDA approval. Coding guidelines based on MISRA-C:2004.

**Test Engineer (Contract) CEMGRAFT, Newbury.**

Aug – Nov 2011

* Testing of in-house manufactured circuit boards. Ranging from digital I/F boards to multi processor semi-automated testing and diagnosis. Analogue/digital, Military and commercial.

**Test Engineer (Contract) COGNITO, Newbury.**

Jan – June 2011

* Verifying code fixes on a range of P.D.A.s used for mobile workforce management solutions.
* Regression testing, logging errors and anomalies to specified performance within an Agile environment.

**Software Design Eng. (Permanent) PENLON, Abingdon.**

Mar 2007 – Oct 2010

* Software designer for Medical Ventilators and Anaesthetic Vaporisers. Using Linux and MPE Forth. Coding on Single and Multi uP systems Environment Perforce S.C.M. UltraEdit and CodeWright.
* Design and Test code for enhancements to current products in order to meet newer stringent Safety Critical and Quality Standards (ISO 62304, ISO 14971 ISO 13485).
* Ownership of design and regression testing of Medical Ventilator GUI for new markets.
* Solely responsible for improving the accuracy and repeatability of Vaporiser delivery using software to compensate for hardware and mechanical limitations.
* Preparation of 510(k) for FDA approval and test procedures for the Anaesthesia System test dept.

**Software Designer (Contract) MOTOROLA, Swindon.**

Apr 2005 – Mar 2006

* Working in a small team on a Packet Subrate Interface for a GSM Base Station Controller (BSC).
* Strict adherence to GSM protocol with peer reviews for design and rigorous process testing .
* Assembler coding on multiple DSP56311’s. “C” programming implemented on OS features of DSP’s and process testing the product. Using UNIX and CLEARCASE throughout the project.

**Software Designer (Contract) VERTU LTD, Church Crookham**

Mar – Dec 2004

* Working for the Reliability Dept. Solely responsible for creating a suite of automated tests controlling a robot to check reliability and performance and logging results of batches of Vertu GSM phones.
* Visit Nokia’s Copenhagen site for consultation with their Reliability Dept. and implementation of Vertu phone specific testing based on Nokia core.
* Using AgilentVEE controlling a Mitsubishi MELFA 6 Axis robot and interfacing to Audio, R.F., Camera equipment and H.M.I. aspects of Mobile phones via GPIB, USB links.

**Software Designer (Contract) INFOCELL TELECOM LTD.Copthorne**

Mar – June 2003

* Solely responsible for “C++” coding of Real Time Passenger Information Displays in the Infocell Transport Information System. Mainly working from my Home/Office.
* Providing a generic driver solution for different display types and protocols. Using VxWorks on a WIN32 S.B.C. platform.

**Software Designer (Contract) HITACHI MICRO SYSTEMS EUROPE LTD, Maidenhead**

Oct 2001 – Apr 2002

* Real Time “C” Programmer working on a Mobile Phone platform demonstrating Hitachi GSM /GPRS chipset based on SH7430uP (Super H RISC engine).
* Optimising 3rd party library and watermarking system. Using HIOS (RTOS) and Hitachi S/W tools with CLEARCASE SCM.

**Freelance Designer**

# 1985 – Oct 1998 (Numerous Hardware and Software design contracts)

# **Education and Qualifications**

Wolverton C.F.E. Higher Technical Certificate Electronics HTC 5

Cheltenham College of Technology.

# **Personal Interests**

Maintain and updating website for local charity. Repairing and optimising computers/laptops. I have an interest in Audio, Hi-Fi and music.