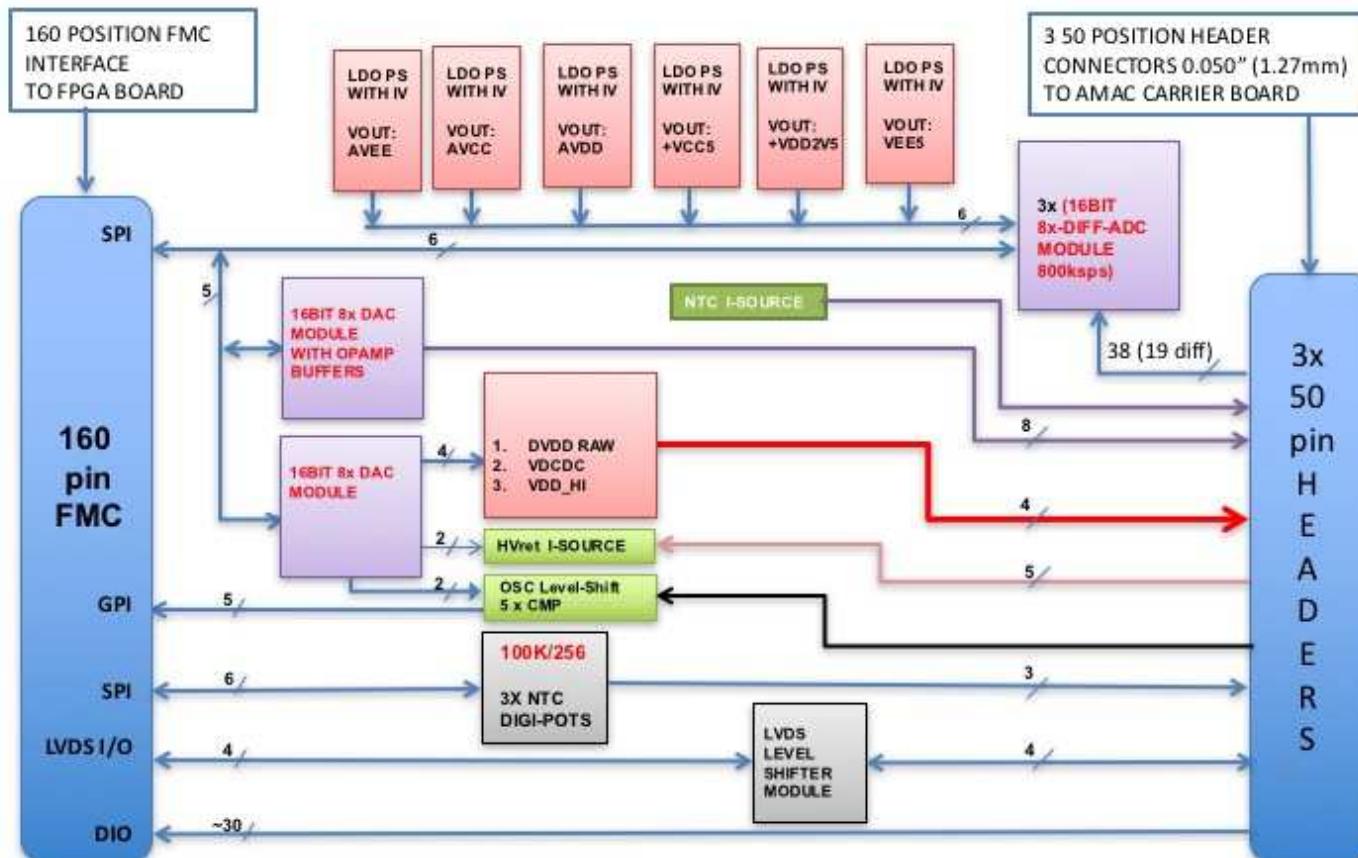


## AMAC V2 TEST & CONTROL BLOCK DIAGRAM



4/27/18

Godwin Mayers



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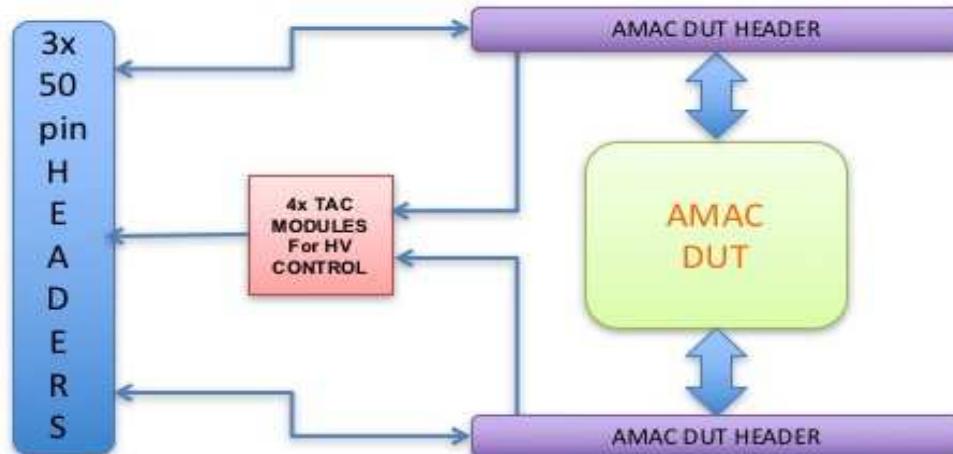
ATCB Block Diagrams

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## AMAC CARRIER AND DUT BOARDS

D  
3 50 POSITION HEADER  
CONNECTOR 0.050(1.27mm)  
TO AMAC TEST & CONTROL  
BOARD



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2



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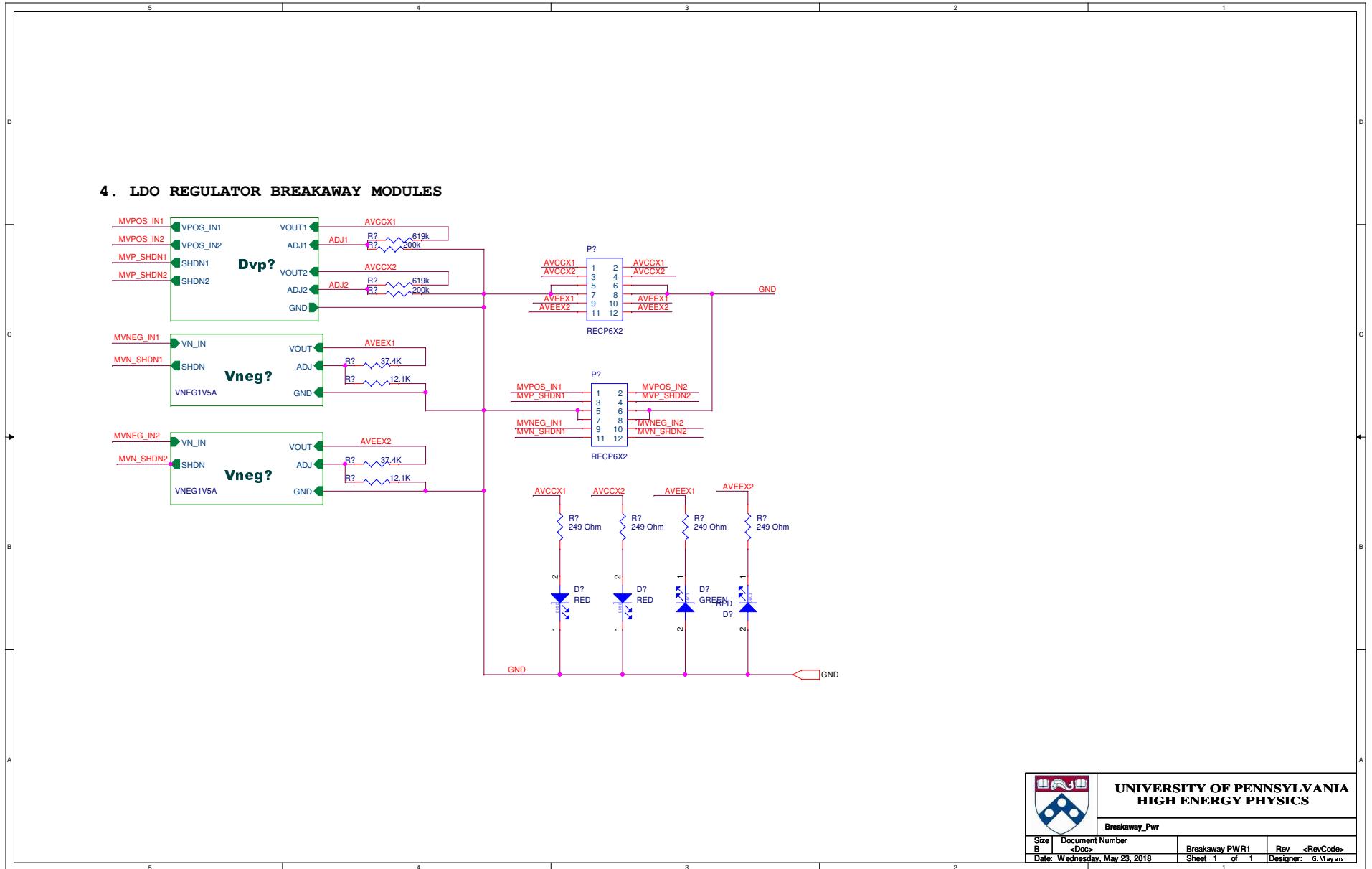
**AMAC CARRIER & DUT**

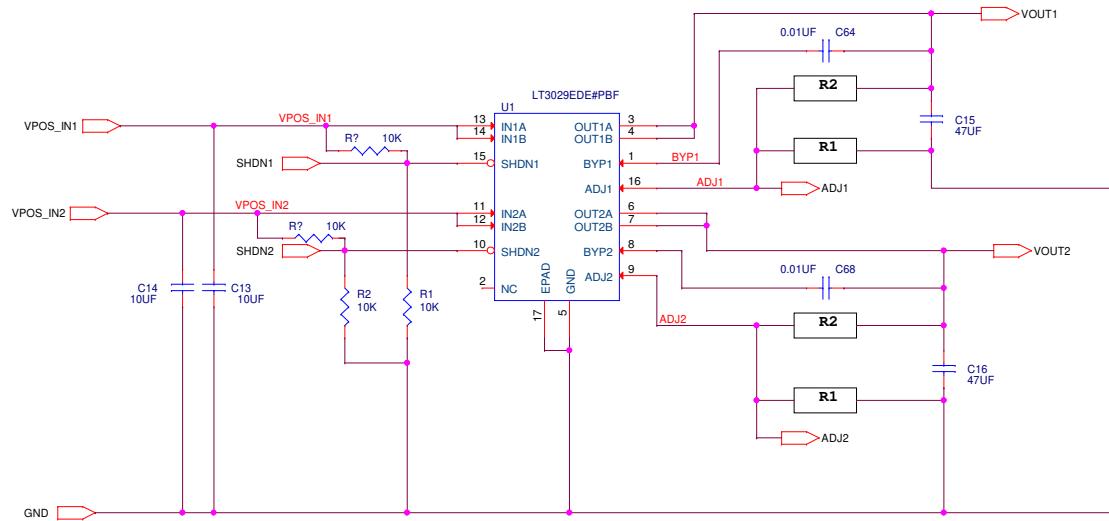
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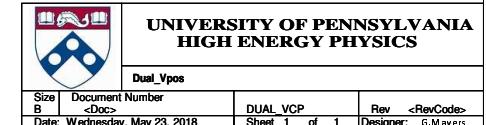


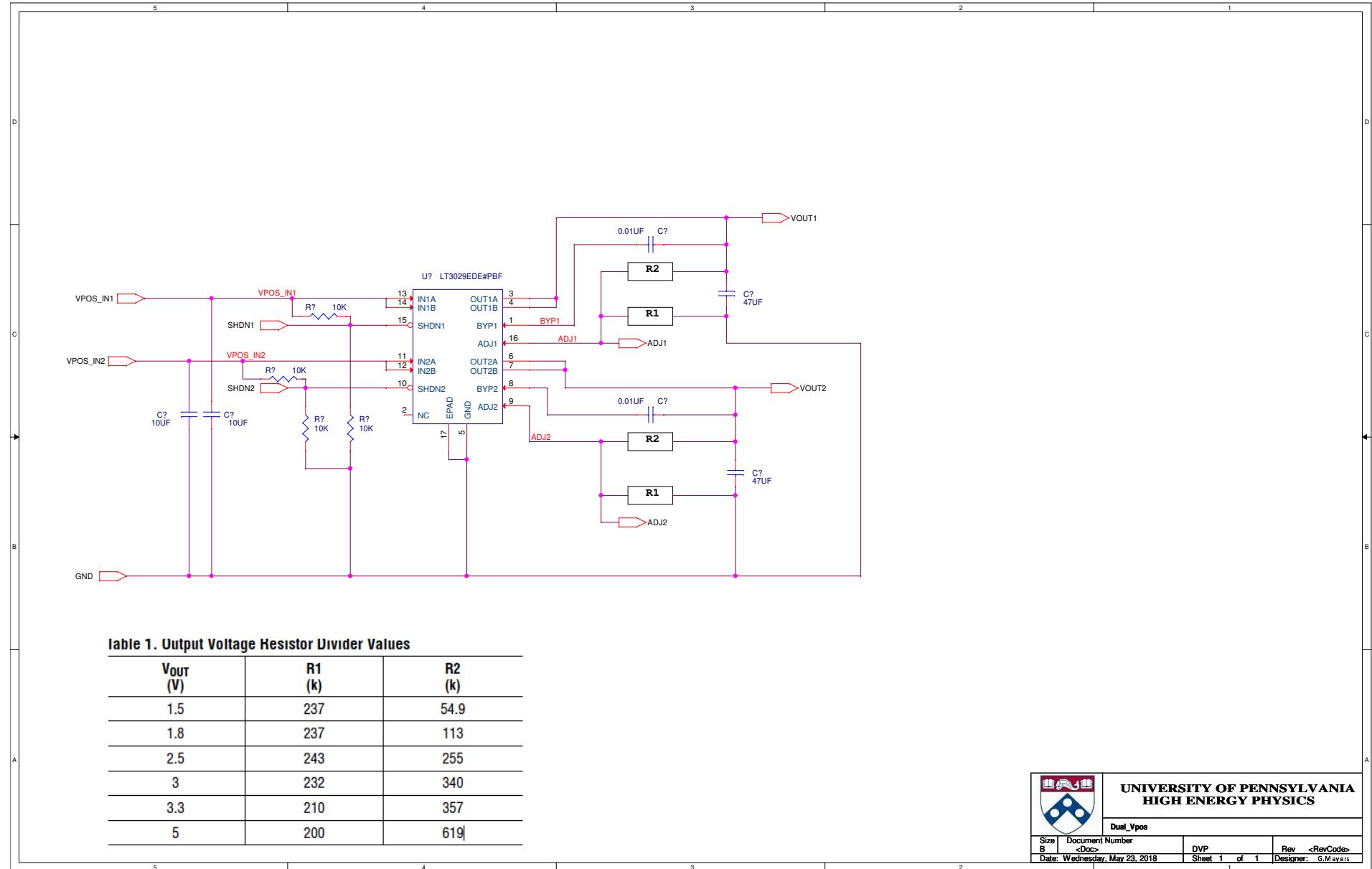


R1 and R2 blocks only serve as place holders when referencing table below  
and show where external resistor fit in the circuit.

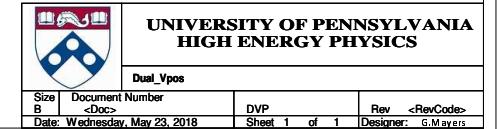
**Table 1. Output Voltage Resistor Divider Values**

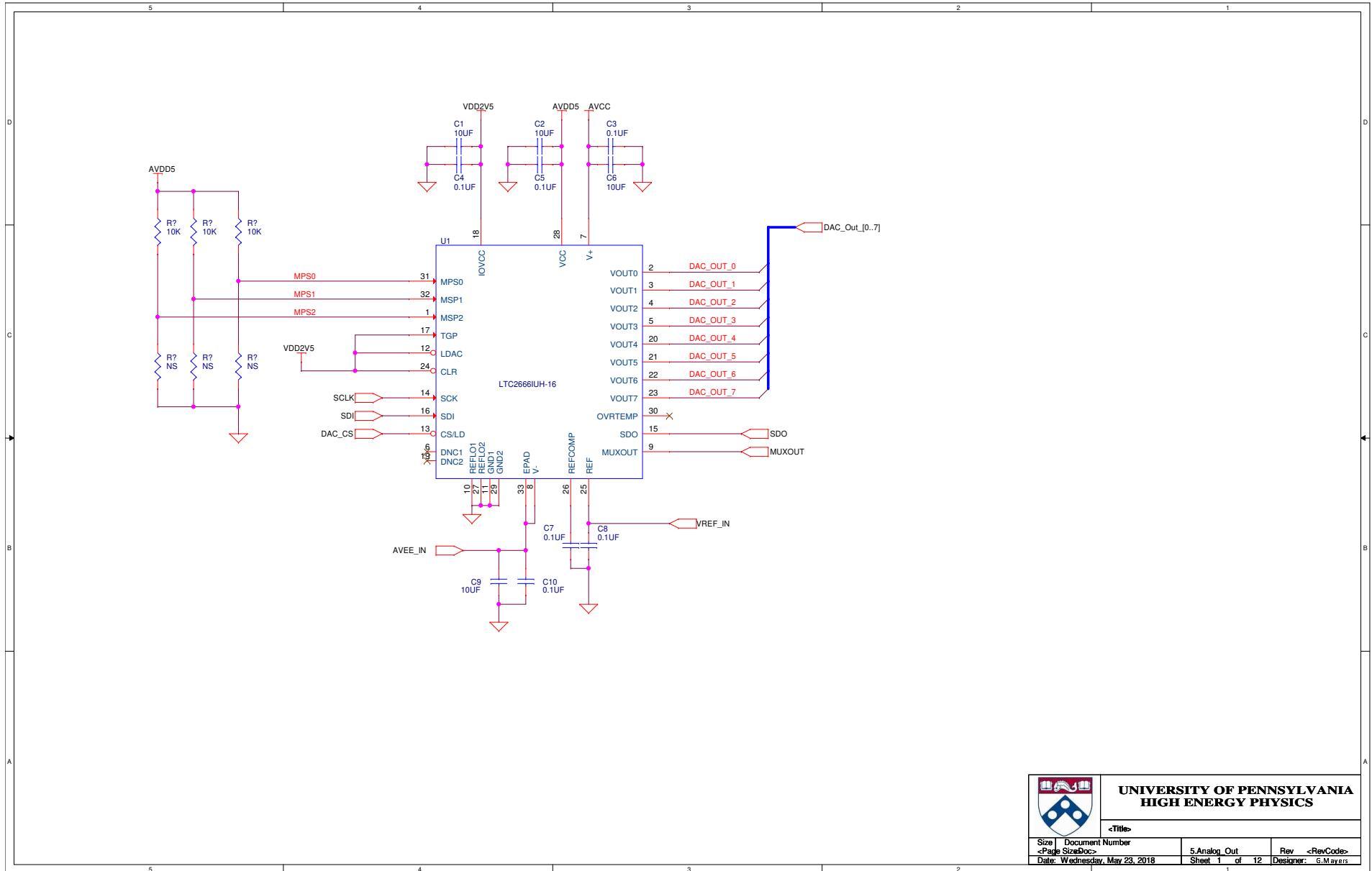
V <sub>OUT</sub> (V)	R <sub>1</sub> (k)	R <sub>2</sub> (k)
1.5	237	54.9
1.8	237	113
2.5	243	255
3	232	340
3.3	210	357
5	200	619





**Table 1. Output Voltage Resistor Divider Values**

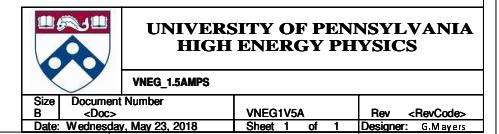
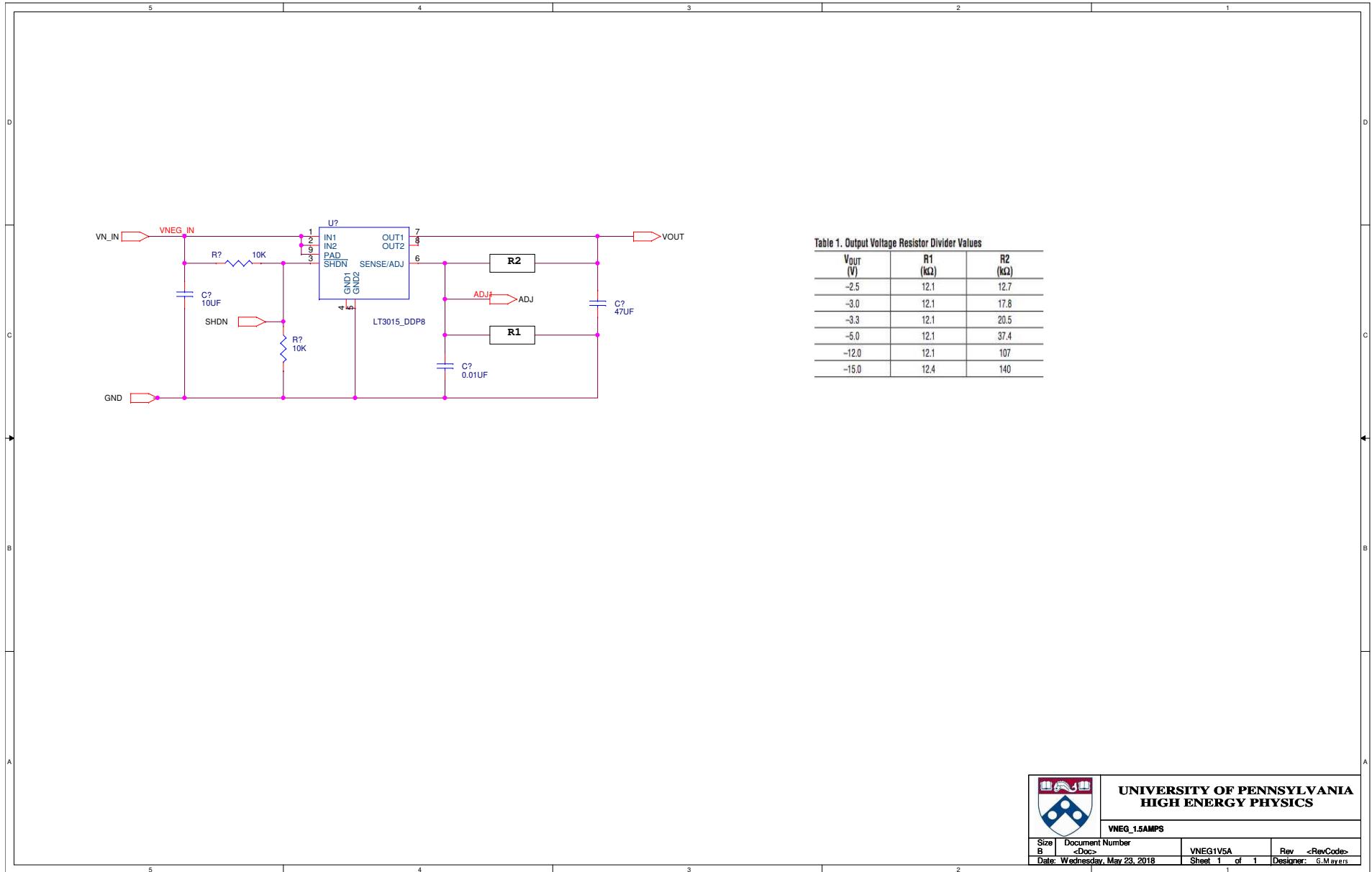




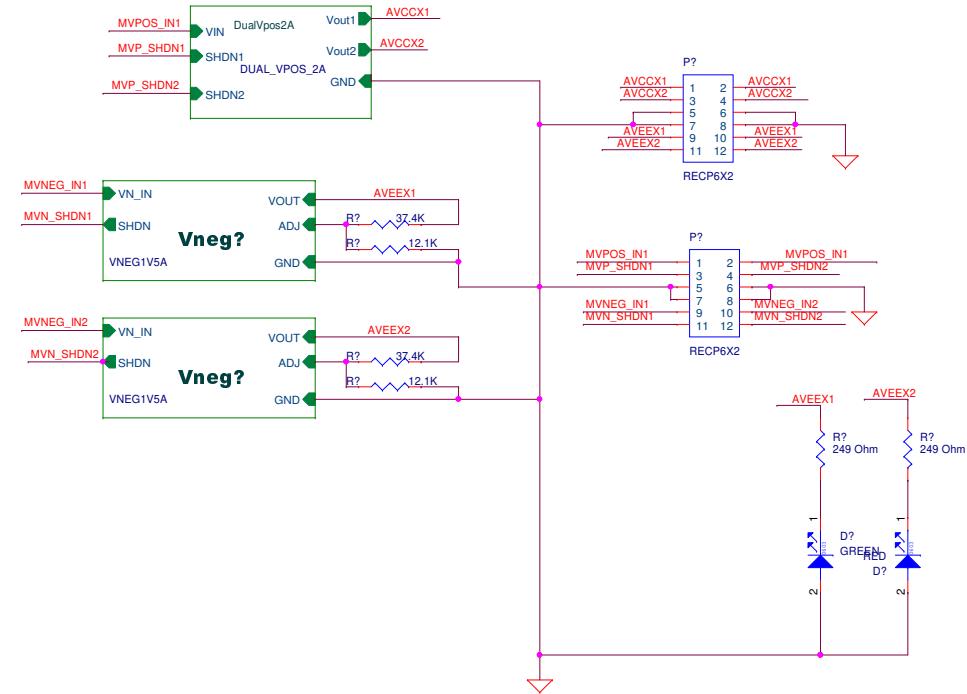
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#### 4. LDO REGULATOR BREAKAWAY MODULES



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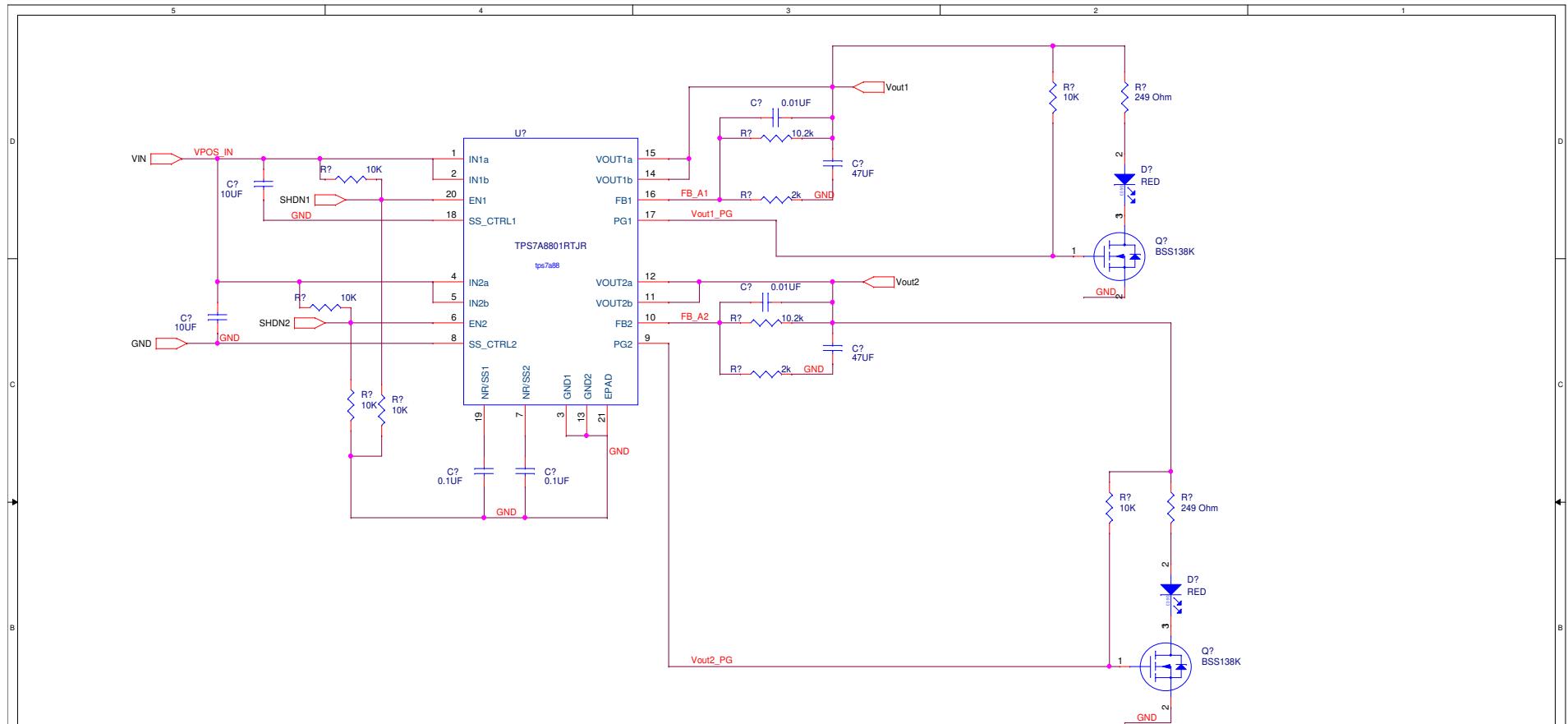
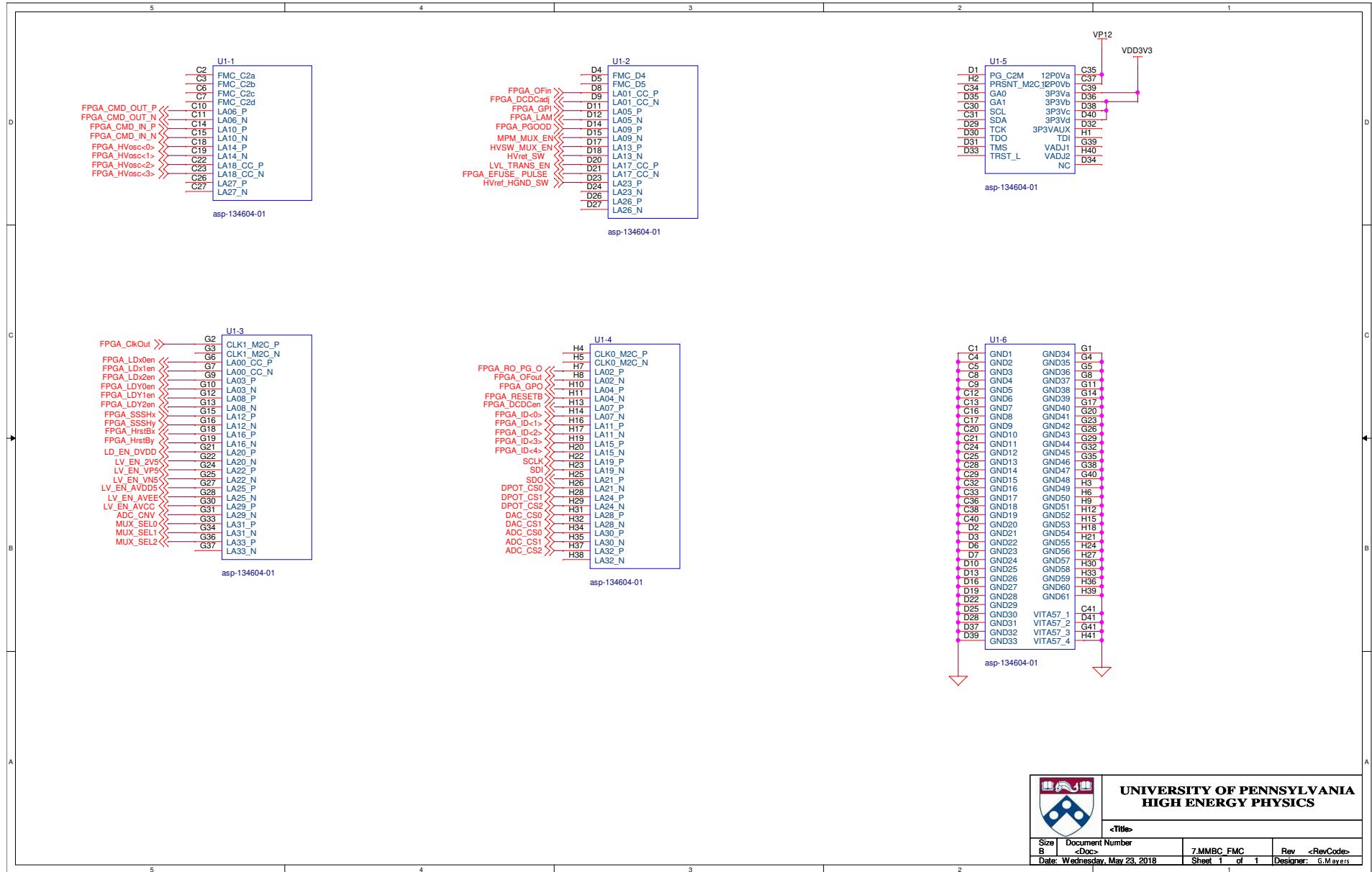
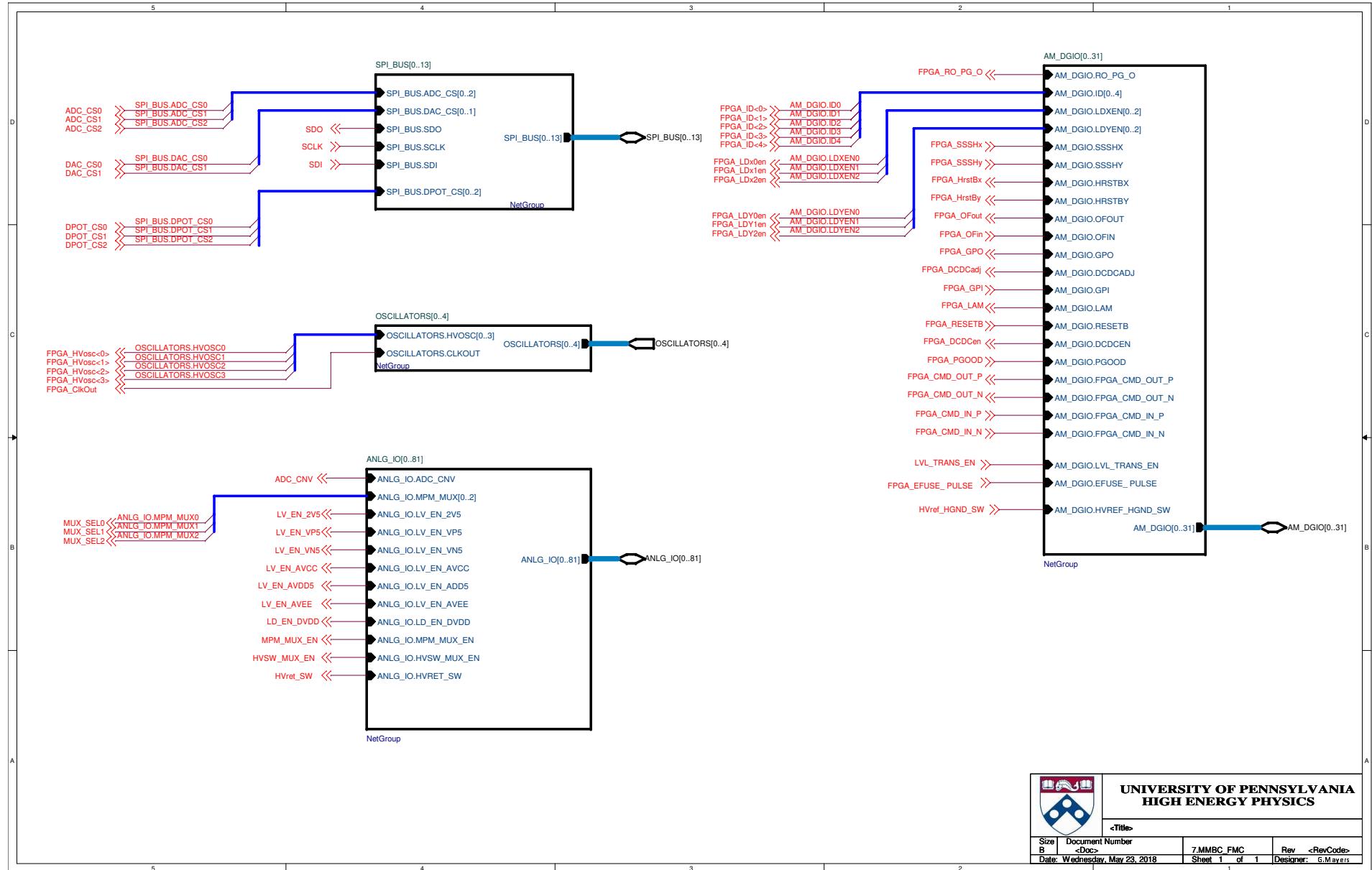


Table 2. Recommended Feedback-Resistor Values

V <sub>OUT(TARGET)</sub> (V)	FEEDBACK RESISTOR VALUES <sup>(1)</sup>	
	R <sub>1</sub> , R <sub>3</sub> (kΩ)	R <sub>2</sub> , R <sub>4</sub> (kΩ)
0.8	Short	Open
1.00	2.55	10.2
1.20	5.9	11.8
1.50	9.31	10.7
1.80	1.87	1.5
1.90	15.8	11.5
2.50	2.43	1.15
3.00	3.16	1.15
3.30	3.57	1.15
5.00	10.5	2

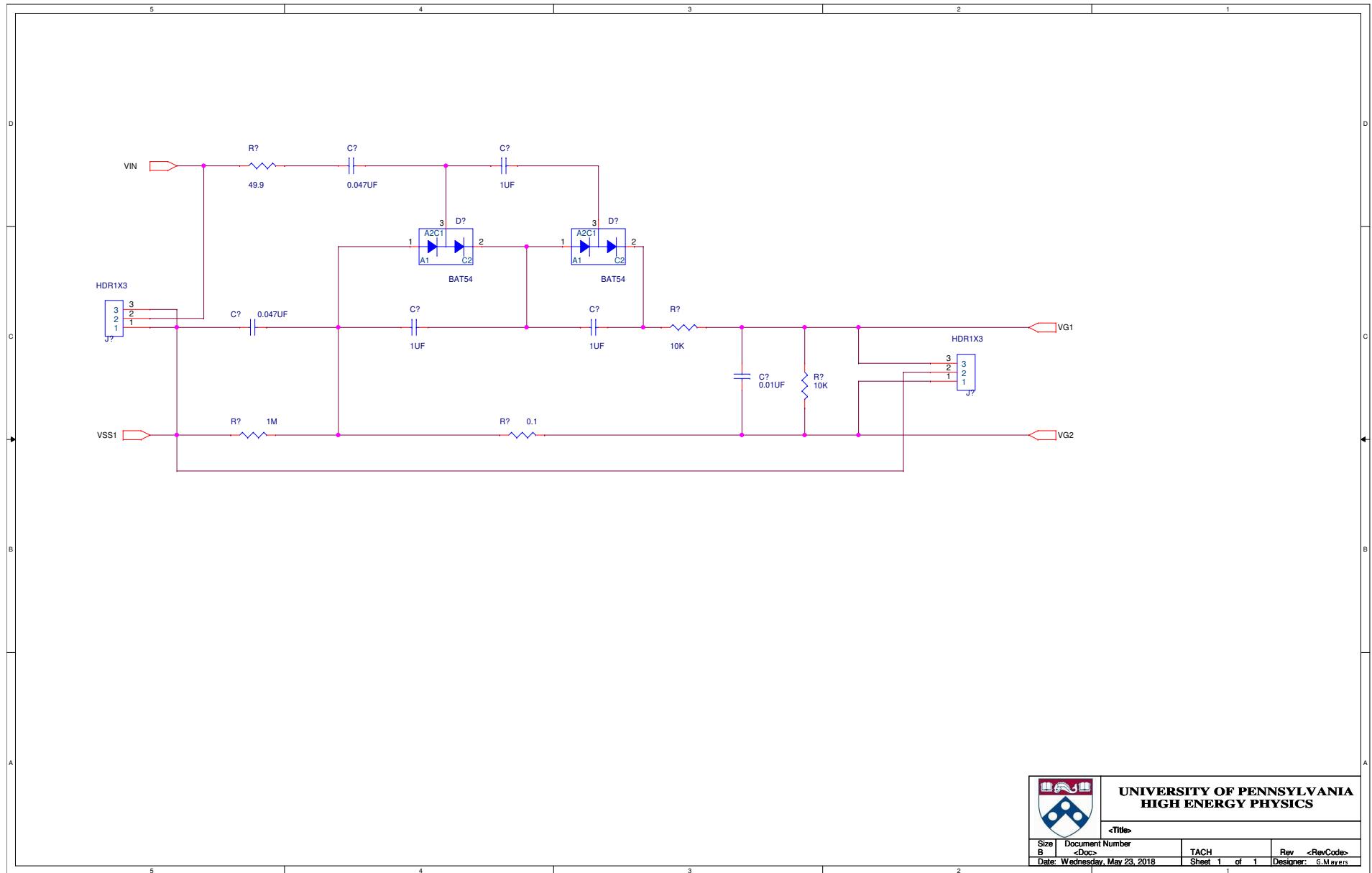


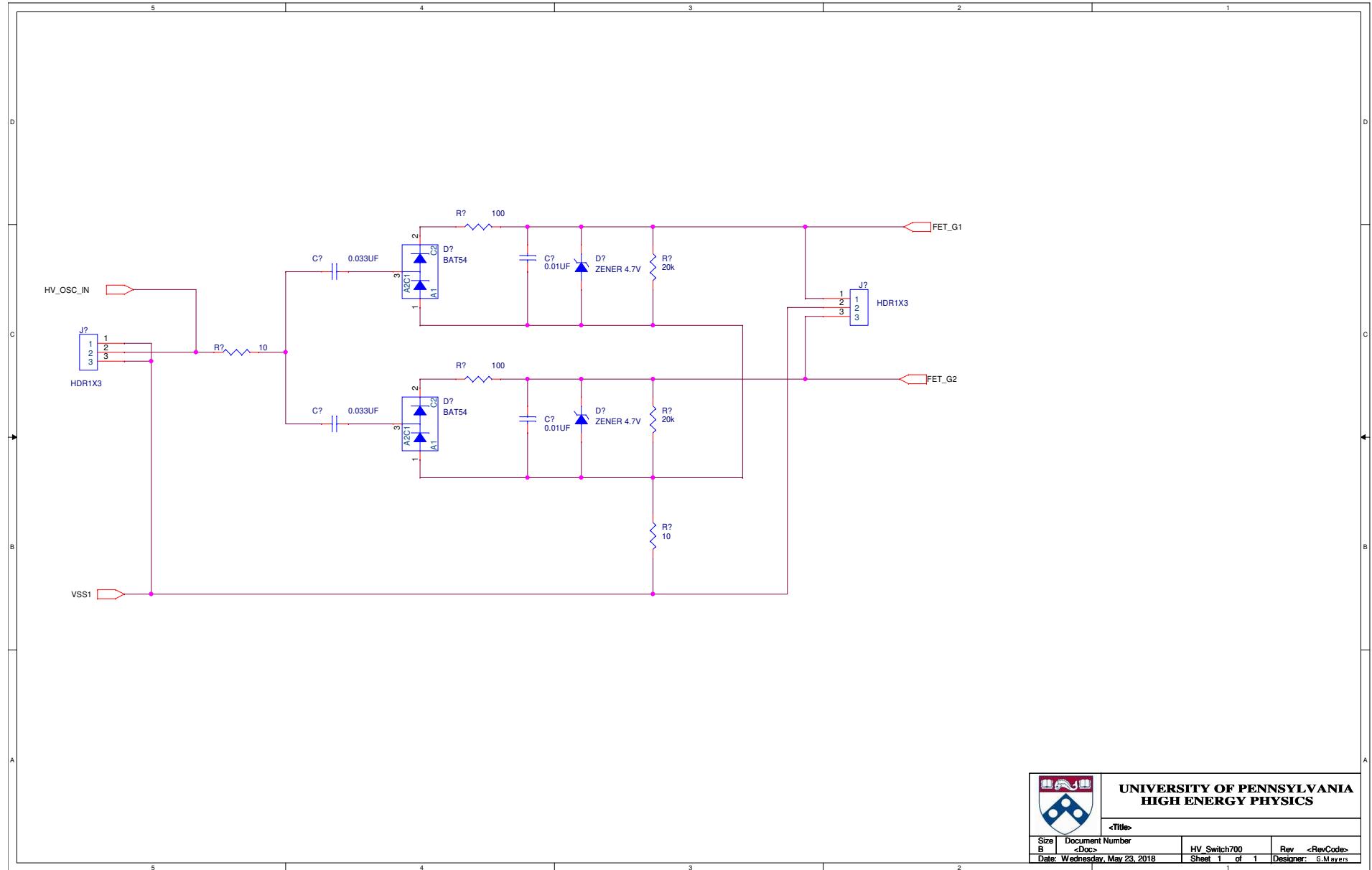


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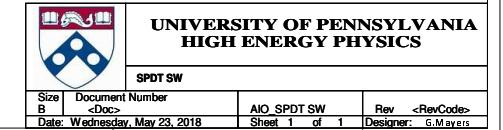
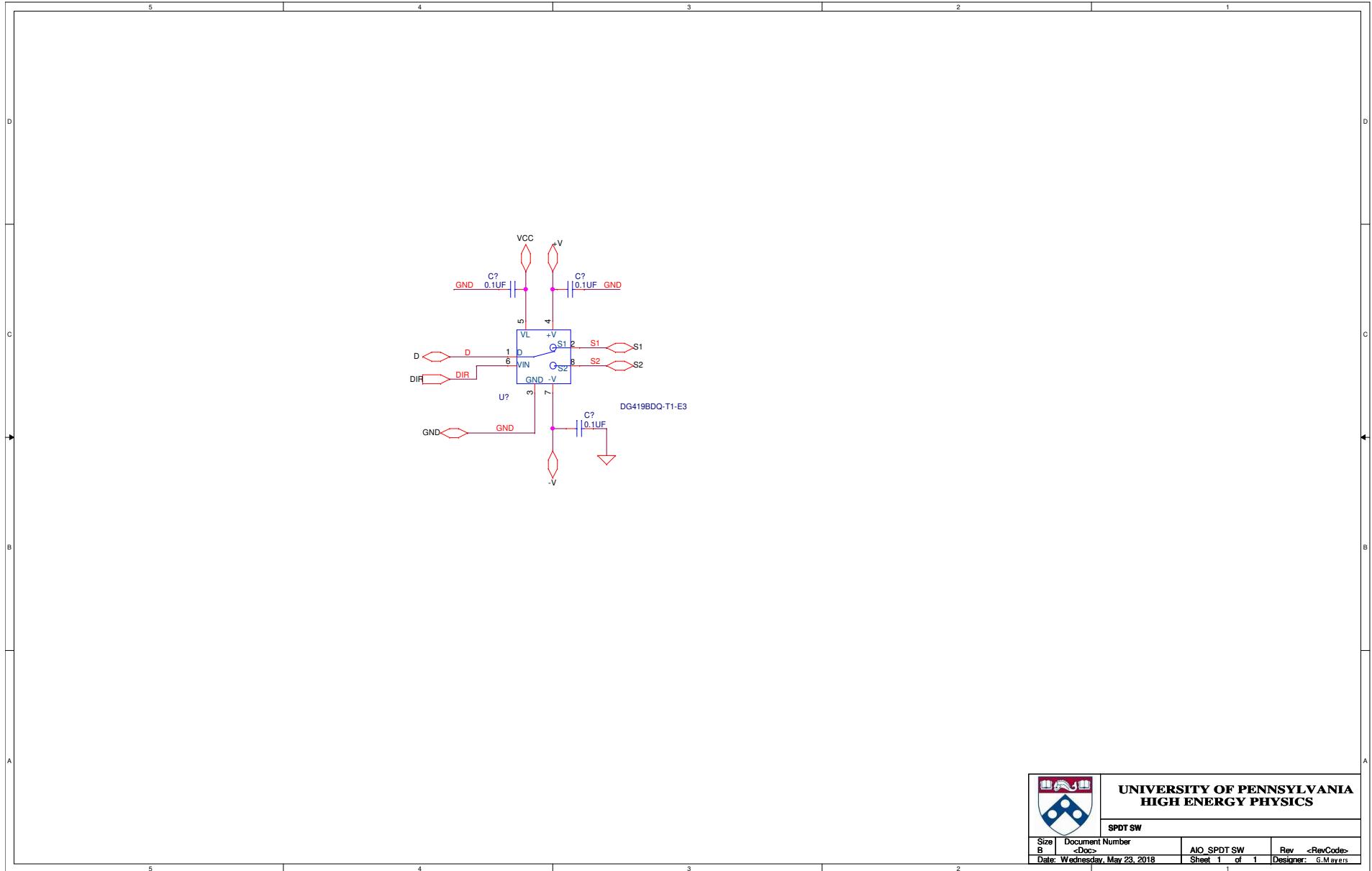
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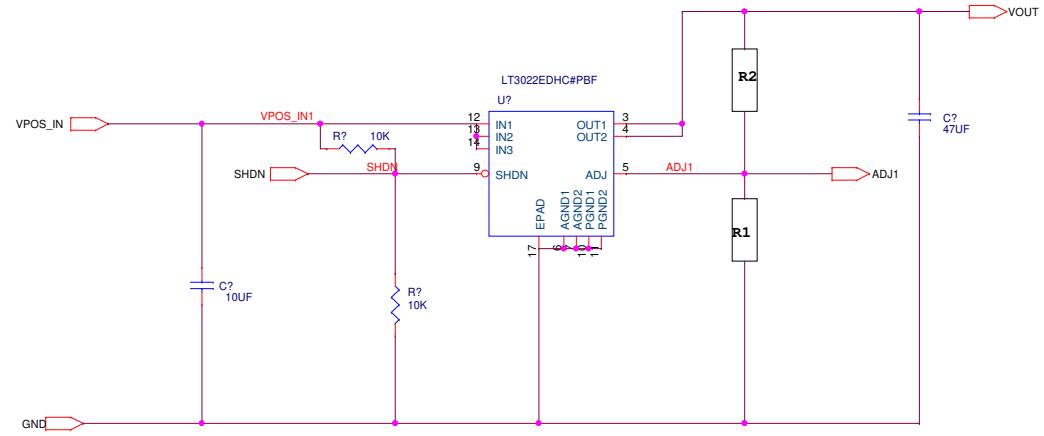




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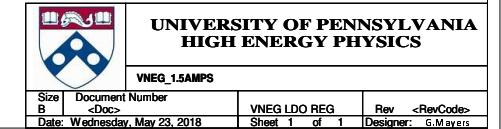
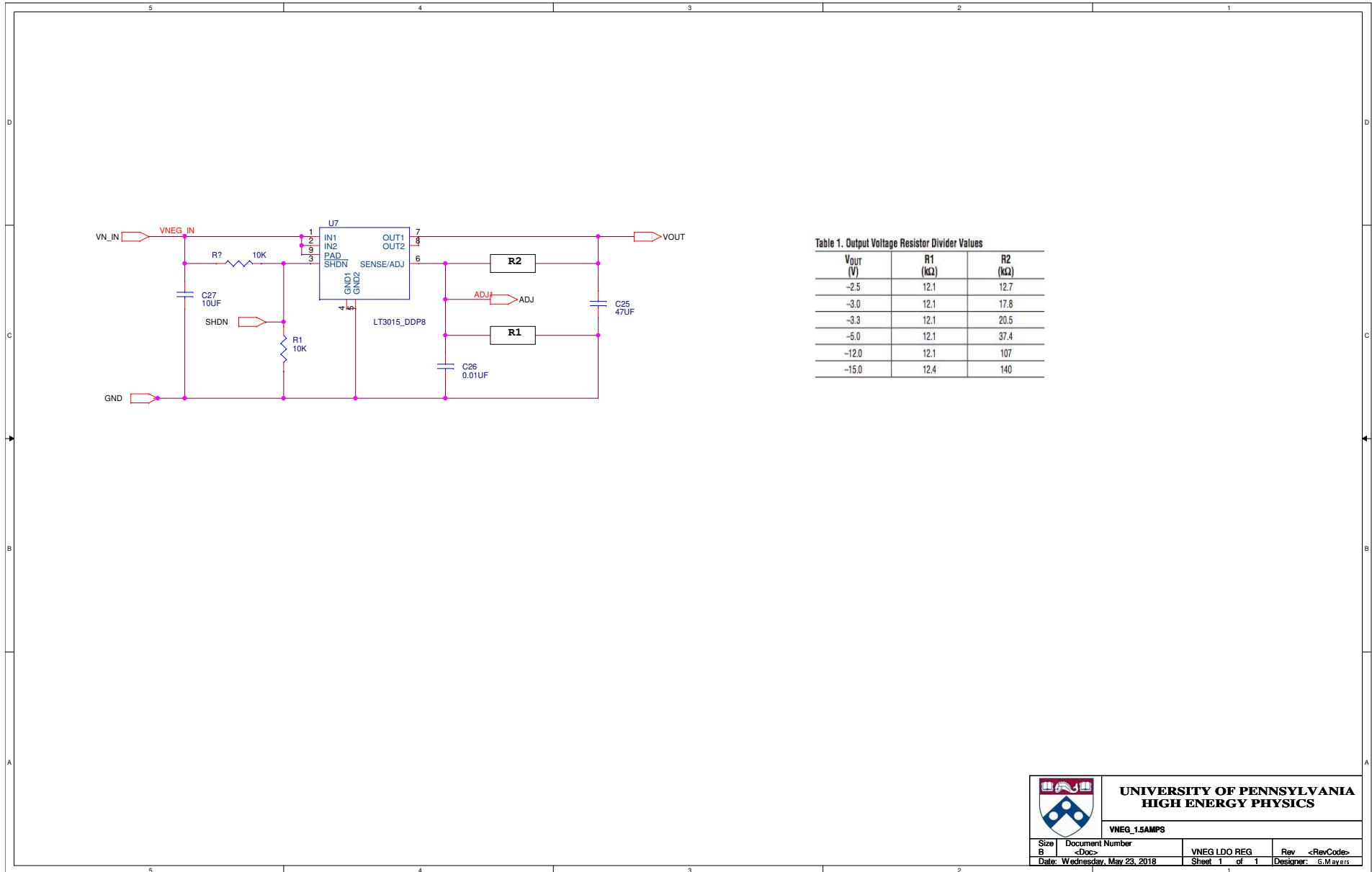
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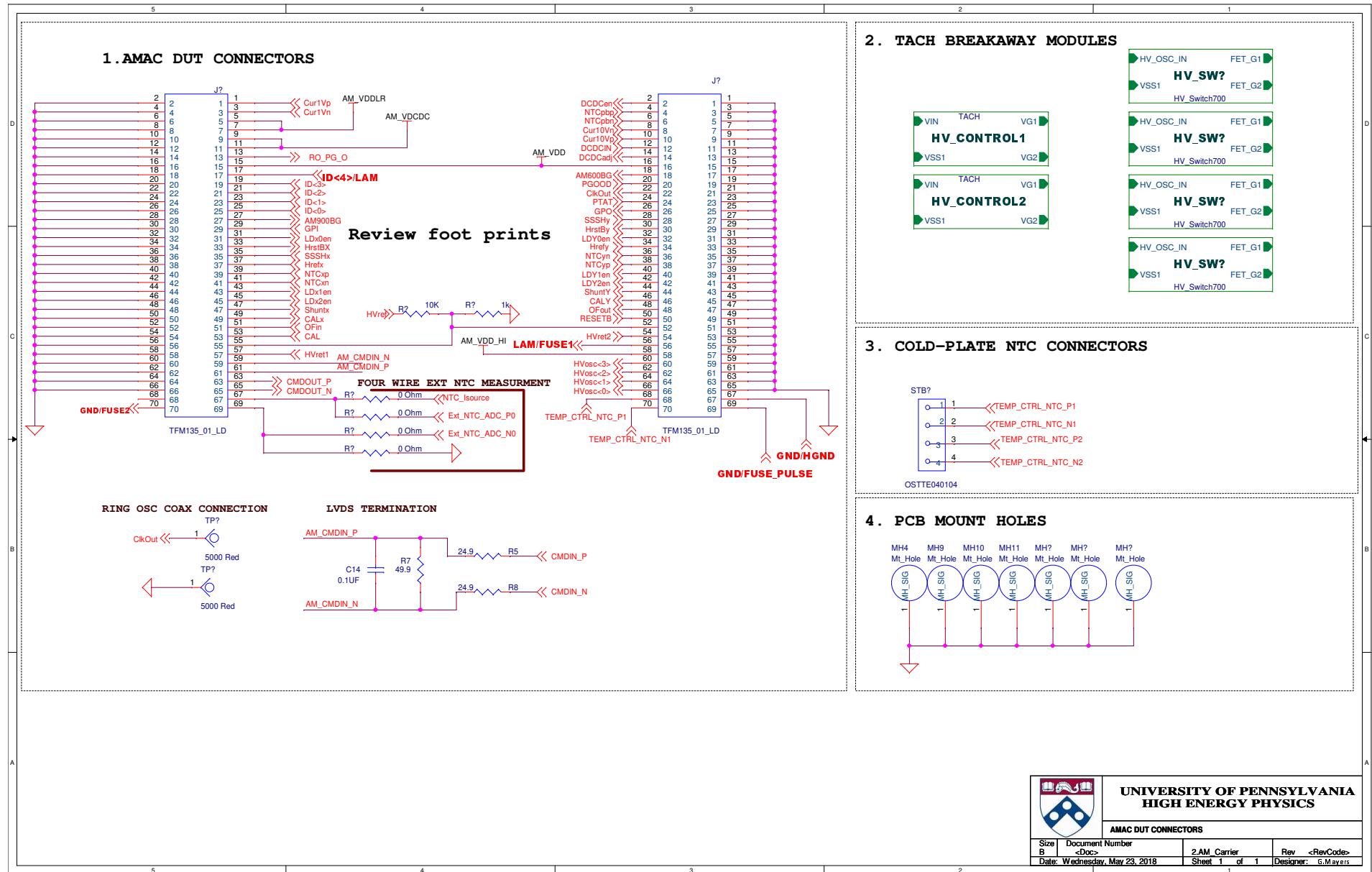


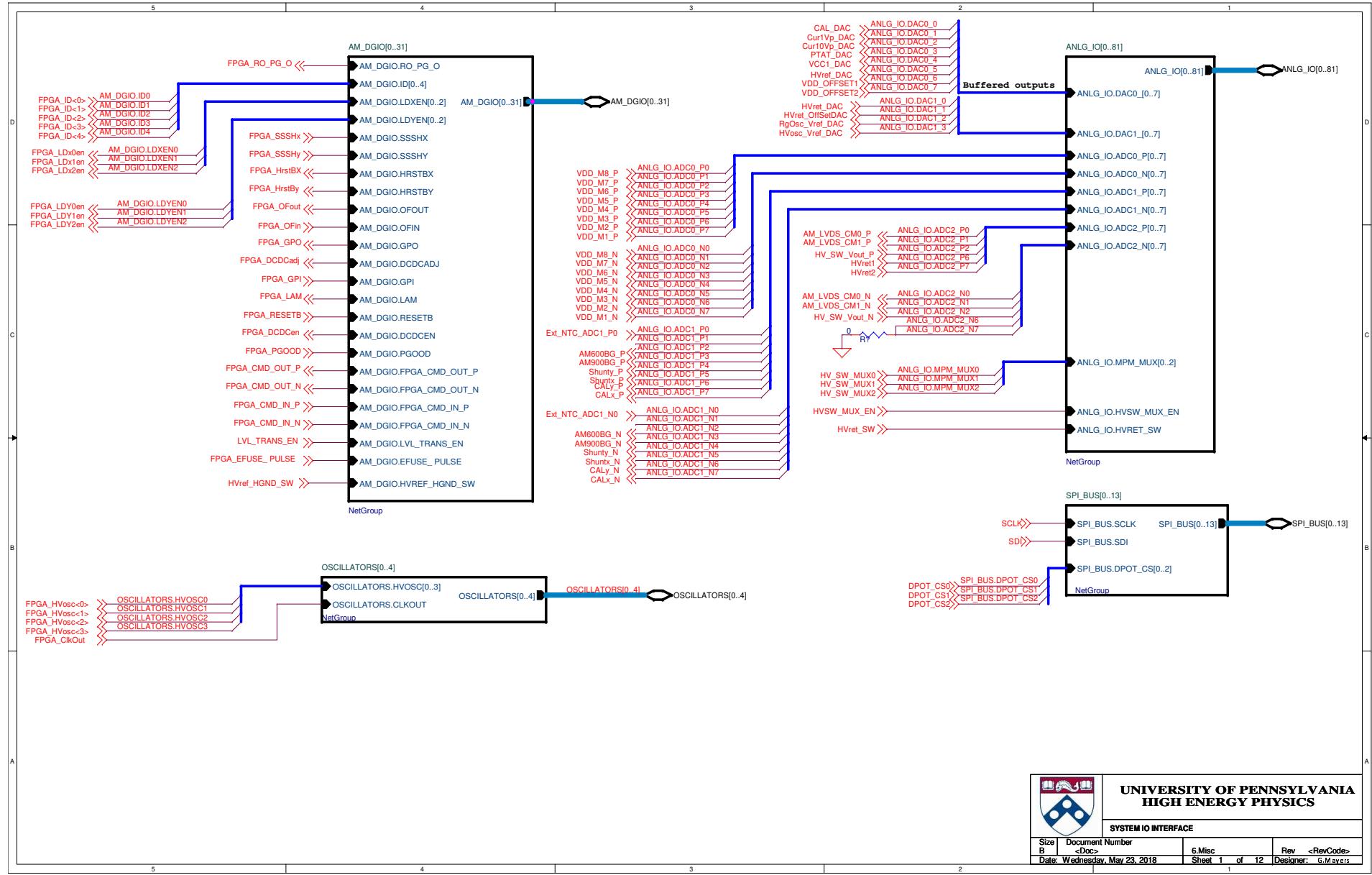


**Table 1**

V <sub>OUT</sub> (V)	R <sub>1</sub> ( $\Omega$ )	R <sub>2</sub> ( $\Omega$ )
0.9	200	698
1.0	187	750
1.2	200	1000
1.5	200	1300
1.8	187	1500
2.5	187	2150
3.3	200	3090





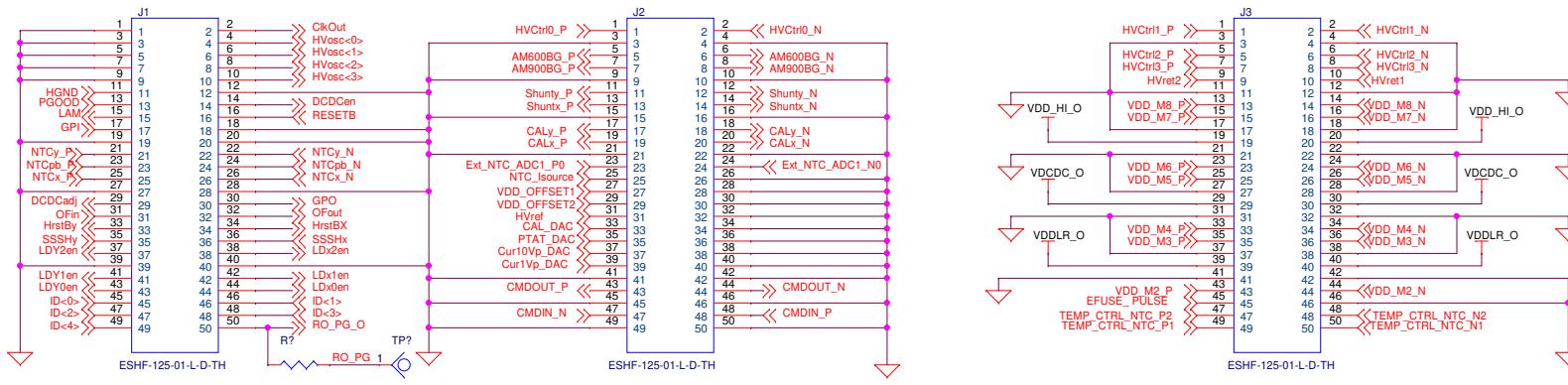


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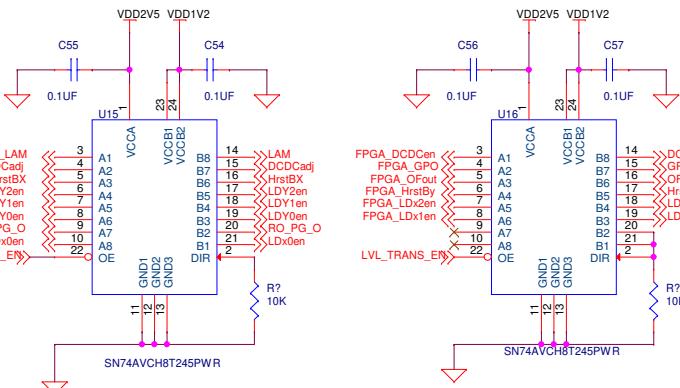
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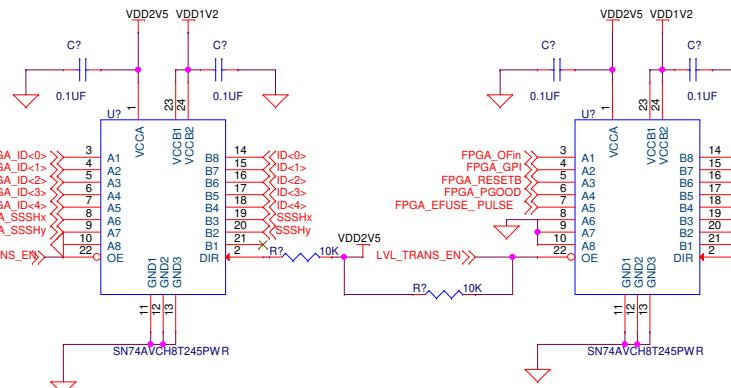
## 1. ATCB Interface Connectors



## 2. AMAC To FPGA DIO Level Translators



## 3. FPGA To AMAC DIO Level Translators

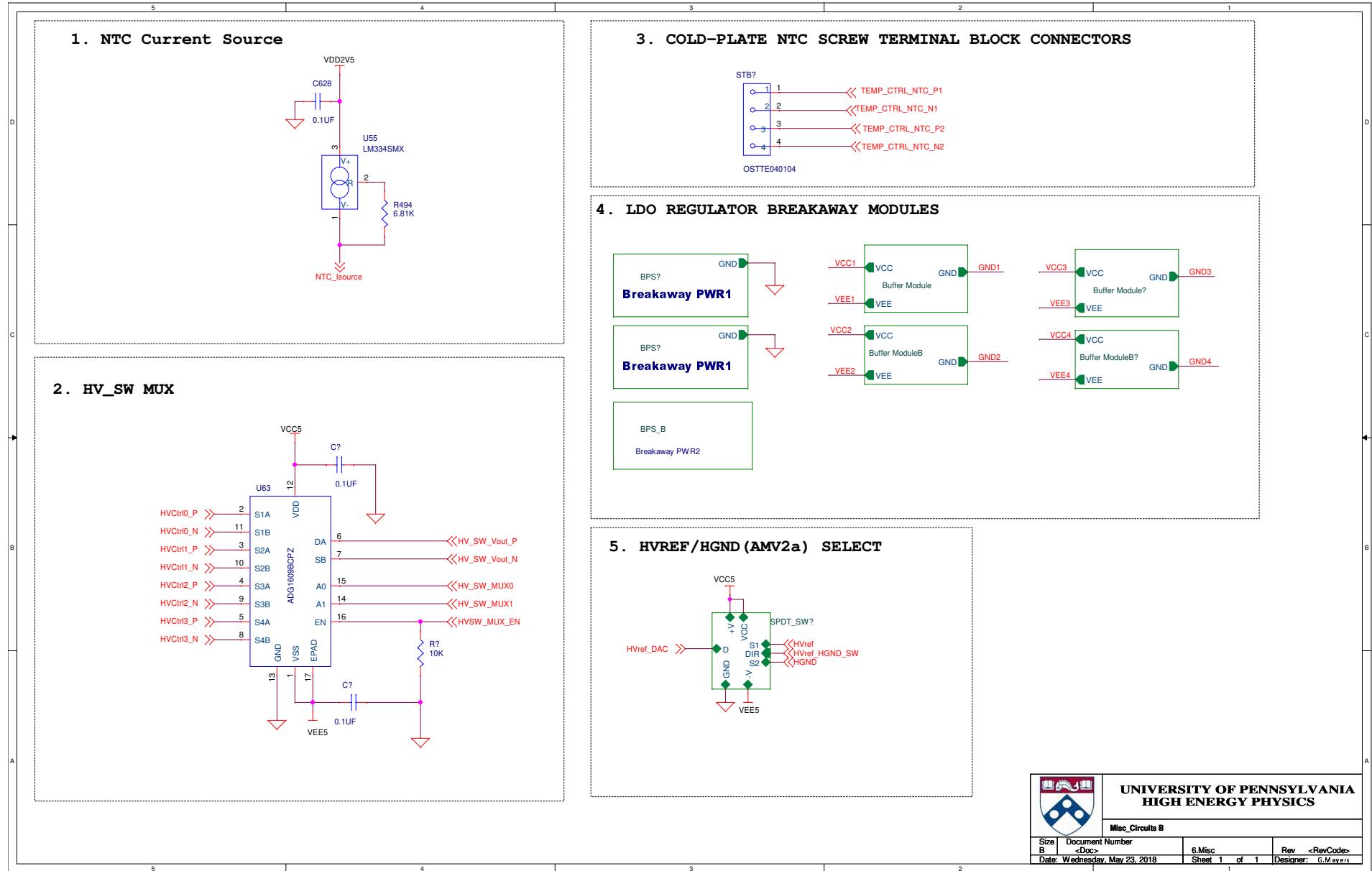


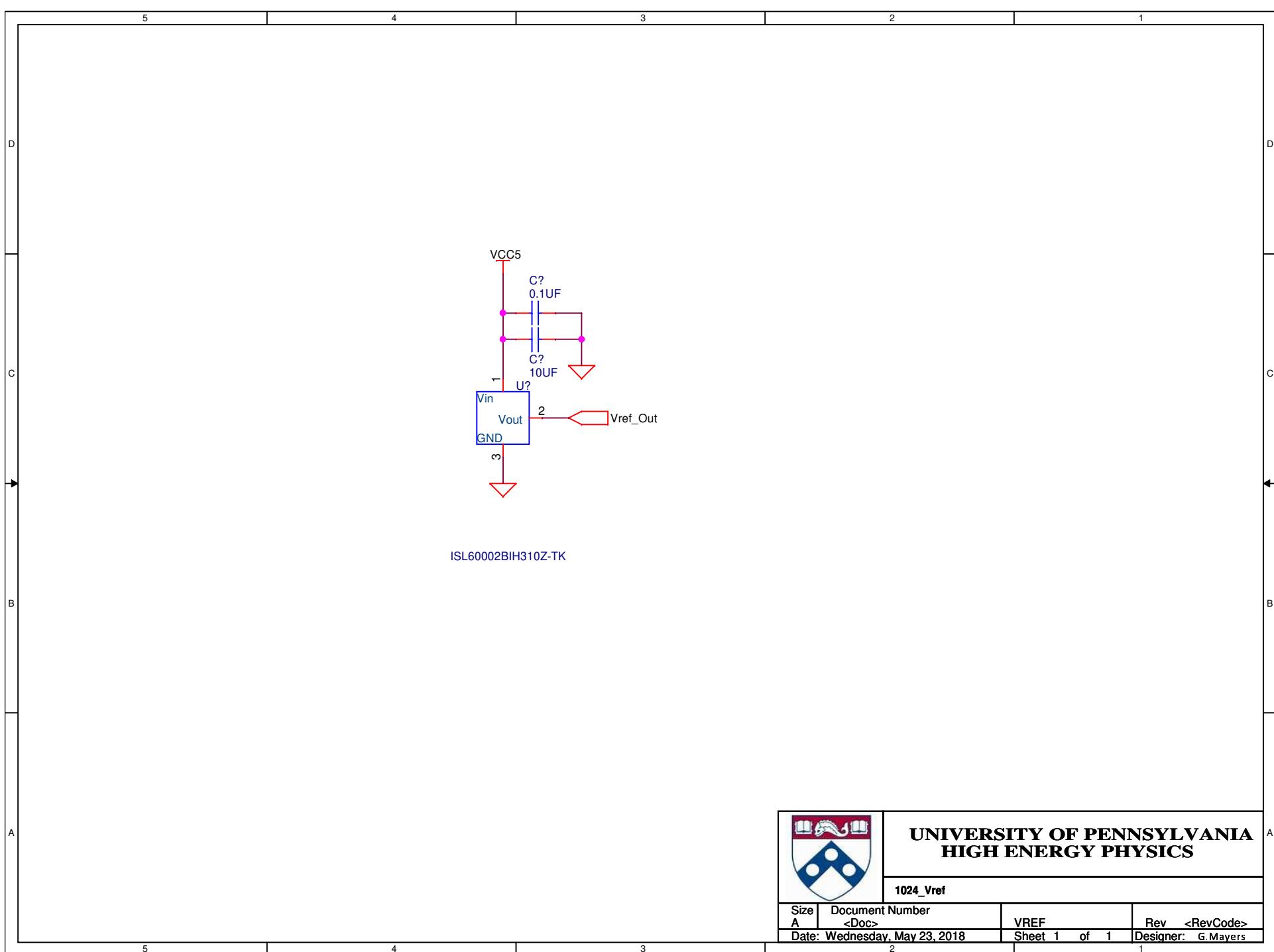
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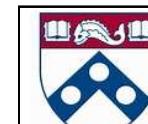
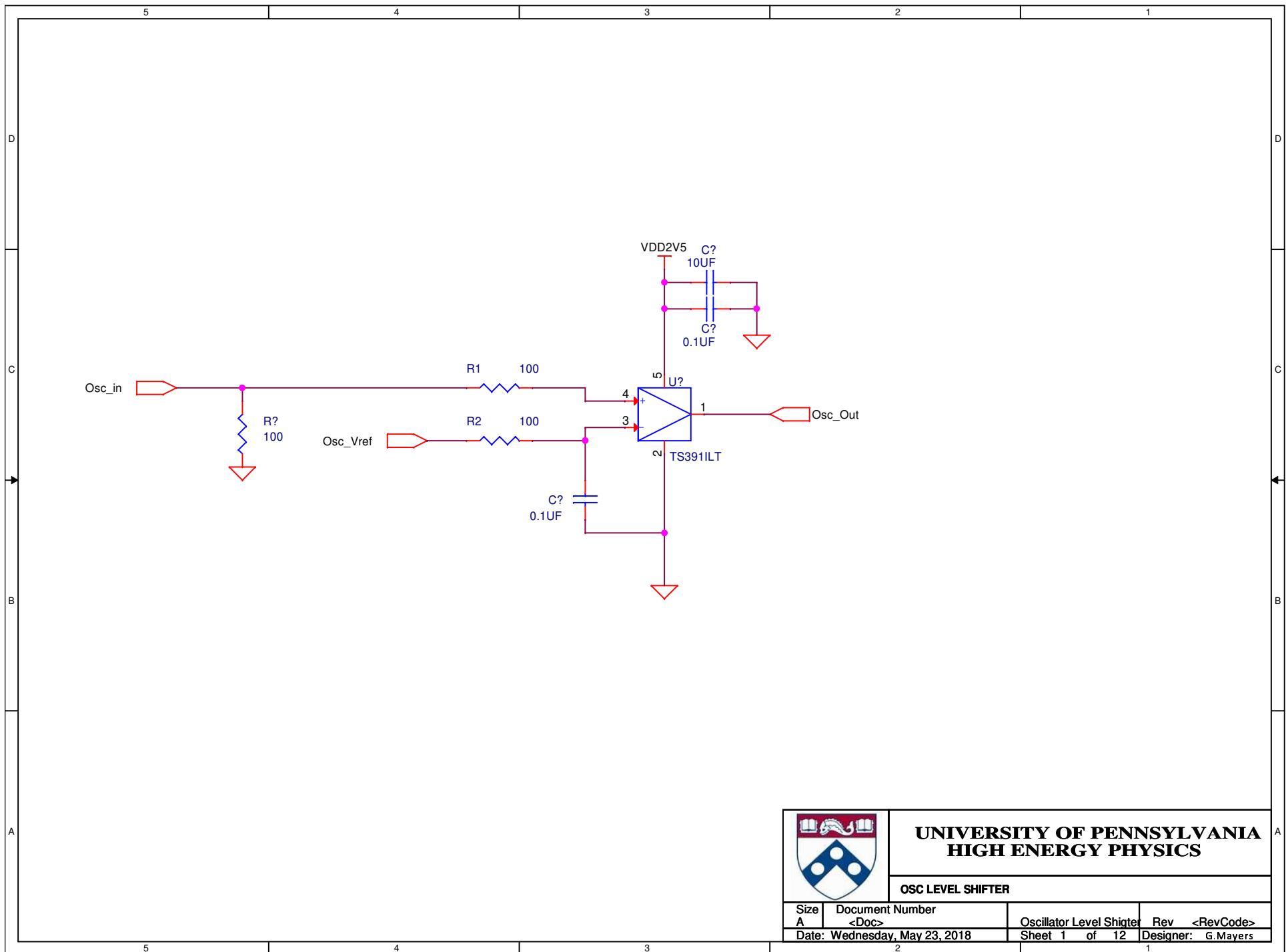
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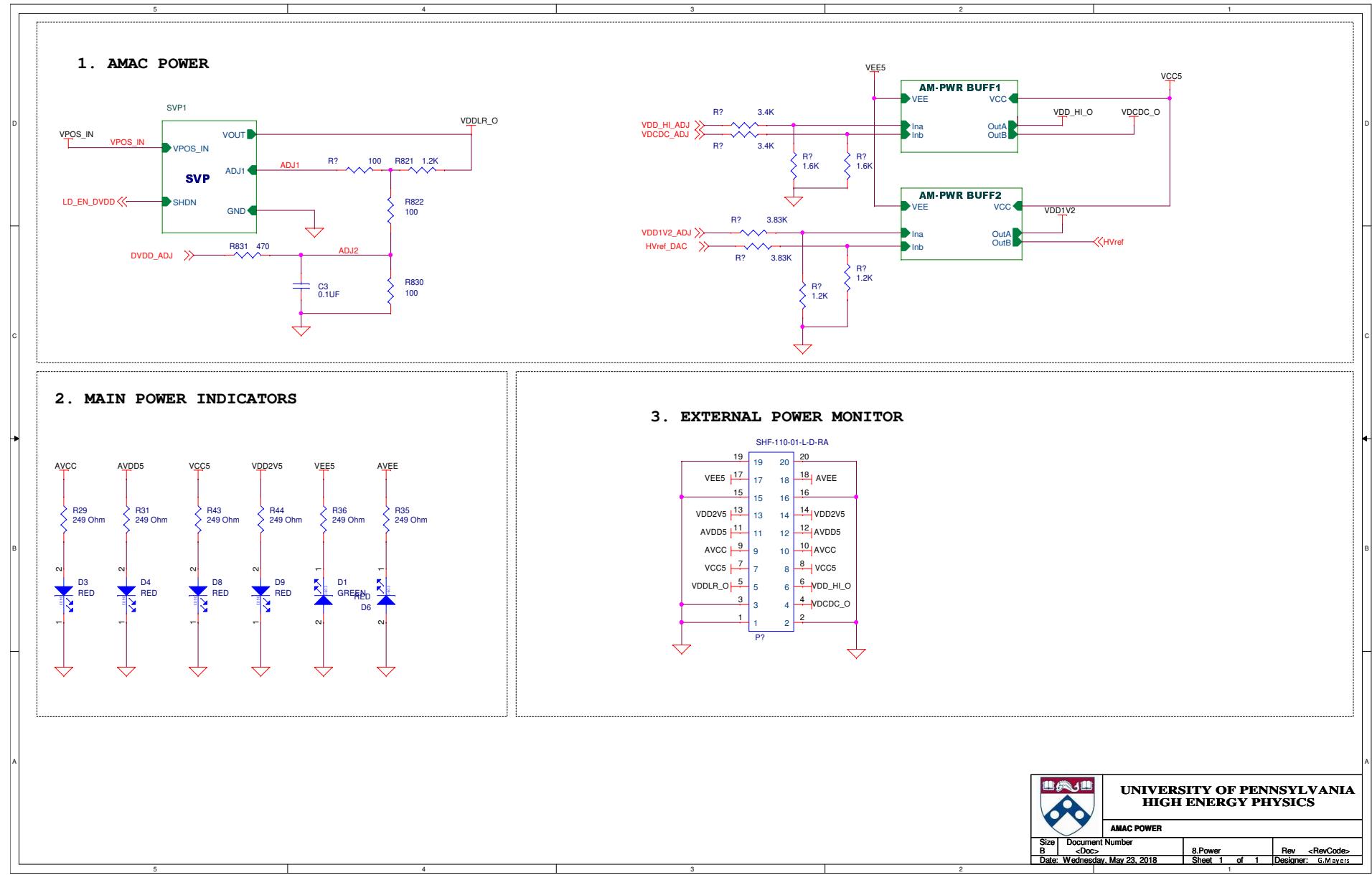


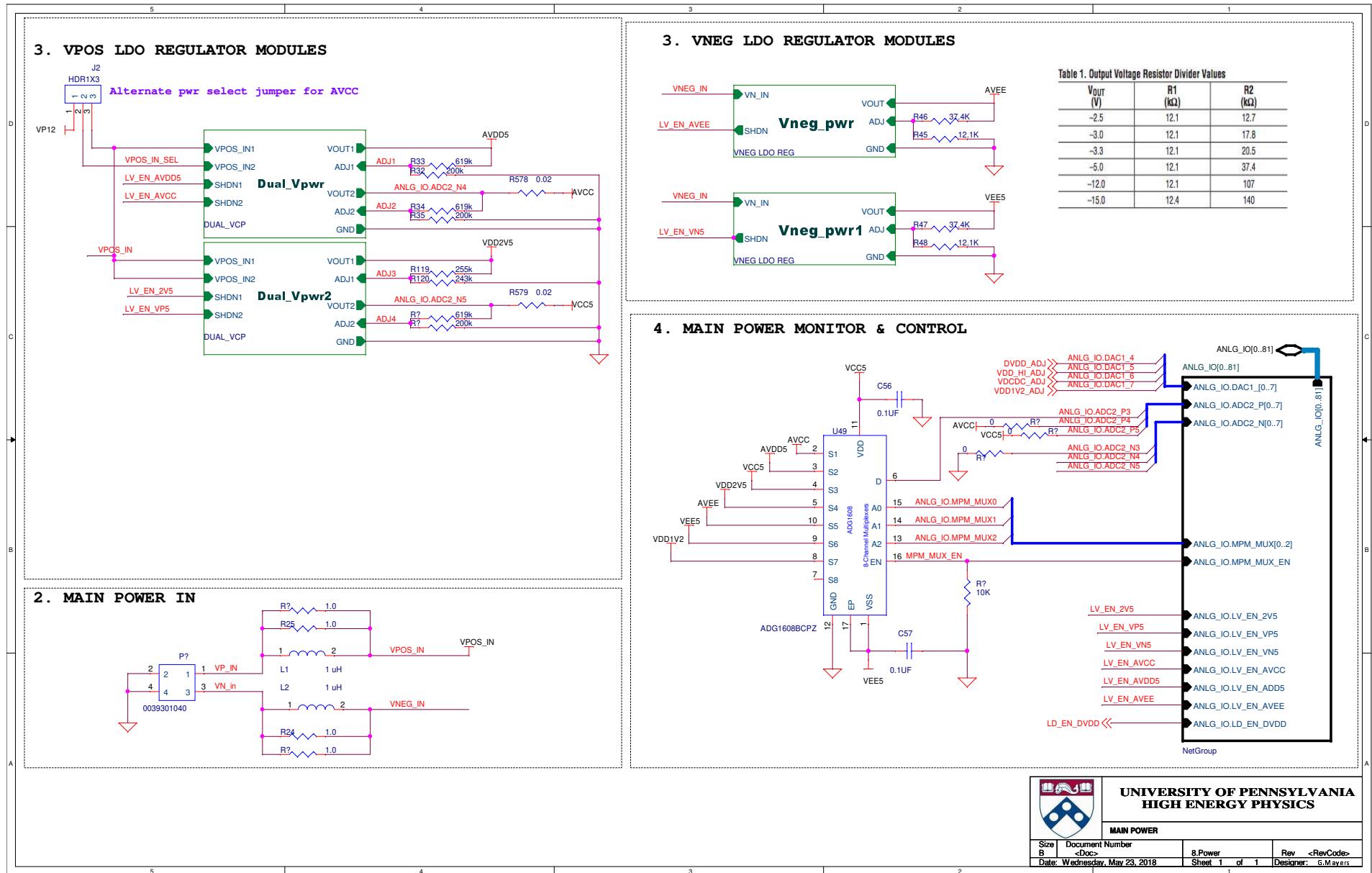




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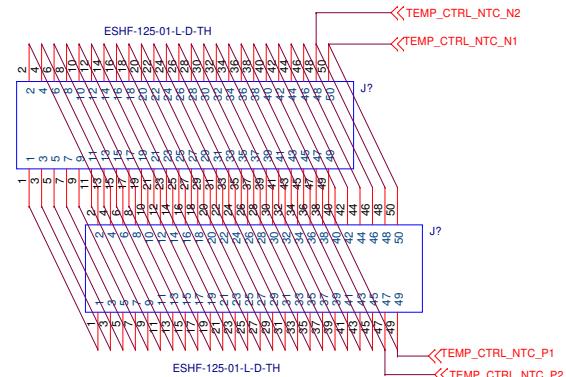
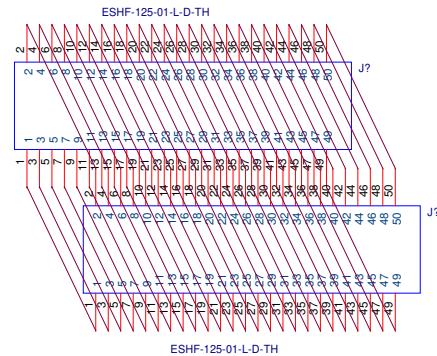
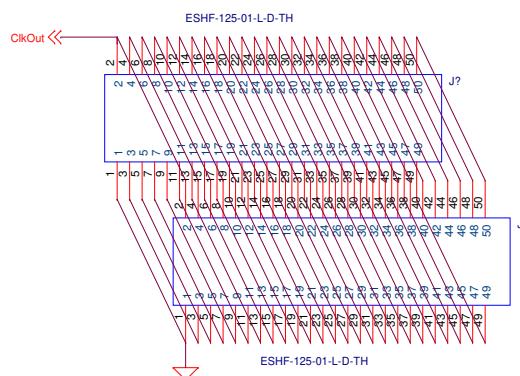




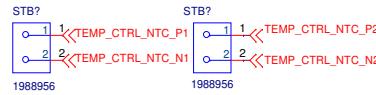
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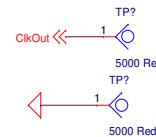
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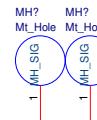
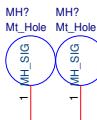
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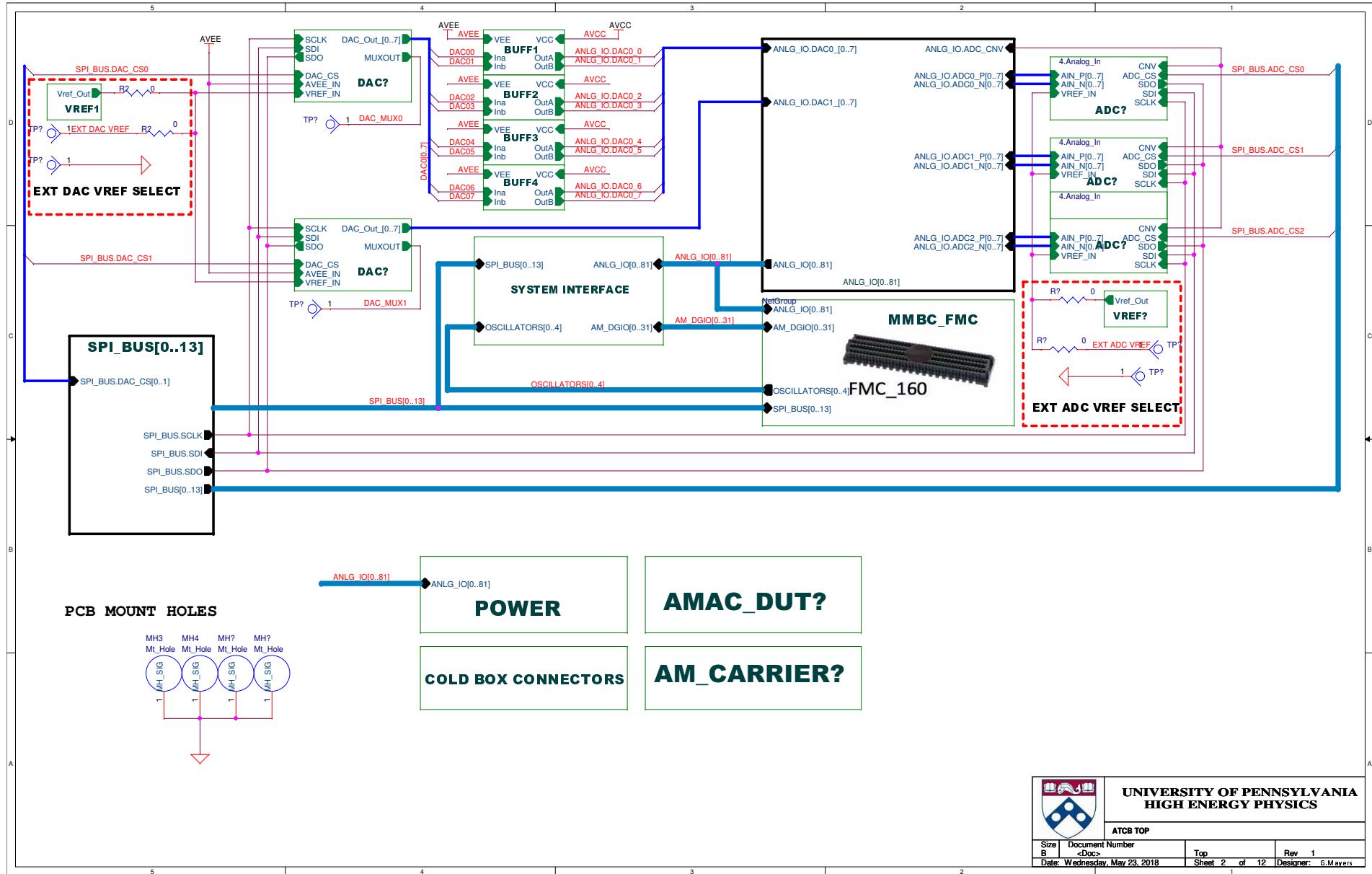


## 3. RING OSC COAX CONNECTION



## 4. PCB MOUNT HOLES

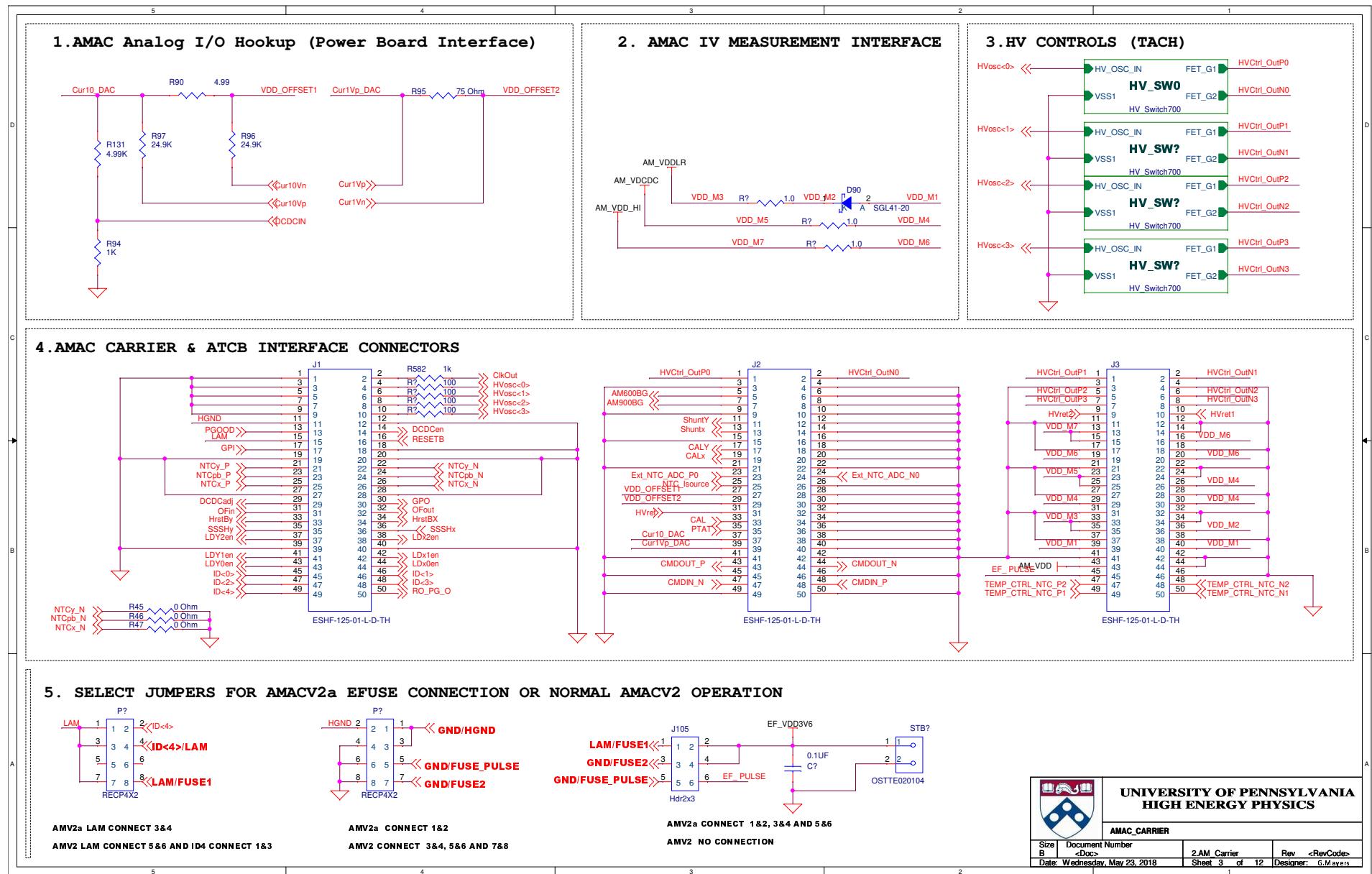


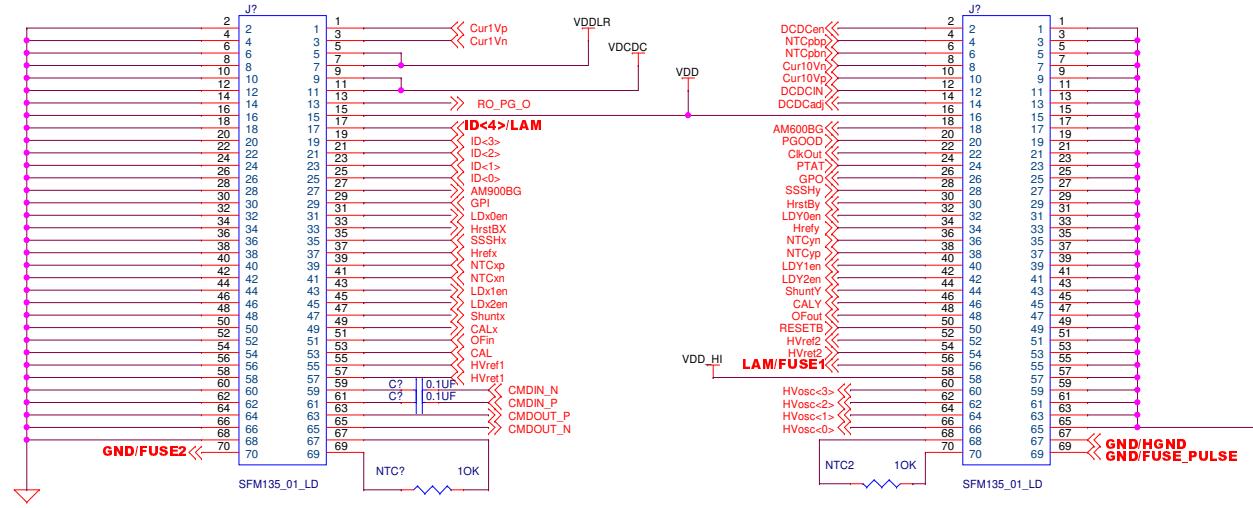


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ATCB TOP

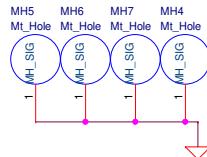
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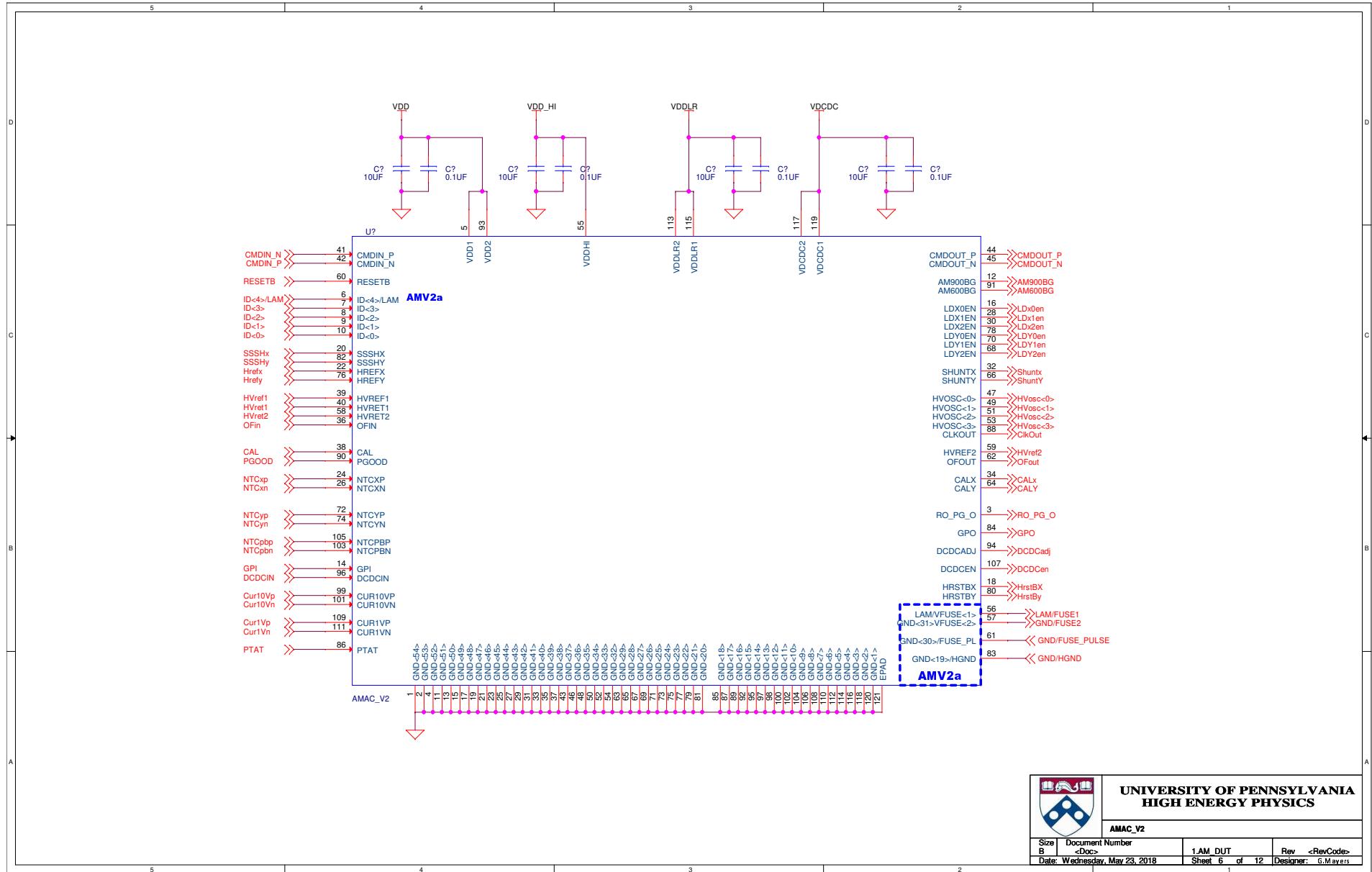


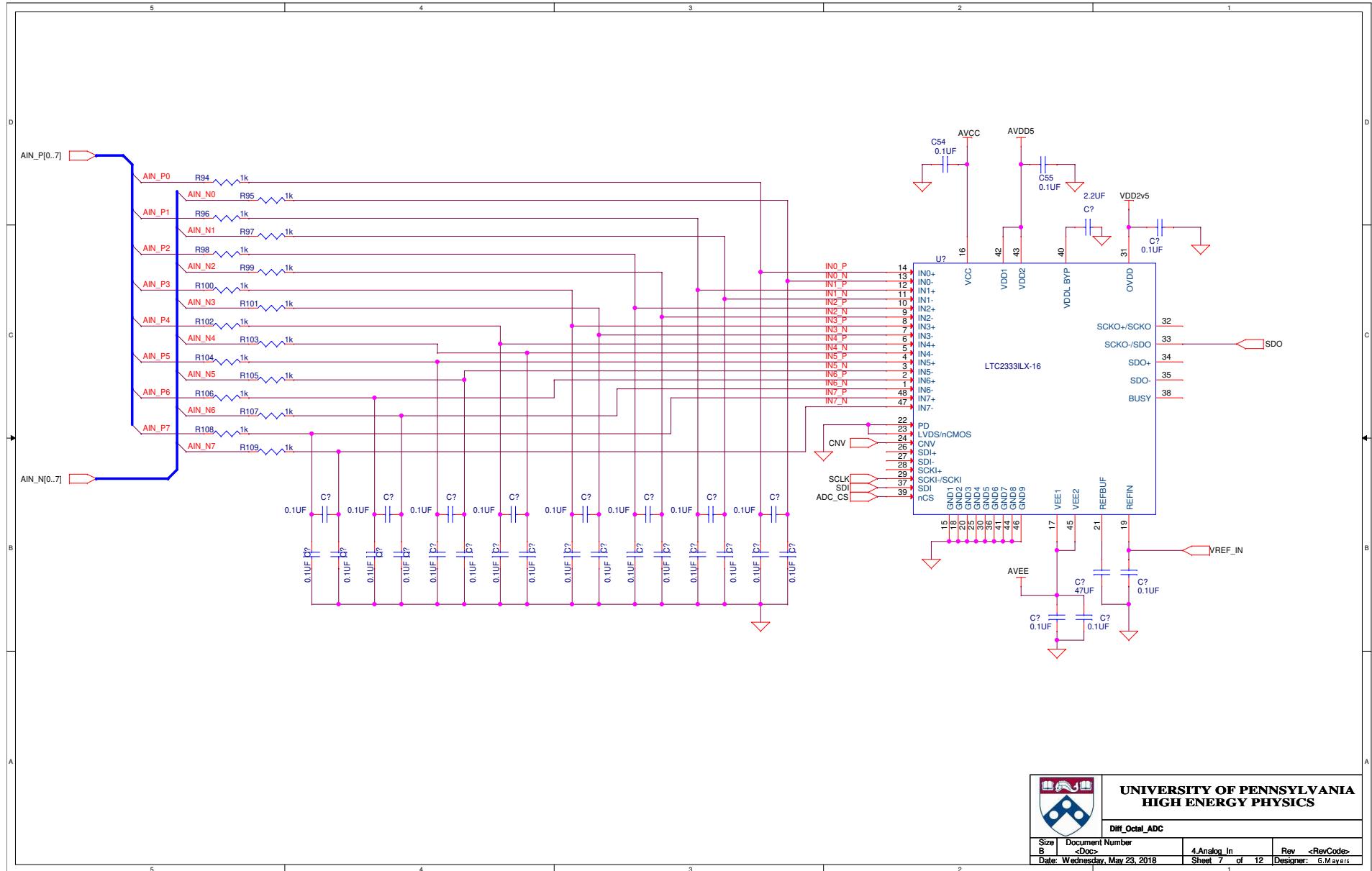
Review foot prints, Vddhv, vdd etc

#### PCB MOUNT HOLES



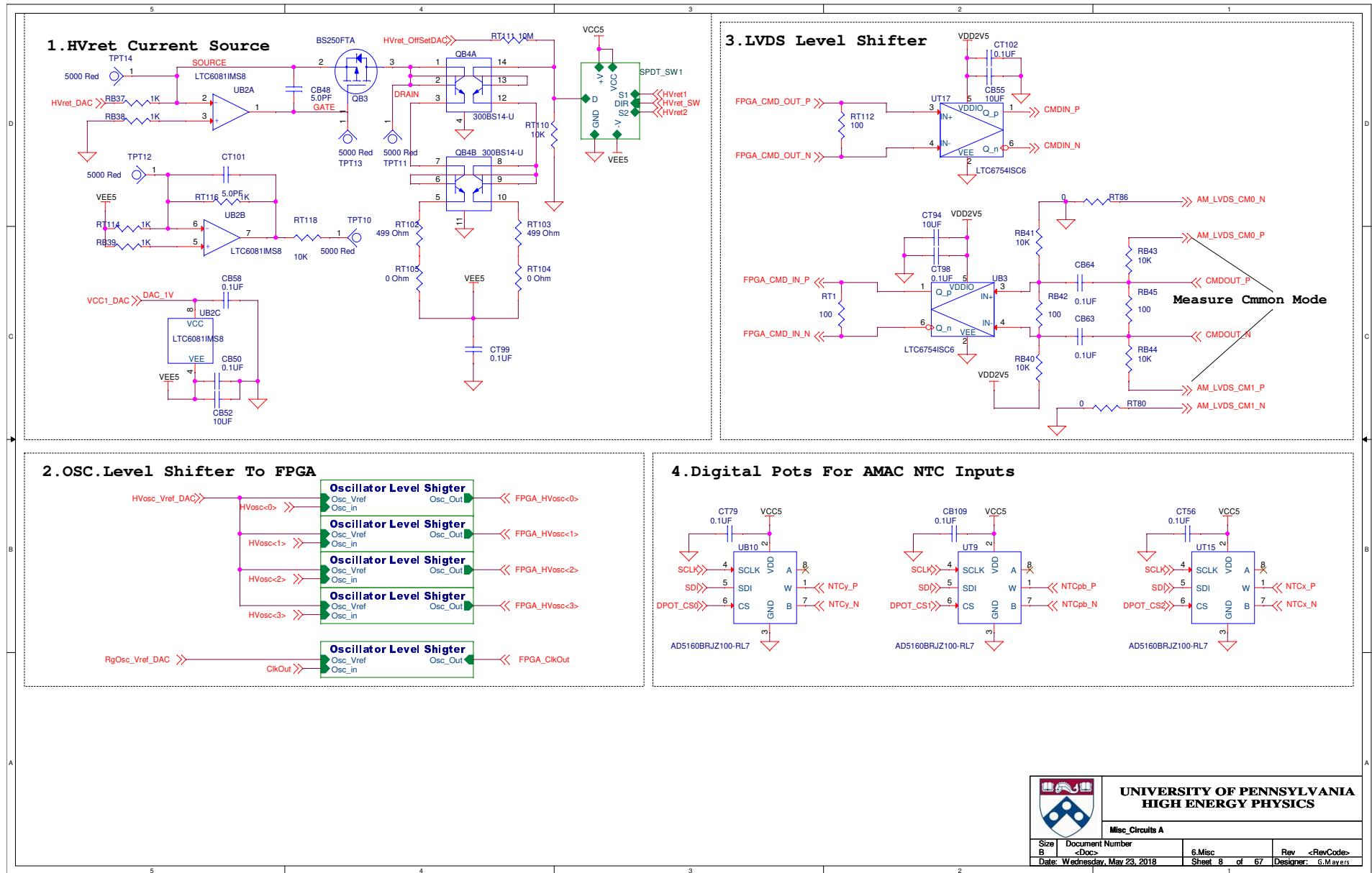
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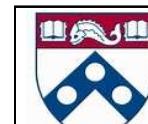
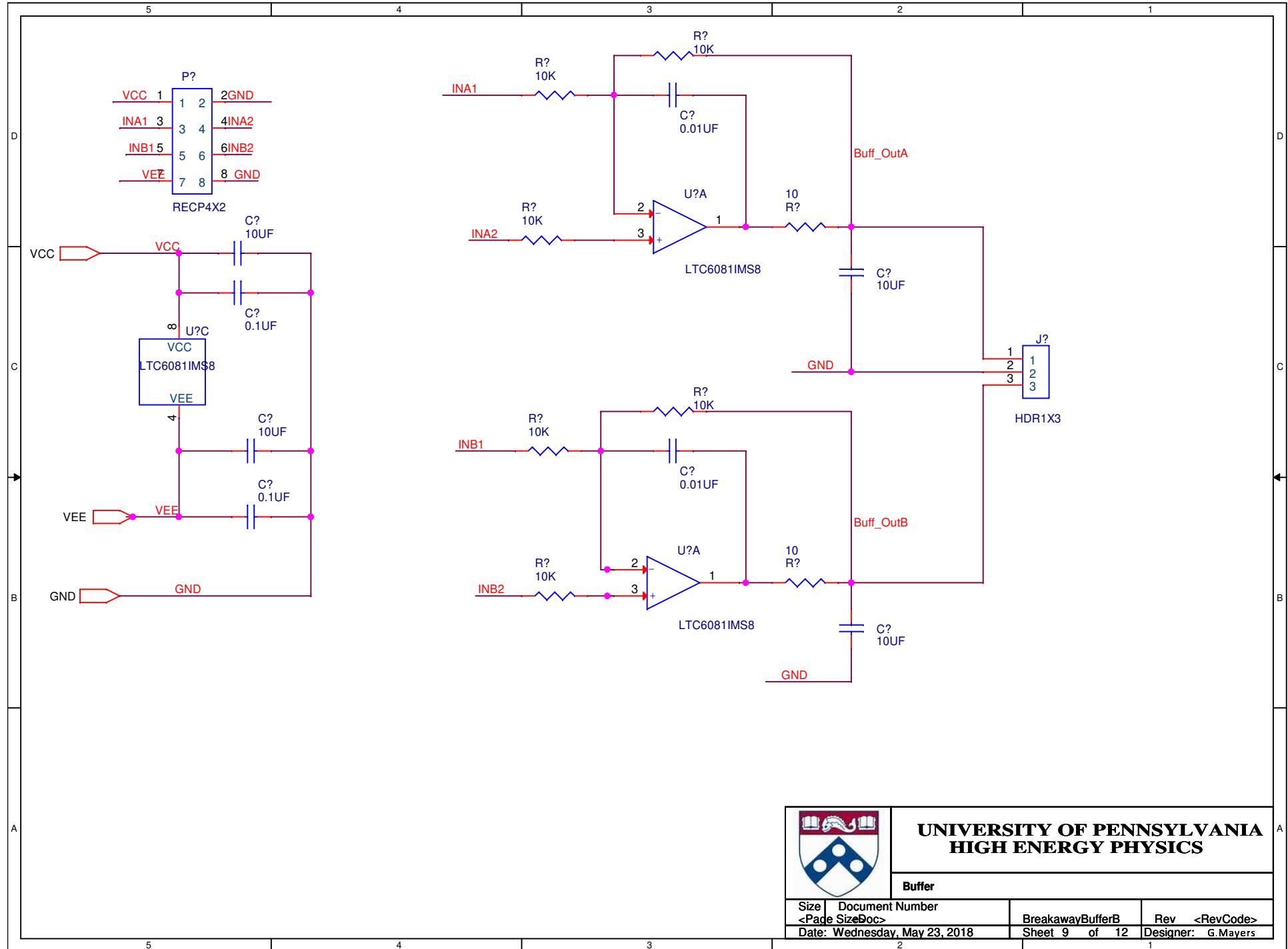




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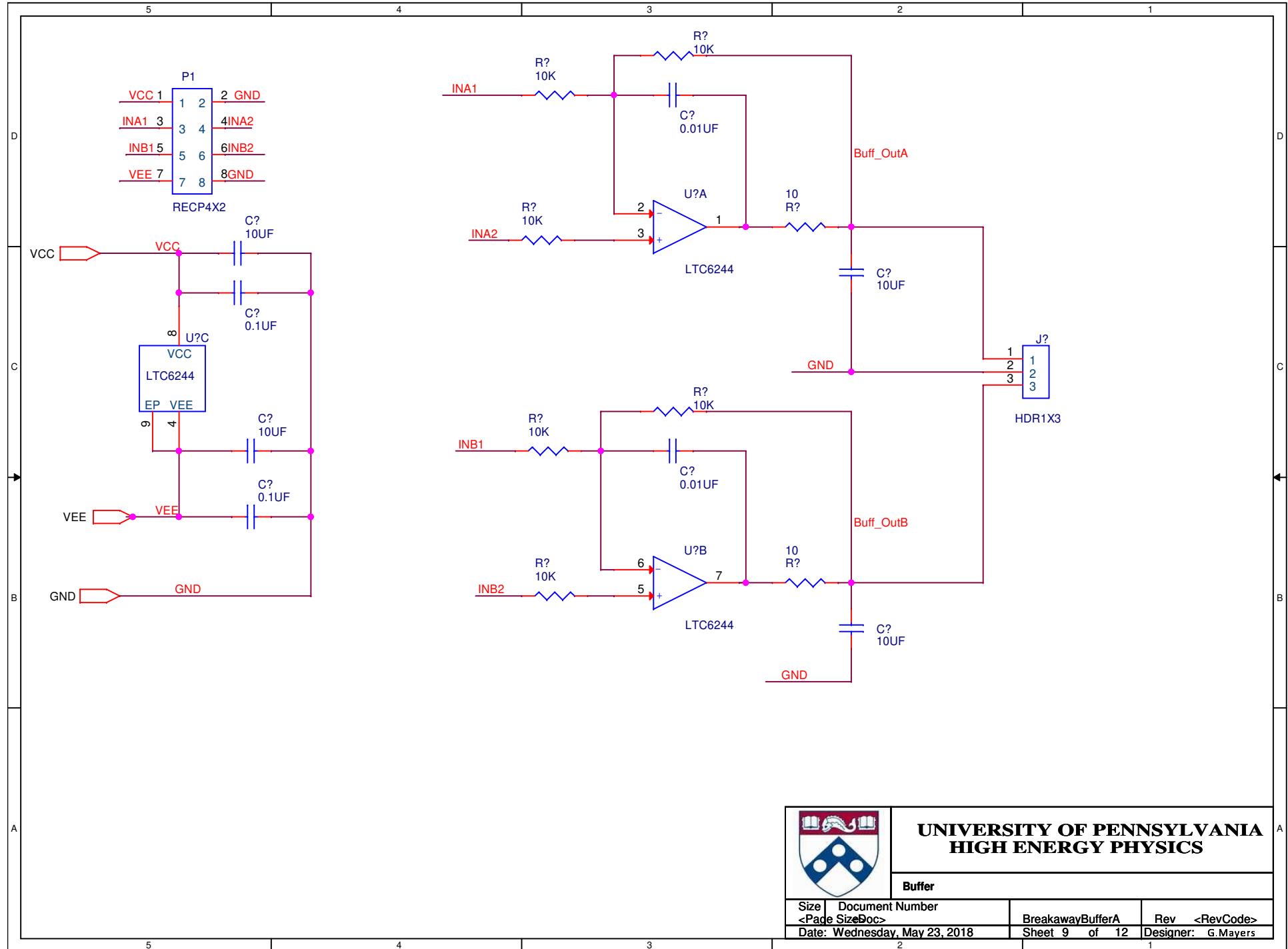




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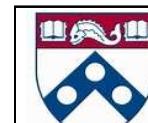
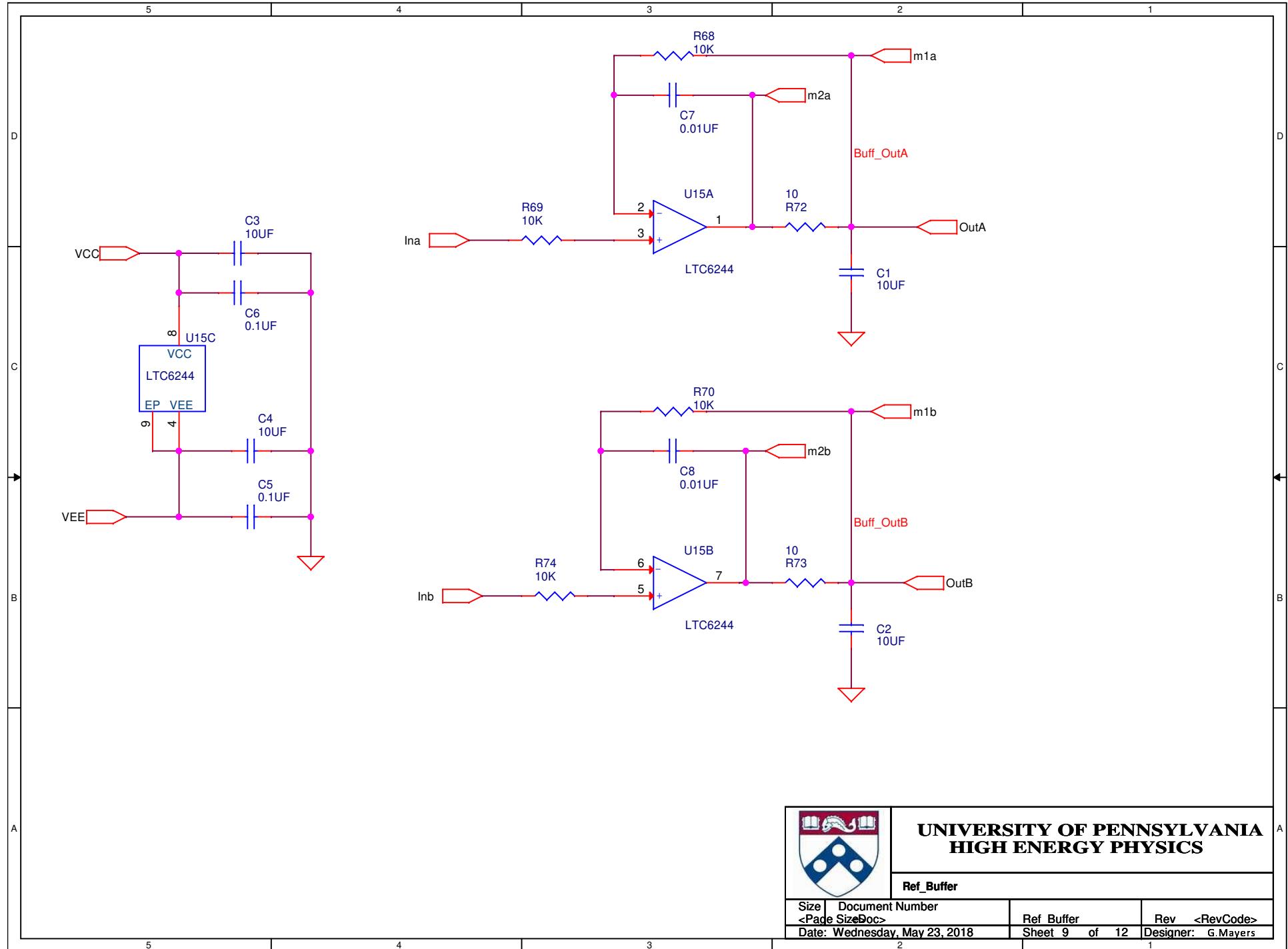
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