

I confirm that I have not received any unauthorized assistance in preparing for or writing this assignment. I acknowledge that a mark of 0 may be assigned for copied work. Deni Rakovic 110081508

1. A) Invalid – size mismatch  
B) Valid  
C) Invalid - size mismatch  
D) Invalid – Invalid memory to memory operation  
E) Invalid – Using movzx when type sizes are the same  
F) Invalid – Using movzx with var as destination  
G) Valid  
H) Invalid – Moving val into seg register not allowed
2. AL = FC , AH = 01
3. AX = 1000h and after final statement AX = 3000h
4. mov edx,var4 ; EDX = 00000001  
movzx edx,var2 ; EDX = 00001000  
mov edx,[var4+4] ; EDX = 00000002  
movsx edx,var1 ; EDX = FFFFFFFC
5. 1) INC val1  
2) SUB eax, val3  
3) mov ax, val2  
sub ax, val4  
mov val2, ax ;  
4) CF = 0, SF = 1 since MSB is 1  
5) CF = 0, SF = 1 since MSB is 1
6. A) CF = 1 SF = 0 ZF = 1 OF = 0  
B) CF = 0 SF = 1 ZF = 0 OF = 1  
C) CF = 0 SF = 1 ZF = 0 OF = 0
7. 1) 1  
2) mov dx, WORD PTR [myBytes]  
3) mov al, BYTE PTR [myWords + 1]  
4) mov eax, DWORD PTR [myBytes]
8. mov esi,OFFSET myBytes                      ESI = address of myBytes  
mov al,[esi]                                      AL = 10h  
mov al,[esi+3]                                   AL = 40h  
mov esi,OFFSET myWords + 2                  ESI = address of myWords + 2  
mov ax,[esi]                                      AX = 3B8Ah  
mov edi,8    EDI = 8

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mov edx,[myDoubles + edi]    EDX = 3
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